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TITLE:	BELGIT
DESCRIPTION:	48GE+8SFP+

DESIGNER:	DESIGNER
REVIEWER:	REVIEWER
APPROVER:	APPROVER

PCB LAYOUT PN: R3059-B0001-02

Title: MOR M0	Designer:
Document Number:	Document Version:
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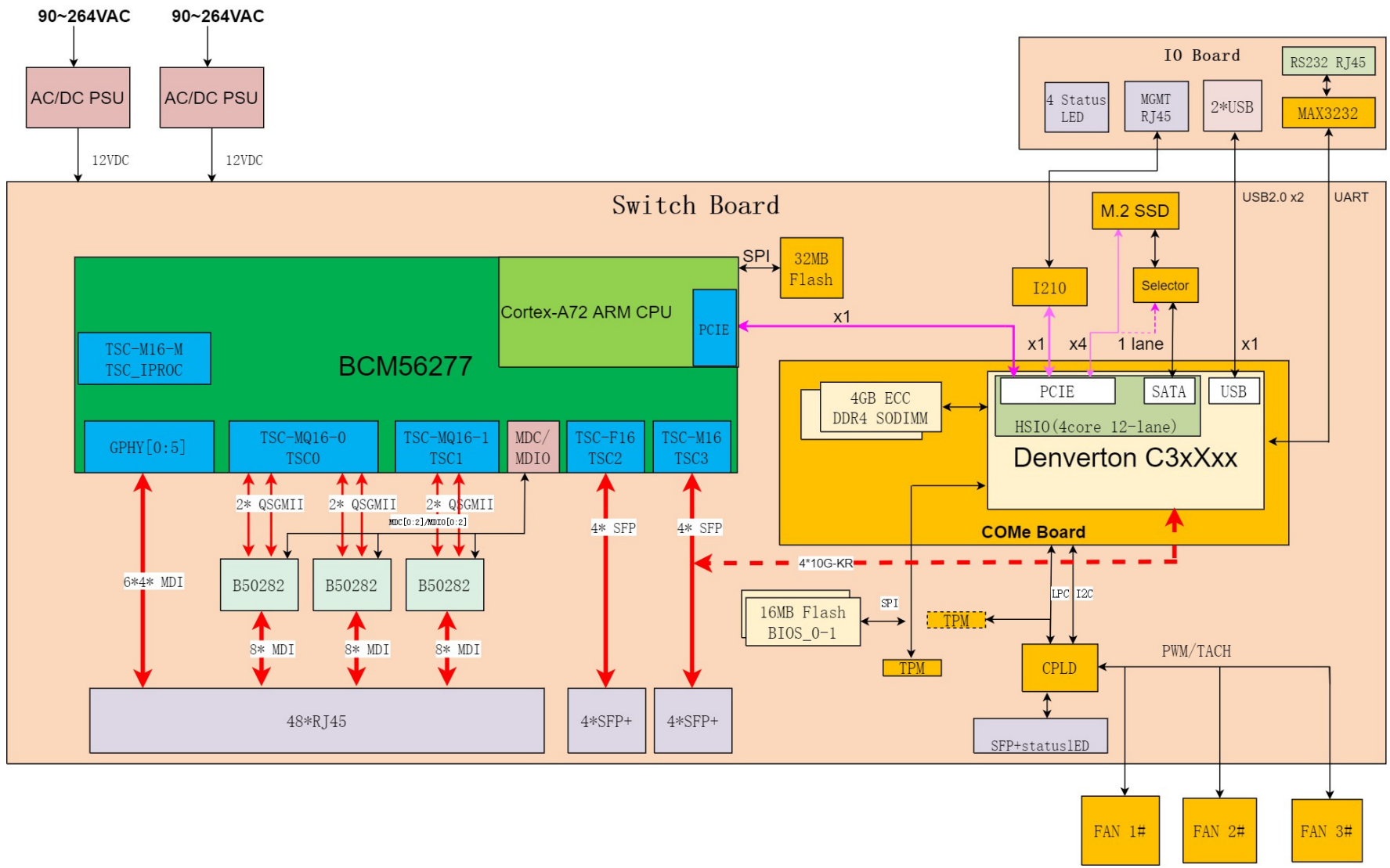
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A3 (297X420)

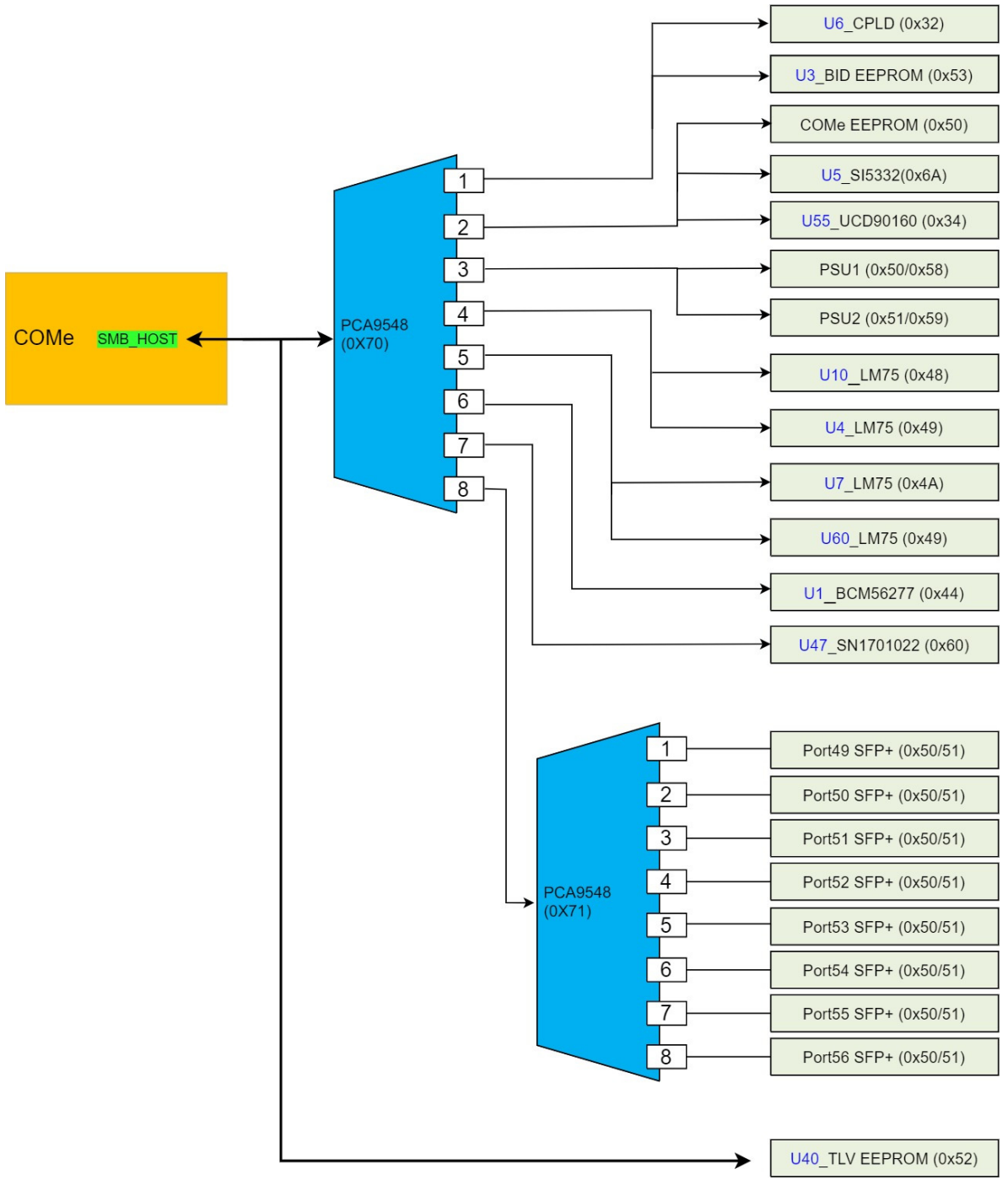
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SYSTEM_DIAGRAM

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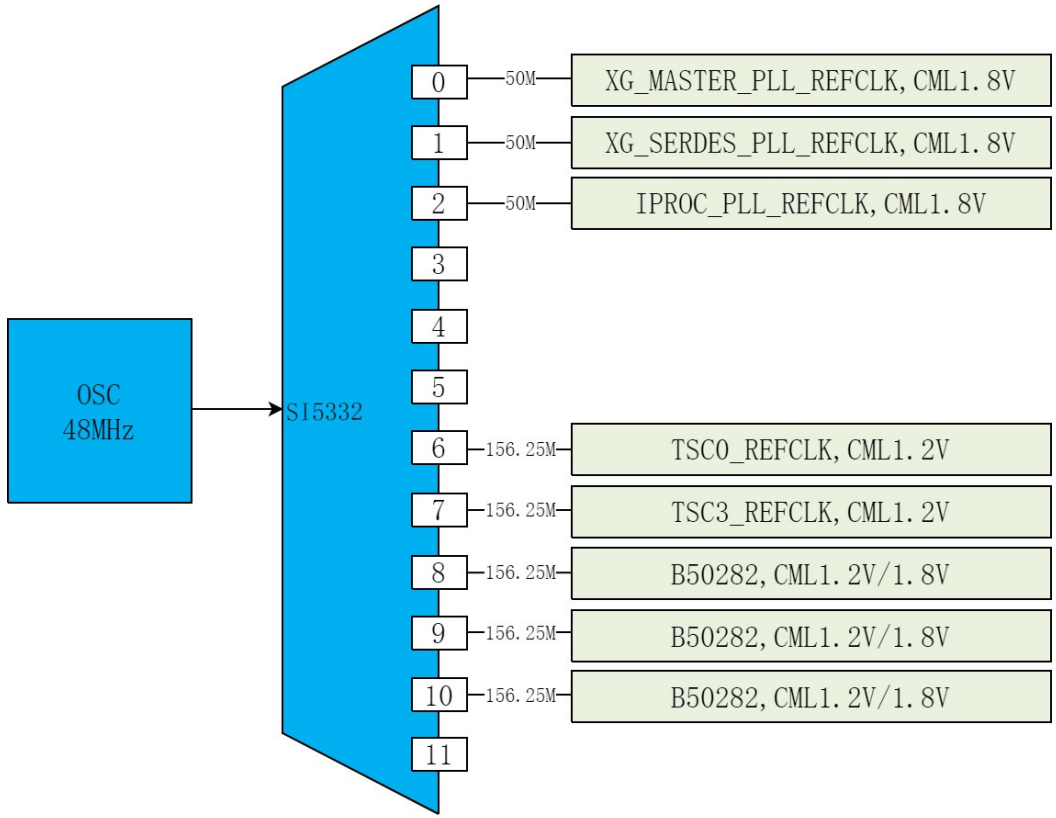
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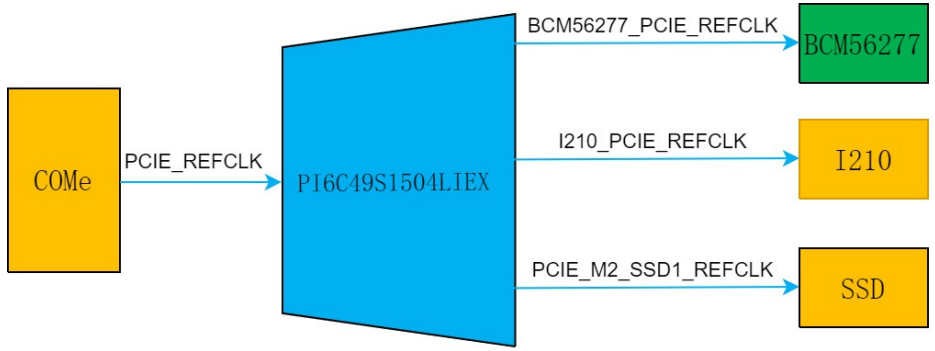


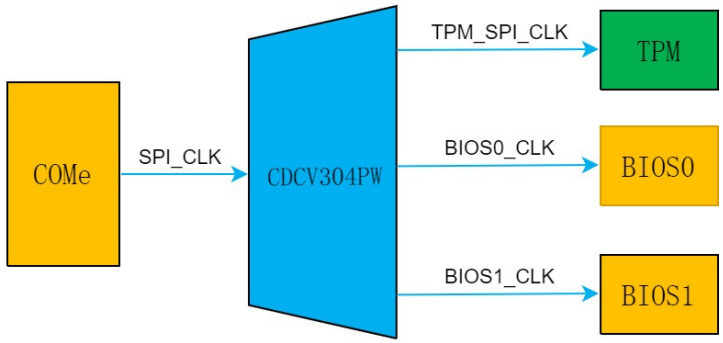
I2C_TOPOLOGY

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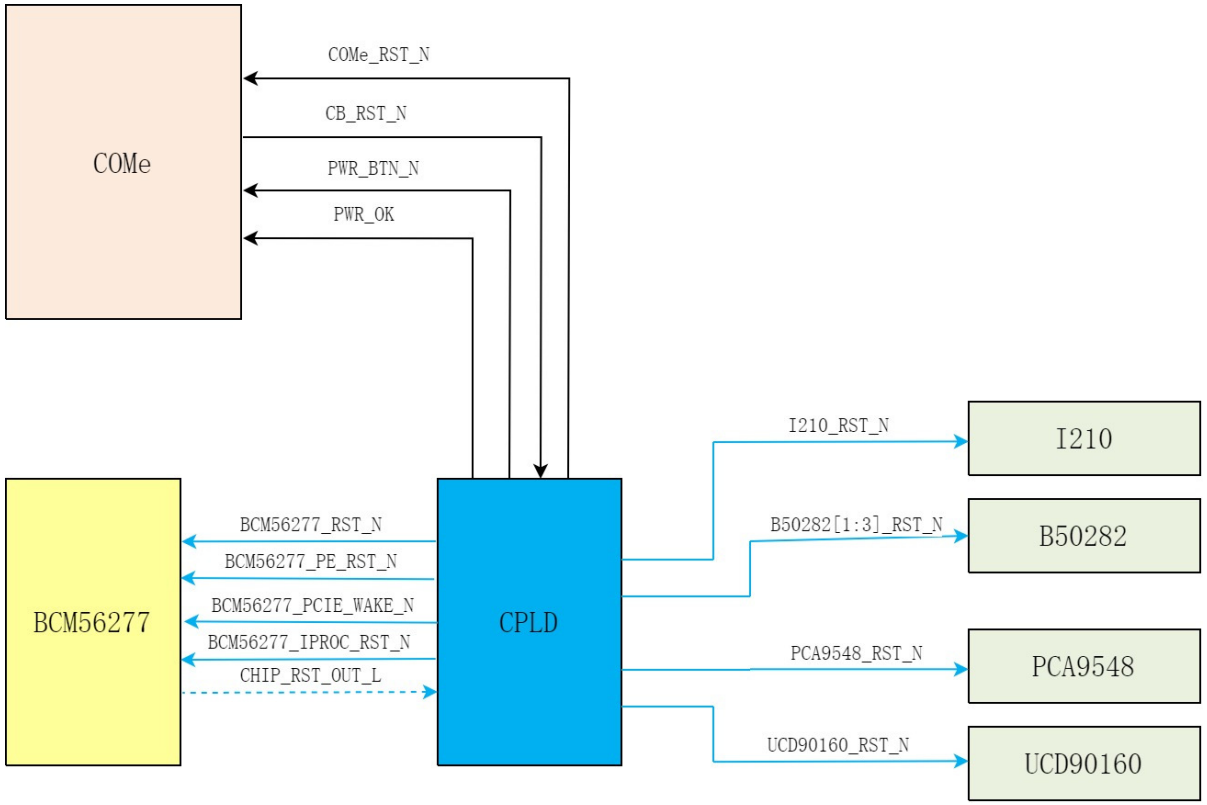




CLOCK_TREE

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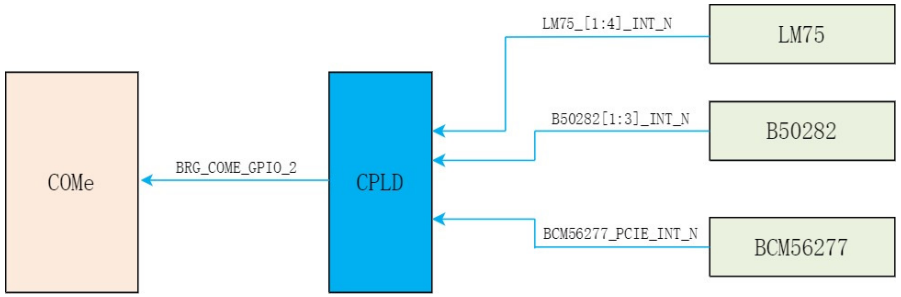
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RESET_TOPOLOGY

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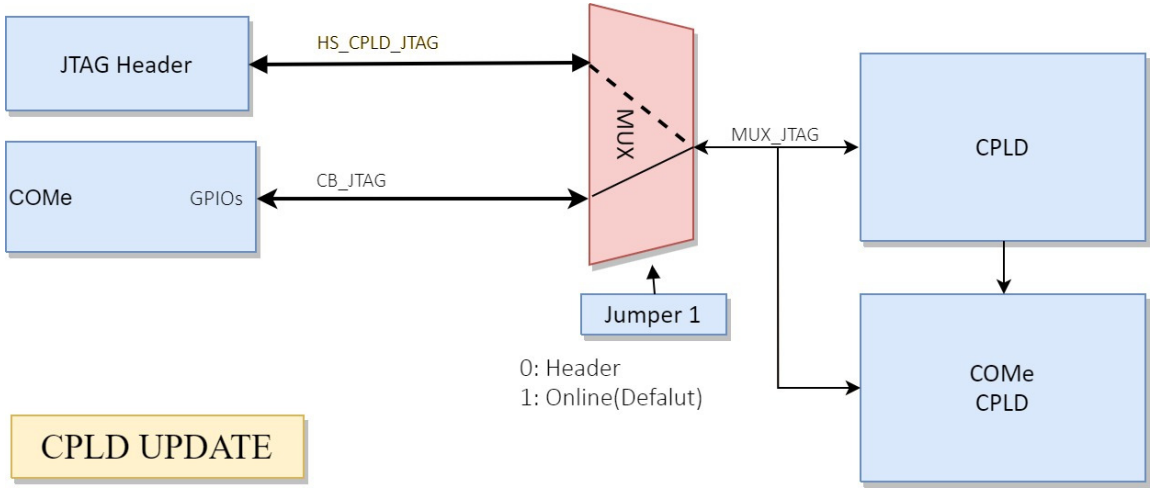
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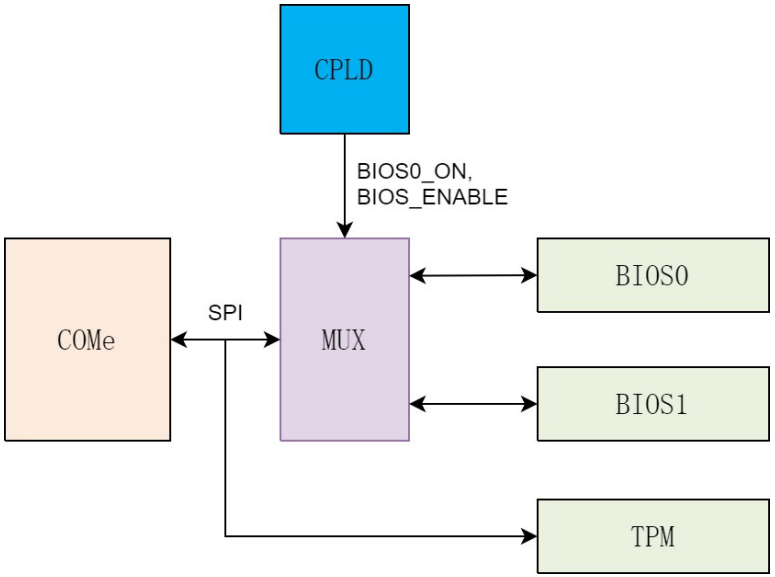
INT_TOPOLOGY

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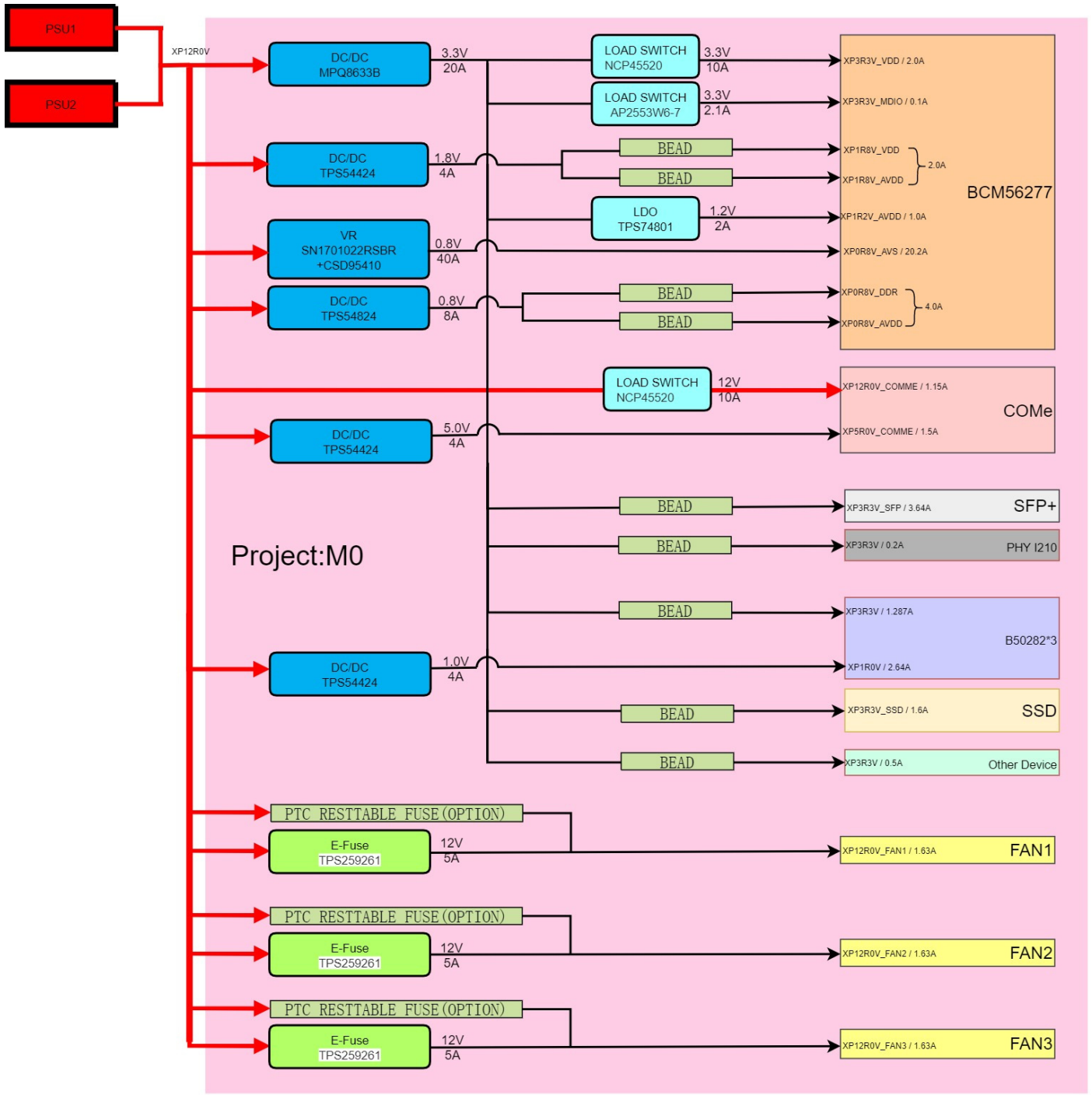
CPLD UPDATE

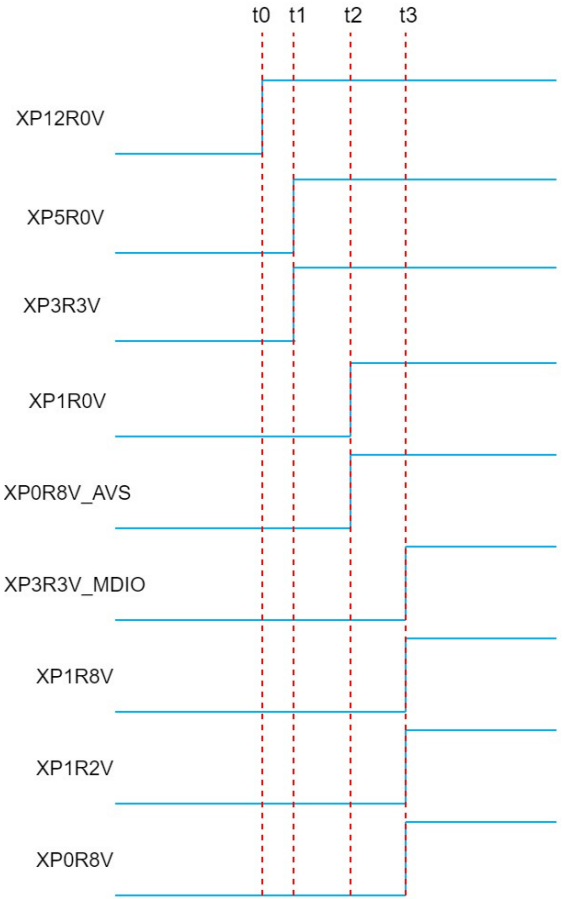


JTAG_BIOS_TOPOLOGY

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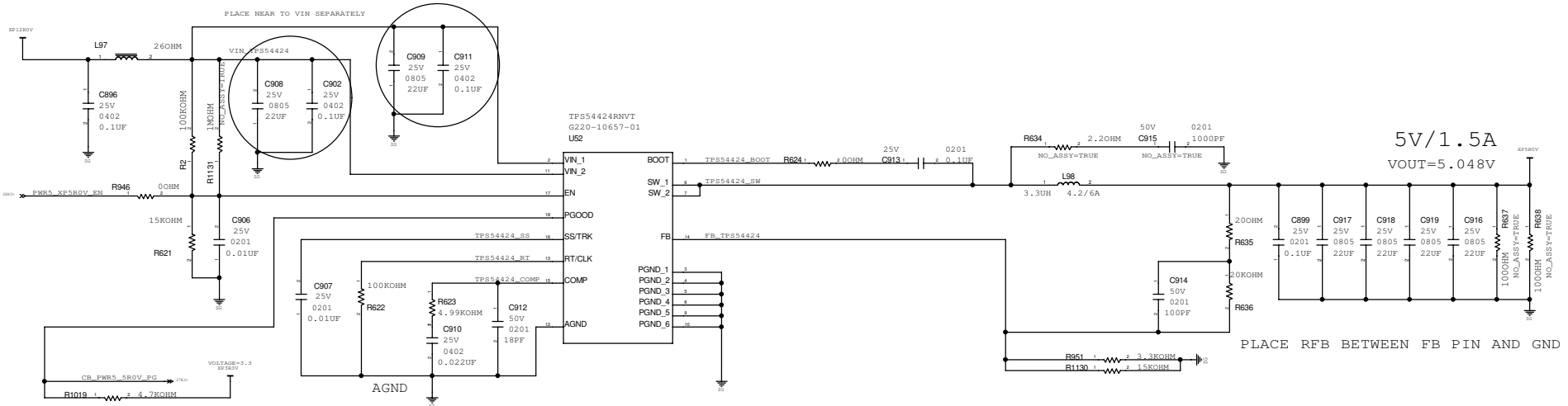




POWER_TOPOLOGY

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5V/1.5A

VOUT=5.048V

PLACE RFB BETWEEN FB PIN AND GND

AGND SINGLE POINT TO GND

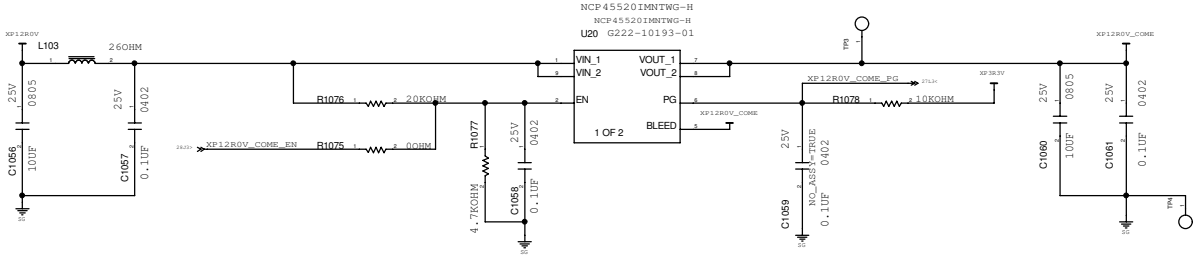
TSS=10NF/8.3=1.2MS

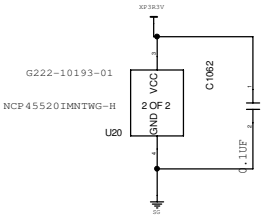
VFB=0.6V RFB=12.012/(VO-0.6)

VO=5.048V	RFB=2.7K OR 3.3K//15K
VO=3.317V	RFB=4.42K
VO=2.537V	RFB=6.2K
VO=1.801V	RFB=10K
VO=1.201V	RFB=20K

CBB_TPS54424

POWER RAIL	CBB_TPS54424
VIN	12V
VOUT	0.8~5V
SS	1.2MS
FSW	500KHZ
IMAX	4A
OCP	108%*VOUT
OVP	108%*VOUT
RIPPLE	+/- 1%



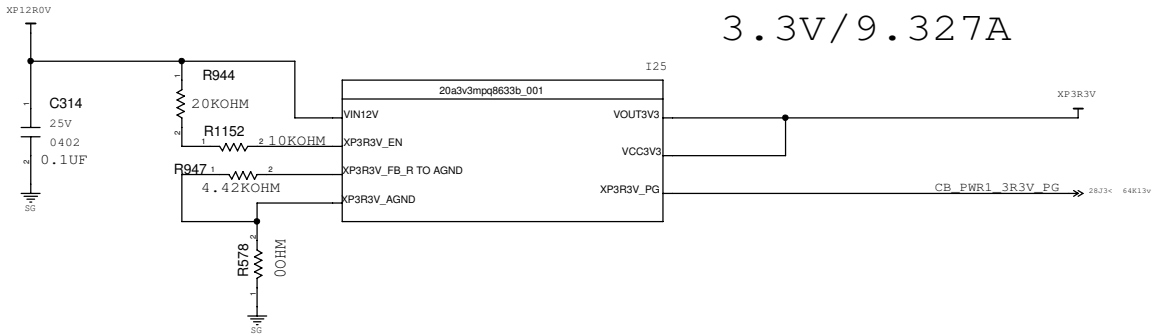


XP5R0V & XP12R0V_COME

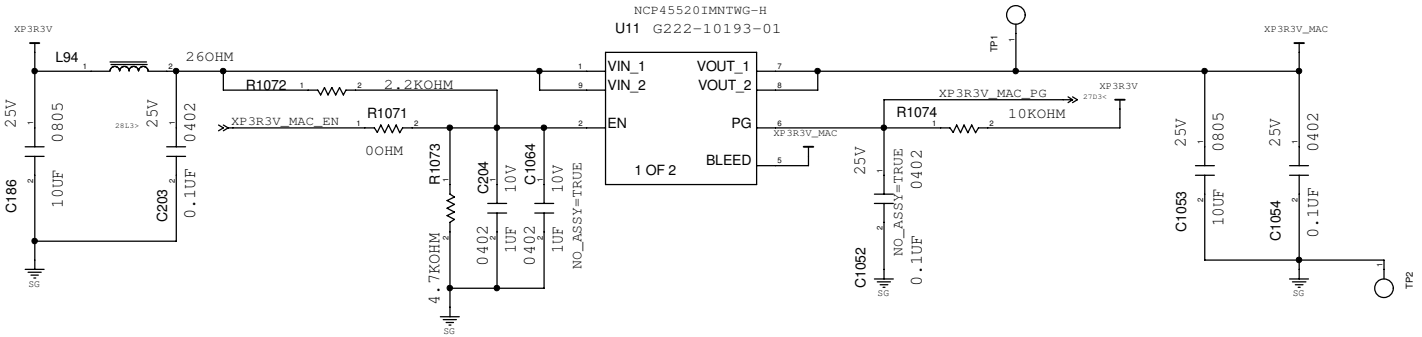
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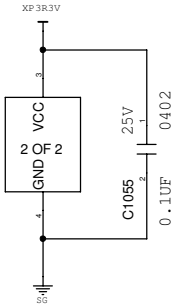
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3.3V/9.327A



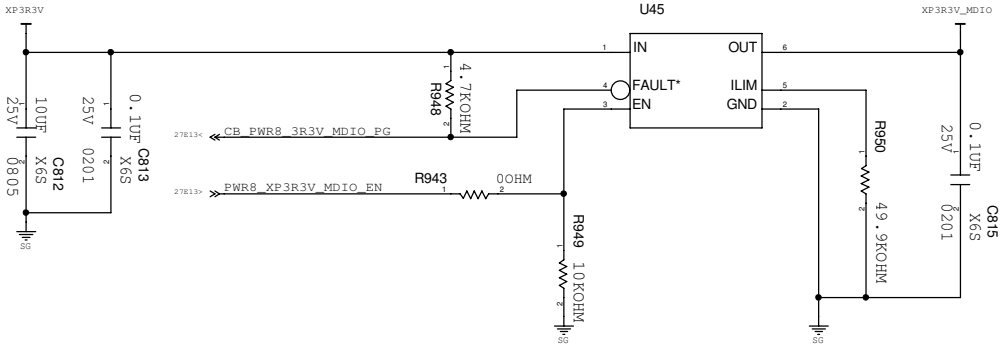
NCP45520IMNTWG-H
NCP45520IMNTWG-H
U11 G222-10193-01





3.3V/0.1A

TPS2553DBVR
G220-10547-01
U45

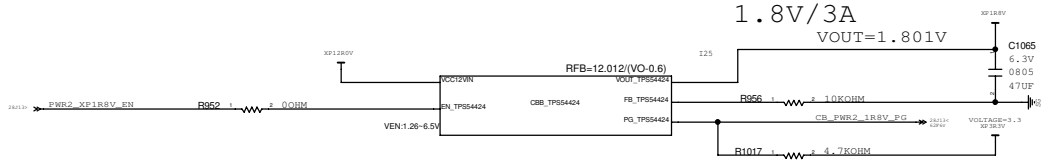


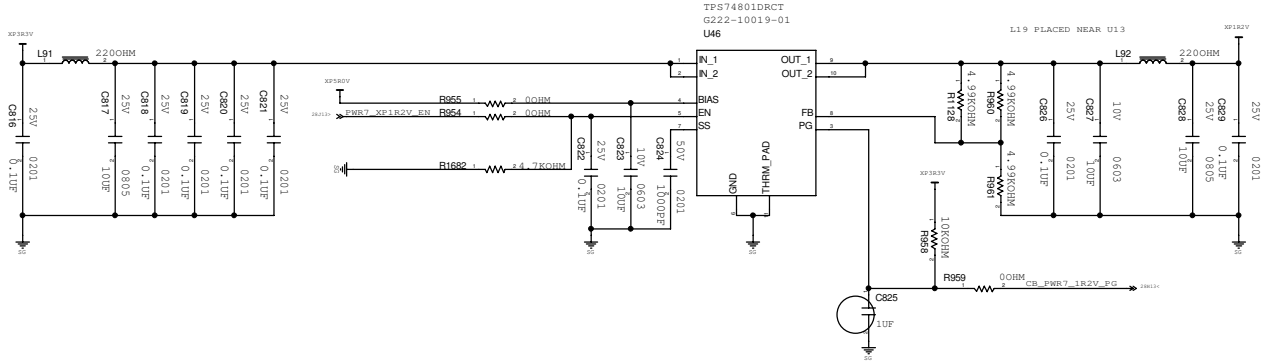
OUTPUT CURRENT LIMIT 0.52A

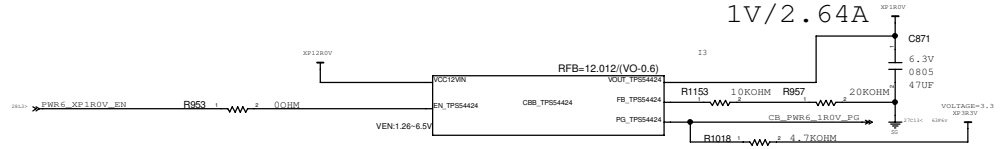
XP3R3V & XP3R3V_MDIO & XP3R3V_MAC

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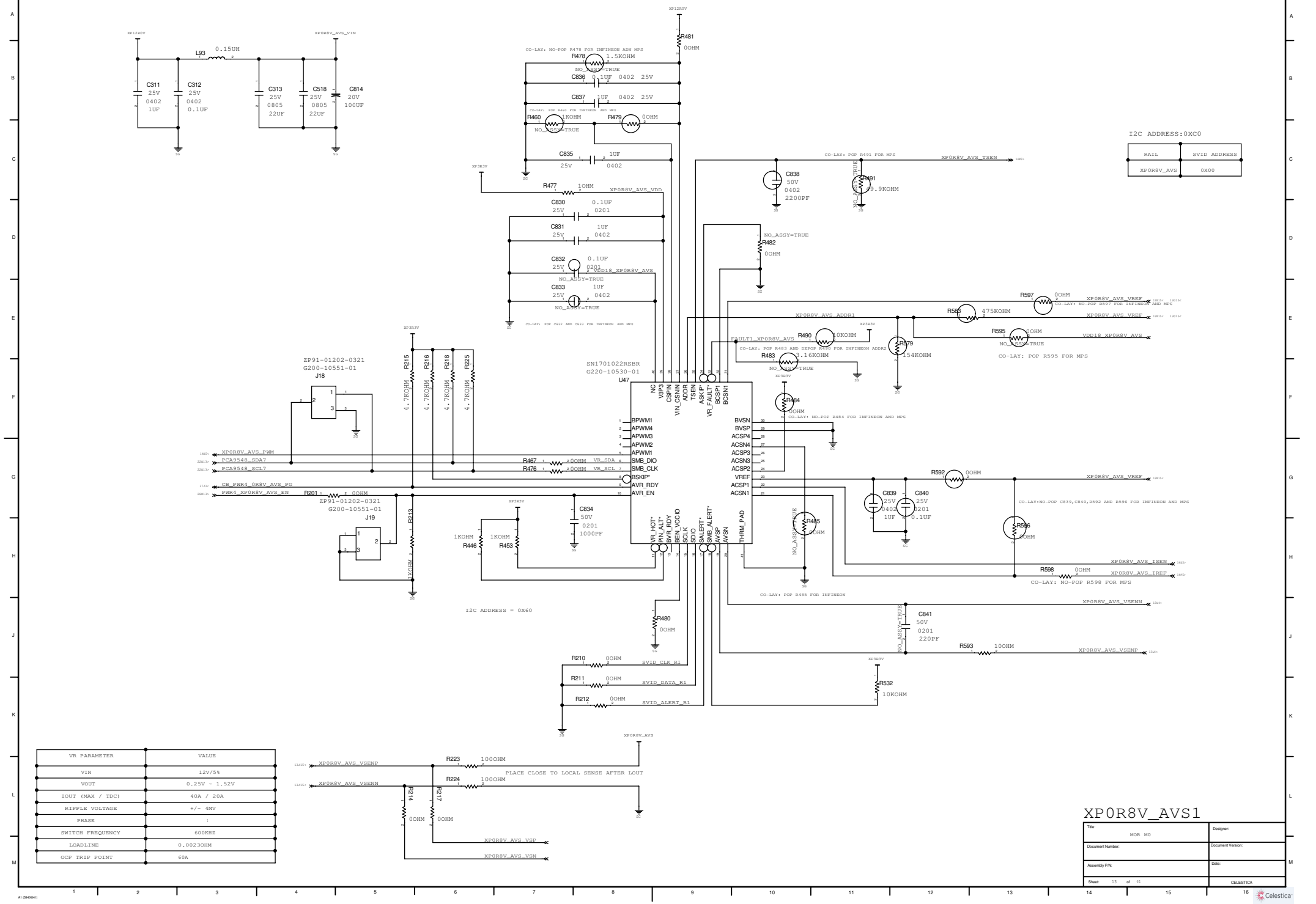




XP1R8V & XP1R2V_AVDD & XP1R0V

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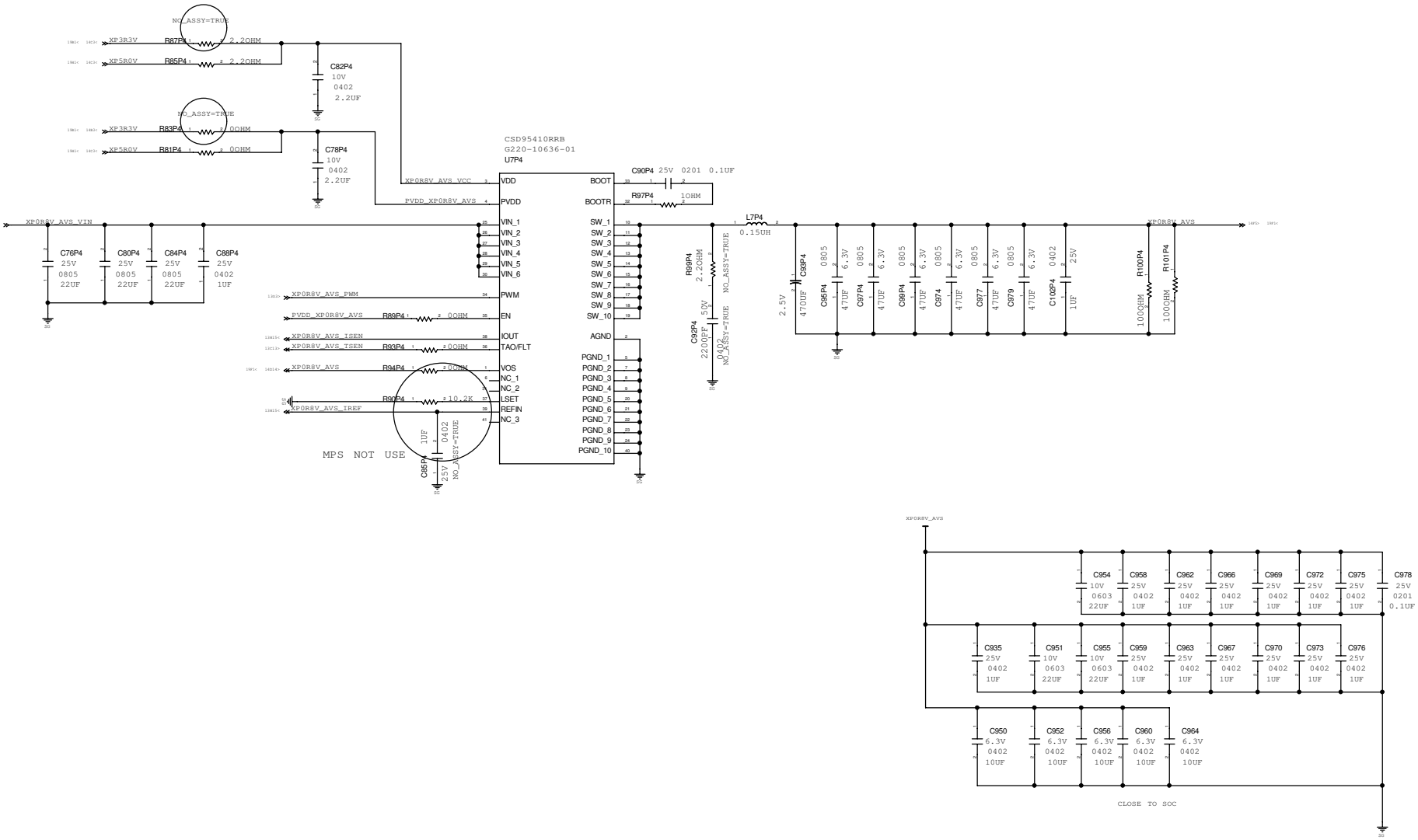
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VR PARAMETER	VALUE
VIN	12V/5%
VOUT	0.25V ~ 1.52V
IOUT (MAX / TDC)	40A / 20A
RIPPLE VOLTAGE	+/- 4MV
PHASE	1
SWITCH FREQUENCY	600KHZ
LOADLINE	0.0023OHM
OCP TRIP POINT	60A

XP0R8V_AVS1

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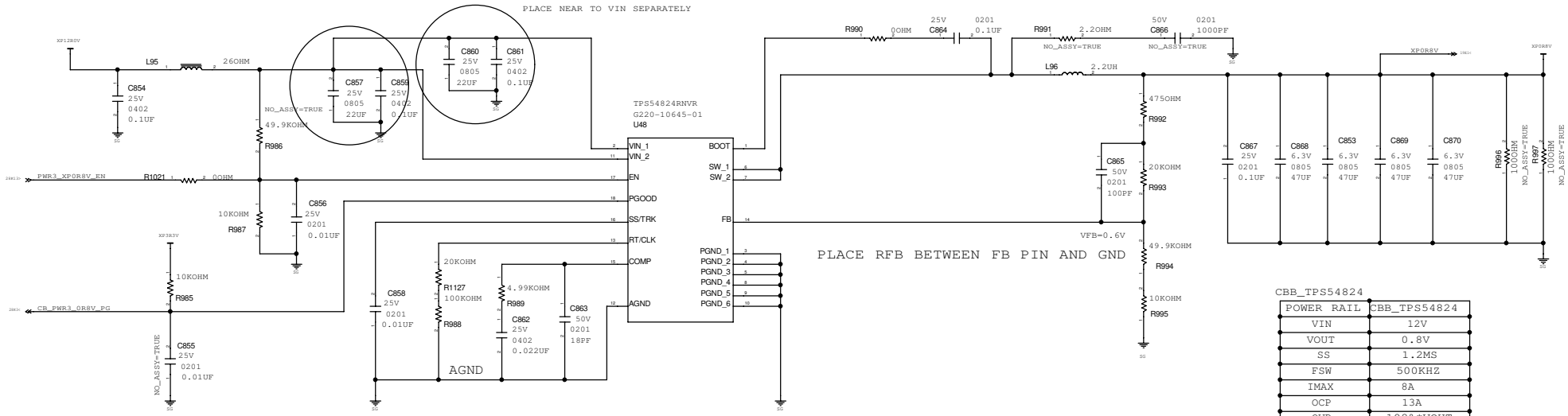


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Celestica, Inc.
Enterprise Server & Storage Development
5000 Long Date Ave
Shanghai P.R.C. 201203

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PLACE RFB BETWEEN FB PIN AND GND

AGND SINGLE PIONT TO GND

CBB_TPS54824

POWER RAIL	CBB_TPS54824
VIN	12V
VOUT	0.8V
SS	1.2MS
FSW	500KHZ
IMAX	8A
OCP	13A
OVP	108%*VOUT
RIPPLE	+/- 1%

XP0R8V

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GBE0_MDI0_P
GBE0_MDI0_N
GBE0_MDI1_P
GBE0_MDI1_N
GBE0_MDI2_P
GBE0_MDI2_N
GBE0_MDI3_P
GBE0_MDI3_N
GBE0_CTREF
GBE0_ACT*
GBE0_LINK*
GBE0_LINK100*
GBE0_LINK1000*
AC/HDA_RST*
AC/HDA_BITCLK
AC/HDA_SYNC
AC/HDA_SDOUT
AC/HDA_SDIN0
AC/HDA_SDIN1
AC/HDA_SDIN2

COME_LPC_SERIRQ
COME_LPC_FRAME_N
COME_LPC_AD0
COME_LPC_AD1
COME_LPC_AD2
COME_LPC_AD3
LPC_DRQ0*
LPC_DRQ1*
COME_LPC_CLK R505 33OHM R_COME_LPC_CLK

LPC_SERIRQ
LPC_FRAME*
LPC_AD0
LPC_AD1
LPC_AD2
LPC_AD3
LPC_CLK

COME_SMB_DAT R506 33OHM
COME_SMB_CK R507 33OHM
COME_SMB_ALERT_N R508 0OHM

SMB_DAT
SMB_CK
SMB_ALERT*

PCA9548_SDA2 R509 0OHM
PCA9548_SCL2 R510 0OHM

I2C_DAT
I2C_CK

COME_SUS_S3_N
COME_SUS_S4_N
COME_SUS_S5_N
COME_SUS_STAT_N

SUS_S3*
SUS_S4*
SUS_S5*
SUS_STAT*

BRG_THRMTRIP_N
BRG_THRM_N

BATLOW*
THRMTRIP*
THRM*
SPKR

CPLD ONLINE UPDATE FOR CB

CB_JTAG_TDO R511 0OHM R_CB_JTAG_TDO
BRG_COME_GPIO_0
BRG_COME_GPIO_1
BRG_COME_GPIO_2
CB_JTAG_TDI R512 0OHM R_CB_JTAG_TDI
CB_JTAG_TMS R513 0OHM R_CB_JTAG_TMS
CB_JTAG_TCK R514 33OHM R_CB_JTAG_TCK
BRG_COME_GPIO_3

GPI0
GPI1
GPI2
GPI3
GPO0
GPO1
GPO2
GPO3

BIOS_SPI INTERFACE

COME_SPI_POWER_3V3 VOLTAGE=3.3

COME_SPI_MISO R502 0OHM
COME_SPI_CLK R503 0OHM
COME_SPI_MOSI R504 0OHM
COME_SPI_CS_N R1188 0OHM

SPI_POWER
SPI_MISO
SPI_CLK
SPI_MOSI
SPI_CS*

VOLTAGE=3.3
COME_SPI_POWER_3V3
R501 4.7KOHM

SATA0_TX_N
SATA0_TX_P
SATA0_RX_P
SATA0_RX_N
SATA1_TX_N
SATA1_TX_P
SATA1_RX_P
SATA1_RX_N
SATA2_TX_P
SATA2_TX_N
SATA2_RX_P
SATA2_RX_N
SATA3_TX_P
SATA3_TX_N
SATA3_RX_P
SATA3_RX_N

CB_SATA_TX0_N
CB_SATA_TX0_P
CB_SATA_RX0_P
CB_SATA_RX0_N

S_ATA_ACT*
COME_S_ATA_ACT_N

USB0_P
USB0_N
USB1_P
USB1_N
USB2_P
USB2_N
USB3_P
USB3_N
USB4_P
USB4_N
USB5_P
USB5_N
USB6_P
USB6_N
USB7_P
USB7_N

COME_USB1_DP
COME_USB1_DM
COME_USB2_DP
COME_USB2_DM

USB_0_1_OC*
USB_2_3_OC*
USB_4_5_OC*
USB_6_7_OC*
R523 0OHM USB1_FAULT_N
R524 0OHM USB2_FAULT_N

TPM_PP

PWRBTN*
R525 0OHM COME_PWR_BTN_N

PWR_OK
COME_PWROK

CB_RESET*
CB_RESET_N

SYS_RESET*
SYS_RESET_N

WDT
COME_BRG_WDT

TYPE10*
BIOS_DIS0*
BIOS_DIS1*
BRG_COME_BIOS_DIS0_N
BRG_COME_BIOS_DIS1_N

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LVDS_A0_P
LVDS_A0_N
LVDS_A1_P
LVDS_A1_N
LVDS_A2_P
LVDS_A2_N
LVDS_A3_P
LVDS_A3_N
LVDS_A_CK_P
LVDS_A_CK_N
LVDS_B0_P
LVDS_B0_N
LVDS_B1_P
LVDS_B1_N
LVDS_B2_P
LVDS_B2_N
LVDS_B3_P
LVDS_B3_N
LVDS_B_CK_P
LVDS_B_CK_N
LVDS_VDD_EN
LVDS_BKLT_EN
LVDS_BKLT_CTRL
LVDS_I2C_DAT
LVDS_I2C_CK

VGA_RED
VGA_GRN
VGA_BLU
VGA_HSYNC
VGA_VSYNC
VGA_I2C_DAT
VGA_I2C_CK

SER0_TX
SER0_RX
SER1_TX
SER1_RX

FAN_PWMOUT
FAN_TACHIN

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PCIE_TX0_P
PCIE_TX0_N
PCIE_RX0_P
PCIE_RX0_N
PCIE_TX1_P
PCIE_TX1_N
PCIE_RX1_P
PCIE_RX1_N
PCIE_TX2_P
PCIE_TX2_N
PCIE_RX2_P
PCIE_RX2_N
PCIE_TX3_P
PCIE_TX3_N
PCIE_RX3_P
PCIE_RX3_N
PCIE_TX4_P
PCIE_TX4_N
PCIE_RX4_P
PCIE_RX4_N
PCIE_TX5_P
PCIE_TX5_N
PCIE_RX5_P
PCIE_RX5_N
PCIE_CLK_REF_P
PCIE_CLK_REF_N

BCM56277_PCIE_RX_P
BCM56277_PCIE_RX_N
BCM56277_PCIE_TX_P
BCM56277_PCIE_TX_N
I210_PCIE_RX_P
I210_PCIE_RX_N
I210_PCIE_TX_P
I210_PCIE_TX_N
COME_PCIE_REFCLK_P
COME_PCIE_REFCLK_N

EXCD0_CPPE*
EXCD0_PERST*
EXCD1_CPPE*
EXCD1_PERST*
R132 0OHM SPI_TPM_CS_N

WAKE0*
WAKE1*
COME_WAKE_N
LID*
LID_N
SLEEP*
SLEEP_N

VOLTAGE=3.3
XP3R3V
R180 4.7KOHM NO_ASSY=TRUE
R187 4.7KOHM NO_ASSY=TRUE
R533 4.7KOHM
R534 4.7KOHM
R535 4.7KOHM NO_ASSY=TRUE
R536 4.7KOHM NO_ASSY=TRUE

COME_UART0_TX
COME_UART0_RX
COME_UART1_TX
COME_UART1_RX

R537 4.7KOHM COME_WAKE_N
R538 4.7KOHM SYS_RESET_N
R539 10KOHM CB_RESET_N
R540 10KOHM COME_PWR_BTN_N
R543 4.7KOHM COME_SMB_CK NO_ASSY=TRUE
R544 4.7KOHM COME_SMB_DAT NO_ASSY=TRUE
R545 4.7KOHM COME_SMB_ALERT_N
R546 4.7KOHM PCA9548_SCL2
R547 4.7KOHM PCA9548_SDA2
R548 4.7KOHM COME_SUS_S3_N
R549 4.7KOHM COME_SUS_S4_N
R550 4.7KOHM COME_SUS_S5_N
R551 4.7KOHM BRG_THRMTRIP_N
R552 4.7KOHM BRG_THRM_N

R553 9.1KOHM BRG_COME_BIOS_DIS0_N
R554 9.1KOHM BRG_COME_BIOS_DIS1_N

COM-E 1/3

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SW_XE1_TX_P
SW_XE1_TX_N
SW_XE0_TX_P
SW_XE0_TX_N
SW_XE1_RX_P
SW_XE0_RX_P
SW_XE0_RX_N
SW_XE3_TX_P
SW_XE3_TX_N
SW_XE2_TX_P
SW_XE2_TX_N
SW_XE3_RX_P
SW_XE3_RX_N
SW_XE2_RX_P
SW_XE2_RX_N

DDI1_PAIR0_P
DDI1_PAIR0_N
DDI1_PAIR1_P
DDI1_PAIR1_N
DDI1_PAIR2_P
DDI1_PAIR2_N
DDI1_PAIR3_P
DDI1_PAIR3_N
DDI1_PAIR4_P
DDI1_PAIR4_N
DDI1_PAIR5_P
DDI1_PAIR5_N
DDI1_PAIR6_P
DDI1_PAIR6_N
DDI1_CTRLDATA_AUX_N
DDI1_CTRLCLK_AUX_P
DDI1_DDC_AUX_SEL
DDI1_HPD
DDI2_PAIR0_P
DDI2_PAIR0_N
DDI2_PAIR1_P
DDI2_PAIR1_N
DDI2_PAIR2_P
DDI2_PAIR2_N
DDI2_PAIR3_P
DDI2_PAIR3_N
DDI2_CTRLDATA_AUX_N
DDI2_CTRLCLK_AUX_P
DDI2_DDC_AUX_SEL
DDI2_HPD
DDI3_PAIR0_P
DDI3_PAIR0_N
DDI3_PAIR1_P
DDI3_PAIR1_N
DDI3_PAIR2_P
DDI3_PAIR2_N
DDI3_PAIR3_P
DDI3_PAIR3_N
DDI3_CTRLDATA_AUX_N
DDI3_CTRLCLK_AUX_P
DDI3_DDC_AUX_SEL
DDI3_HPD

USB_SSTX0_P
USB_SSTX0_N
USB_SSRX0_P
USB_SSRX0_N
USB_SSTX1_P
USB_SSTX1_N
USB_SSRX1_P
USB_SSRX1_N
USB_SSTX2_P
USB_SSTX2_N
USB_SSRX2_P
USB_SSRX2_N
USB_SSTX3_P
USB_SSTX3_N
USB_SSRX3_P
USB_SSRX3_N

PCIE_TX6_P
PCIE_TX6_N
PCIE_RX6_P
PCIE_RX6_N
PCIE_TX7_P
PCIE_TX7_N
PCIE_RX7_P
PCIE_RX7_N

3 OF 6

VOLTAGE=3.3
XP3R3V

4.7KOHM R555
4.7KOHM R556
4.7KOHM R557

TYPE0*
TYPE1*
TYPE2*
TYPE_0
TYPE_1
TYPE_2
R558 0OHM COME_TYPE0
R559 0OHM COME_TYPE1
R560 0OHM COME_TYPE2

3-5353652-6 3-5353652-6
G200-11673-01
J11

PEG_TX0_P
PEG_TX0_N
PEG_RX0_P
PEG_RX0_N
PEG_TX1_P
PEG_TX1_N
PEG_RX1_P
PEG_RX1_N
PEG_TX2_P
PEG_TX2_N
PEG_RX2_P
PEG_RX2_N
PEG_TX3_P
PEG_TX3_N
PEG_RX3_P
PEG_RX3_N
PEG_TX4_P
PEG_TX4_N
PEG_RX4_P
PEG_RX4_N
PEG_TX5_P
PEG_TX5_N
PEG_RX5_P
PEG_RX5_N
PEG_TX6_P
PEG_TX6_N
PEG_RX6_P
PEG_RX6_N
PEG_TX7_P
PEG_TX7_N
PEG_RX7_P
PEG_RX7_N
PEG_LANE_RV*

PEG_TX8_P
PEG_TX8_N
PEG_RX8_P
PEG_RX8_N
PEG_TX9_P
PEG_TX9_N
PEG_RX9_P
PEG_RX9_N
PEG_TX10_P
PEG_TX10_N
PEG_RX10_P
PEG_RX10_N
PEG_TX11_P
PEG_TX11_N
PEG_RX11_P
PEG_RX11_N
PEG_TX12_P
PEG_TX12_N
PEG_RX12_P
PEG_RX12_N
PEG_TX13_P
PEG_TX13_N
PEG_RX13_P
PEG_RX13_N
PEG_TX14_P
PEG_TX14_N
PEG_RX14_P
PEG_RX14_N
PEG_TX15_P
PEG_TX15_N
PEG_RX15_P
PEG_RX15_N

CPUPCIE_TD_SSD1_RX0_P
CPUPCIE_TD_SSD1_RX0_N
CPUPCIE_RX_SSD1_TD0_P
CPUPCIE_RX_SSD1_TD0_N
CPUPCIE_TD_SSD1_RX1_P
CPUPCIE_TD_SSD1_RX1_N
CPUPCIE_RX_SSD1_TD1_P
CPUPCIE_RX_SSD1_TD1_N
CPUPCIE_TD_SSD1_RX2_P
CPUPCIE_TD_SSD1_RX2_N
CPUPCIE_RX_SSD1_TD2_P
CPUPCIE_RX_SSD1_TD2_N
CPUPCIE_TD_SSD1_RX3_P
CPUPCIE_TD_SSD1_RX3_N
CPUPCIE_RX_SSD1_TD3_P
CPUPCIE_RX_SSD1_TD3_N

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G200-11673-01
J11

GND_FIXED_1
GND_FIXED_2
GND_FIXED_3
GND_FIXED_4
GND_FIXED_5
GND_FIXED_6
GND_1
GND_FIXED_7
GND_2
GND_FIXED_8
GND_FIXED_9
GND_FIXED_10
GND_FIXED_11
GND_FIXED_12
GND_FIXED_13
GND_FIXED_14
GND_FIXED_15
GND_FIXED_16
GND_FIXED_17
GND_FIXED_18
GND_FIXED_19
GND_FIXED_20
GND_FIXED_21
GND_FIXED_22
GND_FIXED_23
GND_FIXED_24

VCC_RTC
VCC_5V_SBY_1
VCC_5V_SBY_2
VCC_5V_SBY_3
VCC_5V_SBY_4
VCC_12V_1
VCC_12V_2
VCC_12V_3
VCC_12V_4
VCC_12V_5
VCC_12V_6
VCC_12V_7
VCC_12V_8
VCC_12V_9
VCC_12V_10
VCC_12V_11
VCC_12V_12
RSVD_1
RSVD_2
RSVD_3
EDP_HPD

P3V_BAT
VOLTAGE=5V
XP5R0V
C519 0.1UF 25V X6S 0201
C520 0.1UF 25V X6S 0201
C521 0.1UF 25V X6S 0201
C522 0.1UF 25V X6S 0201
XP12R0V_COME
COME_CPLD_TMS

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P3V_BAT
IRLML6302TRPBF
G121-10009-01
Q2
100OHM R561
NTA4001NT1G
G121-10093-01
Q1
FROM CPLD
RTC_CLEAR
R563 100OHM
1KOHM R562
BAT54C-7
G122-00003-01
CR1
VOLTAGE=3.3
XP3R3V
R564 100OHM
R565 1KOHM
C523 22UF 10V X5R 0603
C524 1UF 25V X6S 0402
C525 0.1UF 25V X6S 0201
NO_ASSY=TRUE
SMTU2032-LF
G210-10034-01
X2
Battery Holder
SMTU2430-LF
G210-10049-01
X1
Battery Holder

RTC

COM-E 2/3

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XP12R0V_COME

C1	GND_FIXED_25	VCC_12V_13	C104		
C2	GND_3	VCC_12V_14	C105		
C5	GND_4	VCC_12V_15	C106		
C8	GND_5	VCC_12V_16	C107		
C11	GND_FIXED_26	VCC_12V_17	C108		
C14	GND_6	VCC_12V_18	C109		
C21	GND_FIXED_27	VCC_12V_19	D104		
C31	GND_FIXED_28	VCC_12V_20	D105		
C41	GND_FIXED_29	VCC_12V_21	D106		
C51	GND_FIXED_30	VCC_12V_22	D107		
C60	GND_FIXED_31	VCC_12V_23	D108		
C70	GND_FIXED_32	VCC_12V_24	D109		
C73	GND_7				
C76	GND_8				
C80	GND_FIXED_33				
C84	GND_9				
C87	GND_10				
C90	GND_FIXED_34				
C93	GND_11				
C96	GND_12				
C100	GND_FIXED_35	RSVD_4	C17		
C103	GND_13	RSVD_5	C18		
C110	GND_FIXED_36	RSVD_6	G27	SW_XE1_RX_N	14J7>
D1	GND_FIXED_37	RSVD_7	G28		
D103	GND_25	RSVD_8	C35		
D2	GND_14	RSVD_9	C45		
D5	GND_15	RSVD_10	C48		
D11	GND_FIXED_38	RSVD_11	Q63	COME_CPLD_TDI	17D3>
D8	GND_16	RSVD_12	Q64	COME_PWR1_3R3V	18C9<
D21	GND_FIXED_39	RSVD_13	Q67	COME_CPLD_TCK	30E11>
D31	GND_FIXED_40	RSVD_14	C77	COME_PWR2_1R0V	18B9<
D41	GND_FIXED_41	RSVD_15	Q83	COME_PWR3_1R05V	18C12<
D51	GND_FIXED_42	RSVD_16	Q97	COME_PWR4_1R8V	18D12<
D60	GND_FIXED_43	RSVD_17	D17		
D14	GND_17	RSVD_18	D18		
D70	GND_FIXED_44	RSVD_19	D24		
D67	GND_18	RSVD_20	D25		
D73	GND_19	RSVD_21	D26		
D80	GND_FIXED_45	RSVD_22	D35		
D76	GND_20	RSVD_23	D38		
D84	GND_21	RSVD_24	D45		
D90	GND_FIXED_46	RSVD_25	D48		
D87	GND_22	RSVD_26	D63	R1191 1 2 0OHM	COME_CPLD_TDO 30E11<
D99	GND_23	RSVD_27	D64		COME_PWR5_1R2V 18F9<
D100	GND_FIXED_47	RSVD_28	D77		COME_PWR6_1R0V 18E9<
D96	GND_24	RSVD_29	D83		COME_PWR7_1R24V 18F12<
D110	GND_FIXED_48	RSVD_30	D97		COME_PWR8_2R5V 18E12<
G1	GND_SH1				
G2	GND_SH2				
G3	GND_SH3				
G4	GND_SH4				

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VOLTAGE=0

XP12R0V_COME

C526 0.1UF 25V X7R 0402
C527 0.1UF 25V X7R 0402
C528 0.1UF 25V X7R 0402
C529 0.1UF 25V X7R 0402
C530 0.1UF 25V X7R 0402
C531 0.1UF 25V X7R 0402
C532 0.1UF 25V X7R 0402
C533 0.1UF 25V X7R 0402
C534 0.1UF 25V X7R 0402
C535 0.1UF 25V X7R 0402
C536 0.1UF 25V X7R 0402
C537 0.1UF 25V X7R 0402

VOLTAGE=0

EACH 2 POWER PINS SHARE ONE CAP

NO_ASSY=TRUE
COME_CPLD_TDI — R1171 1 2 0OHM — COME_CPLD_TDO

COME_PWR1_3R3V — R572 1KOHM — COME_PWR1_3R3V_MON 18C<
R576 1KOHM — VOLTAGE=0

COME_PWR2_1R0V — R573 1KOHM — COME_PWR2_1R0V_MON 18B<
R577 1KOHM NO_ASSY=TRUE — VOLTAGE=0

COME_PWR5_1R2V — R574 1KOHM — COME_PWR5_1R2V_MON 18F<
R288 1KOHM NO_ASSY=TRUE — VOLTAGE=0

COME_PWR6_1R0V — R575 1KOHM — COME_PWR6_1R0V_MON 18E<
R289 1KOHM NO_ASSY=TRUE — VOLTAGE=0

COME_PWR3_1R05V — R587 1KOHM — COME_PWR3_1R05V_MON 18D<
R591 1KOHM NO_ASSY=TRUE — VOLTAGE=0

COME_PWR4_1R8V — R588 1KOHM — COME_PWR4_1R8V_MON 18D<
R291 1KOHM NO_ASSY=TRUE — VOLTAGE=0

COME_PWR7_1R24V — R589 1KOHM — COME_PWR7_1R24V_MON 18F<
R290 1KOHM NO_ASSY=TRUE — VOLTAGE=0

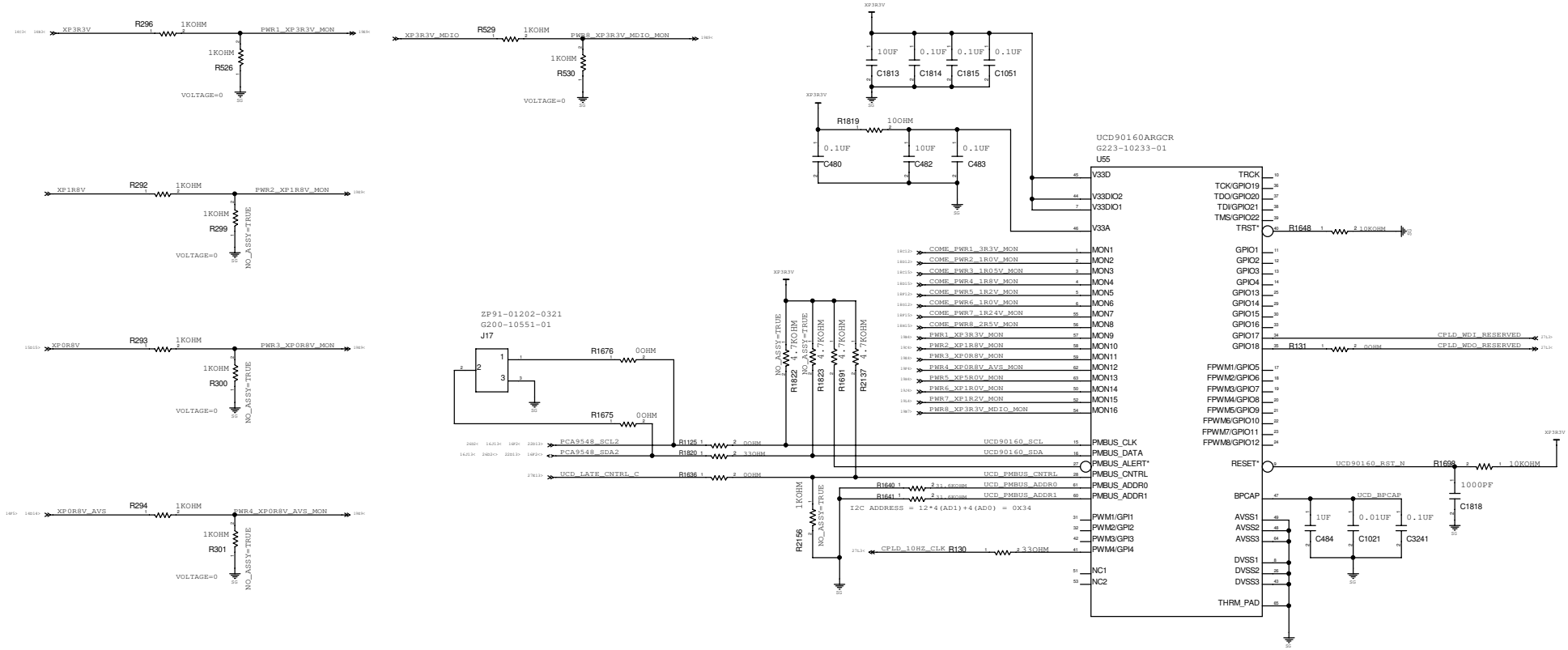
COME_PWR8_2R5V — R590 1KOHM — COME_PWR8_2R5V_MON 18E<
R594 1KOHM — VOLTAGE=0

COM-E 3/3

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POWER MANAGEMENT



POWER_MANAGEMENT

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TPM

NPCT750AADYX
G229-10138-01
U22

NPCT650ABBWX
G229-10088-01
U18

BIOS FOR COM-E

BIOS0 SPI FLASH

ACA-SPI-004-K02
G210-10028-01
J13

G221-10373-01
JU13

TS3A5018PWR
G220-10324-01
U21

BIOS1 SPI FLASH

W25Q128JVSIQ-T
G221-10373-01
U19

CDCV304PW
G220-10291-01
U57

TPM & BIOS FLASH

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I210_PCIE_REFCLK_P R845 100OHM I210_PCIE_REFCLK_N

WGI210IS
WGI210AT S LJXR
G227-10031-01
U23

I210_PCIE_RX_P
I210_PCIE_RX_N
I210_PCIE_TX_P C583 0.1UF 0201 X6S 25V
I210_PCIE_TX_N C584 0.1UF 0201 X6S 25V
I210_PCIE_REFCLK_P
I210_PCIE_REFCLK_N
R663 4.7KOHM NO_ASSY=TRUE I210_RST_N
R570 4.7KOHM I210_WAKE_N
R662 10KOHM
R670 4.7KOHM
MGMT_LED_YELLOW R433 33OHM
MGMT_LINK R434 33OHM
MGMT_LED_GREEN R435 33OHM
R671 4.7KOHM
R672 4.7KOHM
R673 4.7KOHM
R677 4.99KOHM
R1172 0OHM
R1173 0OHM
R1079 4.7KOHM
R1080 4.7KOHM
R1081 4.7KOHM
R1082 4.7KOHM
R674 4.7KOHM
R675 4.7KOHM
R676 4.7KOHM
I210_1_CBOT C585 39NF I210_1_CTOP
16V X7R 0402
R669 4.7KOHM NCSI_ARB_IN
NO_ASSY=TRUE

PE_RP
PE_RN
PE_TP
PE_TN
PECLKP
PECLKN
PE_RST*
PE_WAKE*
DEV_OFF*
LED0
LED1
LED2
SMB_CLK
SMB_DAT
SMB_ALERT*
RSET
XTAL2
XTAL1
NC_SI_CLK_IN
NC_SI_CRS_DV
NC_SI_RXD0
NC_SI_RXD1
NC_SI_TX_EN
NC_SI_TXD0
NC_SI_TXD1
JTAG_TDI
CTOP
NC_SI_ARB_IN
1 OF 2

MDI_PLUS_0/NC
MDI_MINUS_0/SFP_I2C_DATA
MDI_PLUS_1/SFP_I2C_CLK
MDI_MINUS_1/SRDS_SIG_DET
MDI_PLUS_2/SET_P
MDI_MINUS_2/SET_N
MDI_PLUS_3/SER_P
MDI_MINUS_3/SER_N
SDP2
JTAG_TDO
JTAG_TMS
CBOT
SDP3
NC
NC_SI_ARB_OUT
SDP1_PCIE_DIS
SDP0
LAN_PWR_GOOD
JTAG_CLK
NVM_SI
NVM_SO
NVM_SK
NVM_CS*
MGMT_TRD_0_P
MGMT_TRD_0_N
MGMT_TRD_1_P
MGMT_TRD_1_N
MGMT_TRD_2_P
MGMT_TRD_2_N
MGMT_TRD_3_P
MGMT_TRD_3_N
I210_1_JTAG_TDO
I210_1_JTAG_TMS
I210_1_CBOT
R846 4.7KOHM NO_ASSY=TRUE
R847 4.7KOHM NO_ASSY=TRUE
R848 4.7KOHM NO_ASSY=TRUE
R680 4.7KOHM
I210_1_JTAG_TCK
R681 33OHM I210_1_NVM_SI
I210_1_NVM_SO
R682 33OHM I210_1_NVM_SK
R683 33OHM I210_1_NVM_CS_L

25.000MHZ
G131-10014-01
Y3
XTAL 25MHZ
C581 27PF C0G 50V 0201
C582 C0G 50V 27PF 0201

VOLTAGE=3.3
C587 10UF 25V X6S 0805
C593 10UF 25V X6S 0805
C594 0.1UF 25V X6S 0201
C595 0.1UF 25V X6S 0201
C596 0.1UF 25V X6S 0201
C601 0.1UF 25V X6S 0201
C602 0.1UF 25V X6S 0201

VOLTAGE=0.9 VOLTAGE=1.5
C588 X5R 6.3V 47UF 0805
C589 X6S 25V 0.1UF 0201
C590 X6S 25V 0.1UF 0201
C591 X6S 25V 0.1UF 0201
C592 X6S 25V 0.1UF 0201
C597 X5R 6.3V 47UF 0805
C598 X6S 25V 0.1UF 0201
C599 X6S 25V 0.1UF 0201

VOLTAGE=1.5
NO_ASSY=TRUE
R117 0OHM
R118 0OHM
C600 0.1UF X6S 25V 0201
R119 0OHM
C211 0.1UF X6S 25V 0201

G227-10031-01
WGI210AT S LJXR
2 OF 2
U23
VDD3P3_1
VDD3P3_2
VDD3P3_3
VDD3P3_4/VDD1P5
VDD3P3_5
VDD0P9_1
VDD0P9_2
VDD0P9_3
VDD0P9_4
VDD0P9_OUT
VDD1P5_OUT
VDD1P5_1
VDD1P5_2/NC
THRM_PAD

MPN	FUNCTION	PIN51			PIN56	
		POWER	R117	R118	POWER	R119
WGI210AT/WGI210IT	COPPER	3.3V	DE-POP	0 OHM	1.5V	0 OHM
WGI210IS/WGI210CS	SERDES	1.5V	0 OHM	DE-POP	NC	DE-POP

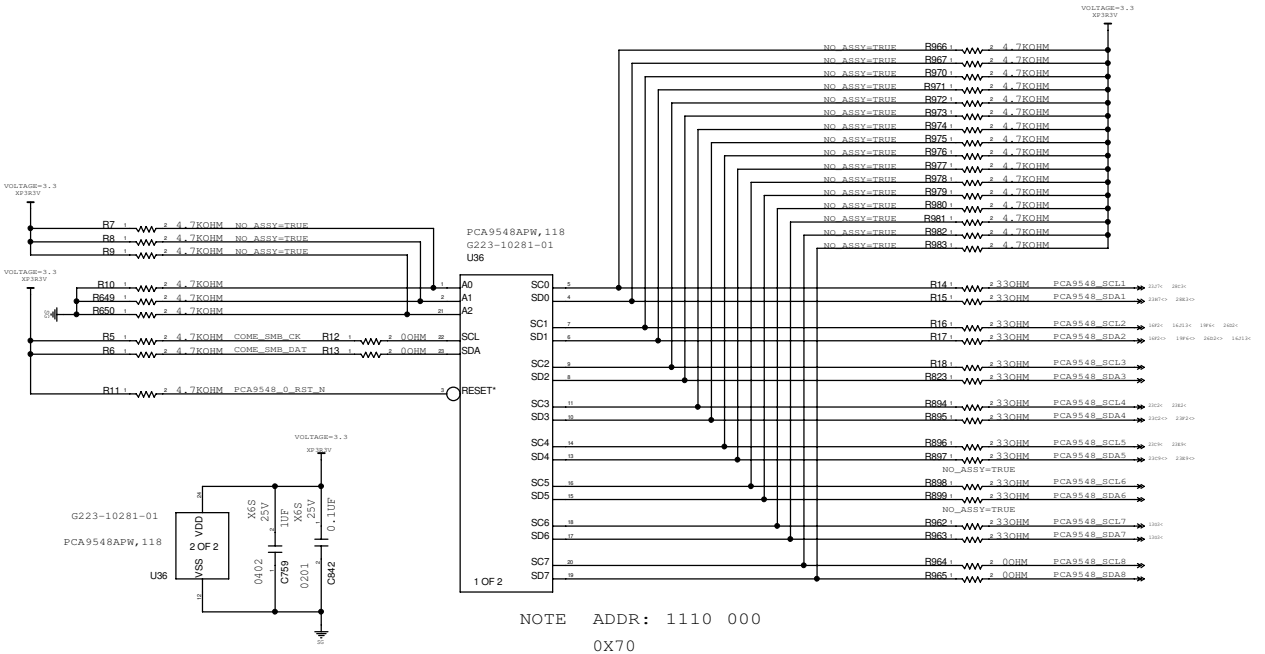
I210_1_JTAG_TDI R840 4.7KOHM
I210_1_JTAG_TMS R841 4.7KOHM
I210_1_JTAG_TDO R842 4.7KOHM
I210_1_JTAG_TCK R843 4.7KOHM NO_ASSY=TRUE
R844 4.7KOHM

W25Q128JVSIQ-T
G221-10373-01
U24
VCC
DI/IO0
GND
WP*/IO2
HOLD*/RESET*/IO3
CS*
DO/IO1
CLK
C586 0.1UF 25V X6S 0201
I210_1_NVM_SI
R698 10KOHM
R665 4.7KOHM
R664 4.7KOHM NO_ASSY=TRUE
R678 4.7KOHM
R667 33OHM
I210_1_NVM_CS_L
I210_1_NVM_SO
I210_1_NVM_SK
R679 33KOHM NO_ASSY=TRUE
R666 10KOHM

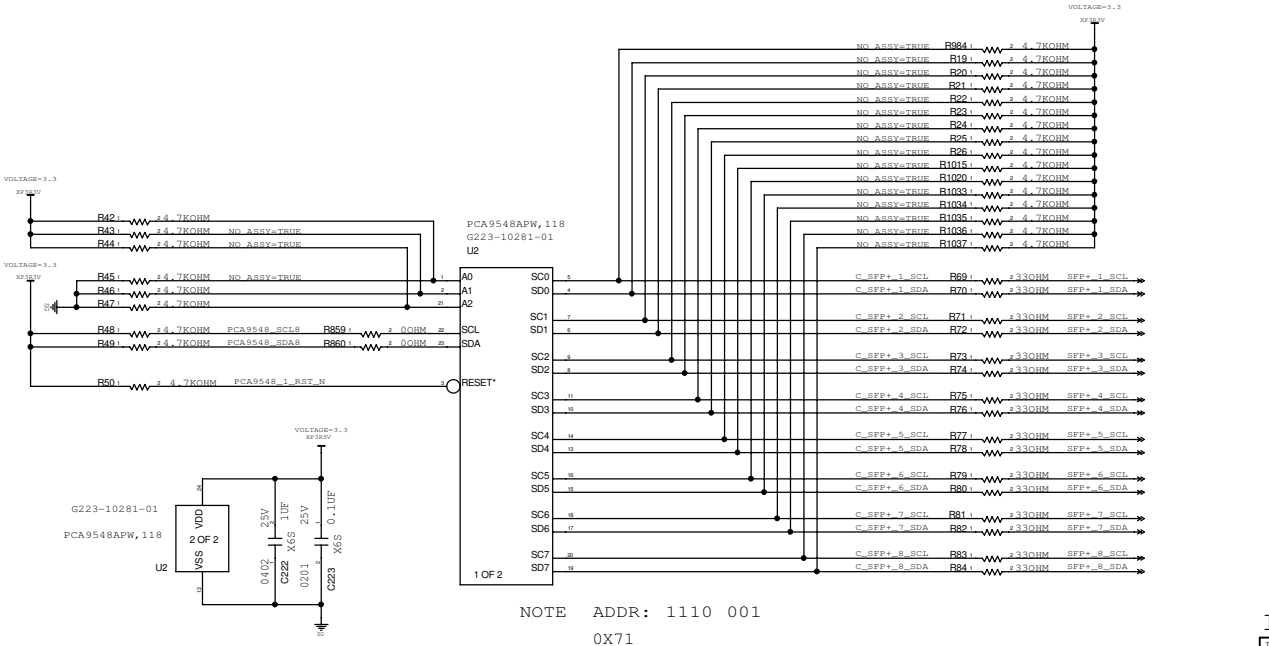
I210AT

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NOTE ADDR: 1110 000
0X70



NOTE ADDR: 1110 001
0X71

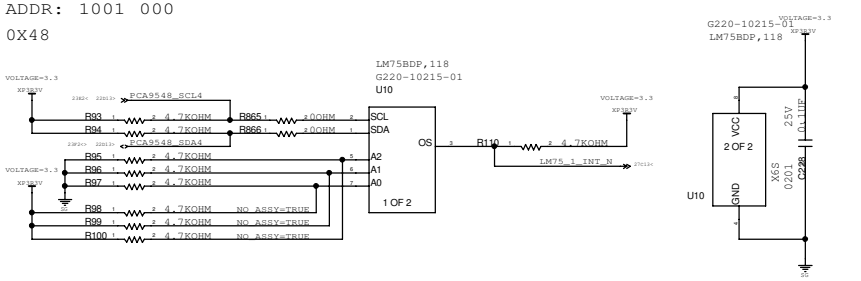
I2C

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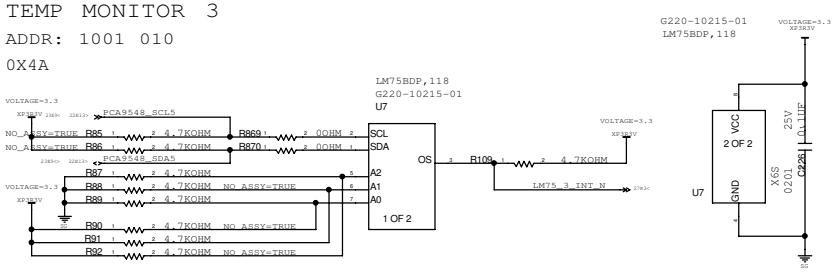
TEMP MONITOR 1

ADDR: 1001 000
0X48



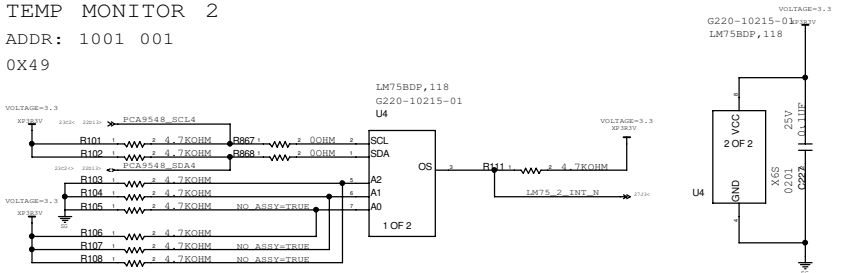
TEMP MONITOR 3

ADDR: 1001 010
0X4A



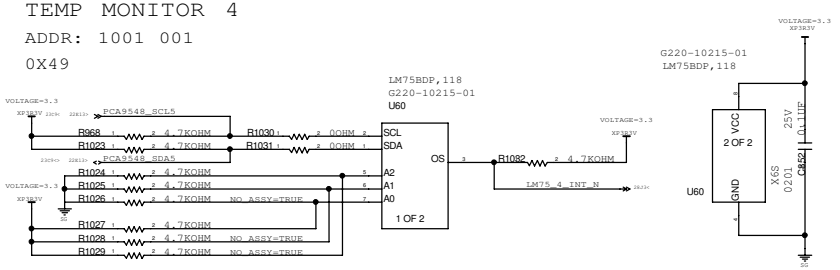
TEMP MONITOR 2

ADDR: 1001 001
0X49

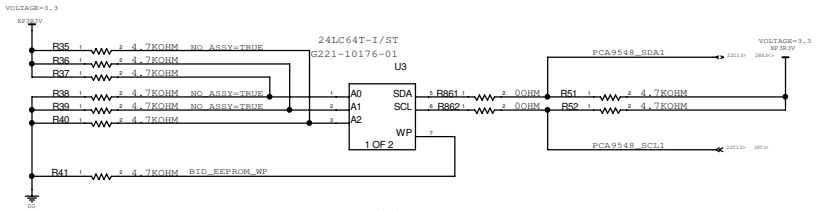


TEMP MONITOR 4

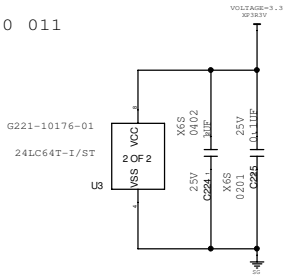
ADDR: 1001 001
0X49



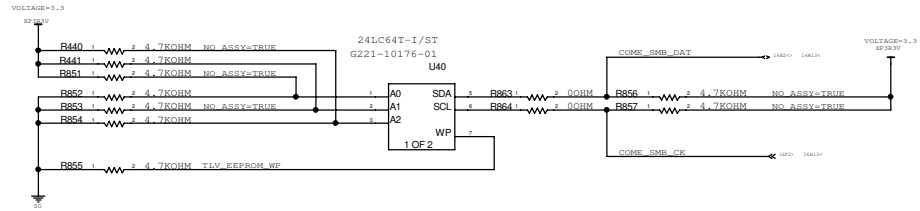
BID EEPROM



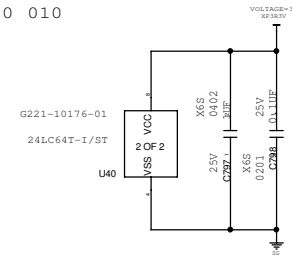
ADDR: 1010 011
0X53



TLV EEPROM



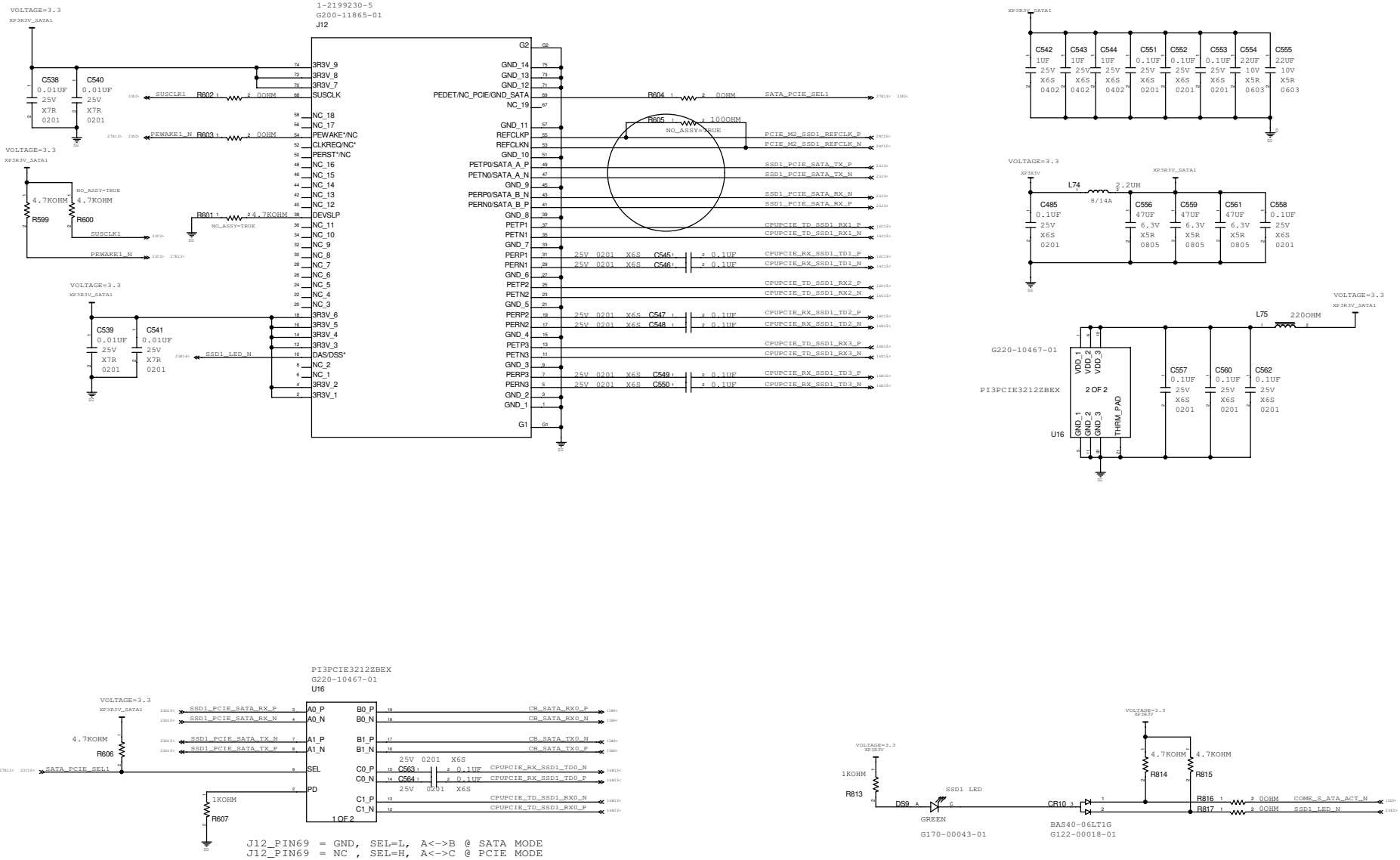
ADDR: 1010 010
0X52



EEPROM&LM75

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PCIE CLK

PI6C49S1504LIEX
G247-10074-01
U13

BUFFER_XP3R3V

100OHM R344, R346
49.9OHM R329, R335
100OHM R345, R347

COME_PCIE_REFCLK_P R325 20OHM
COME_PCIE_REFCLK_N R326 20OHM
C580 0201 0.01UF 25V X7R C_PCIE_REFCLK_DP
C590 0201 0.01UF 25V X7R C_PCIE_REFCLK_DN

BUFFER_CLK_SEL0
BUFFER_CLK_SEL1
SYNC_OE
BUFFER_OPMODEA0
BUFFER_OPMODEA1
BUFFER_OPMODEB0
BUFFER_OPMODEB1

VDD	13	QA0	26
VDDO_1	16	NQA0	27
VDDO_2	26	QA1	23
		NQA1	22
CLK0	5		
NCLK0	6	QB0	20
CLK1	7	NQB0	19
NCLK1	8	QB1	15
		NQB1	14
CLK_SEL0	3		
CLK_SEL1	4	IREF	12
SYNC_OE	11	REF_OUT	21
XTAL_IN	9	XTAL_OUT	10
OPMODEA0	25	NC	2
OPMODEA1	24		
OPMODEB0	17	VEE/GND	1
OPMODEB1	18		

SW_PCIE_REFCLK_P R348 0OHM
SW_PCIE_REFCLK_N R349 0OHM
NA_PCIE_REFCLK_P R350 33OHM
NA_PCIE_REFCLK_N R351 33OHM
PHY_PCIE_REFCLK_P R352 20OHM
PHY_PCIE_REFCLK_N R353 20OHM
SSD_PCIE_REFCLK_P R354 0OHM
SSD_PCIE_REFCLK_N R355 0OHM
BUFFER_IRE R356 475OHM

C791 0201 X7R 0.01UF 25V BCM56277_PCIE_REFCLK_P
C792 0201 X7R 0.01UF 25V BCM56277_PCIE_REFCLK_N
I210_PCIE_REFCLK_P
I210_PCIE_REFCLK_N
C795 0201 X7R 0.01UF 25V PCIE_M2_SSD1_REFCLK_P
C796 0201 X7R 0.01UF 25V PCIE_M2_SSD1_REFCLK_N

R357 49.9OHM
R358 49.9OHM
R359 49.9OHM
R360 49.9OHM
R361 49.9OHM
R362 49.9OHM
R363 49.9OHM
R364 49.9OHM

VOLTAGE=3.3 XP3R3V

SYNC_OE
NO_ASSY=TRUE
R323 4.7KOHM
R324 1KOHM

R327, R330, R336, R338, R340, R342 4.7KOHM
R328, R331, R337, R339, R341, R343 1KOHM

CLK_SEL[1 0]=0 1 CLK0 IS SELECTED

BUFFER_CLK_SEL0
BUFFER_CLK_SEL1
BUFFER_OPMODEA0
BUFFER_OPMODEA1
BUFFER_OPMODEB0
BUFFER_OPMODEB1

OPMODEA/B[1 0]=1 0 HCSL
OPMODEA/B[1 0]=1 1 HIZ

BUFFER_OPMODEA0 R837 0OHM SW_PCIE_REFCLK_EN

L63 220OHM
C417, C419, C421, C423, C425, C426 50V 1000PF X7R 0201
C427 25V 0.1UF X6S 0201

LPC CLK

NO_ASSY=TRUE
R1123 0OHM
CDCLVC1102PWR
G220-10194-01
U41

COME_LPC_CLK
R884 4.7KOHM
R885 33OHM COME_CPLD_LPC_CLK
R886 22OHM COME_TPM_LPC_CLK

CLKIN	1	Y0	3
1G	2	Y1	8
NC_1	5		
NC_2	7		

1 OF 2

C793 25V 0.1UF X6S 0201
U41 2 OF 2 VDD GND

CLOCK

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LVPECL ---> CML

SI5332BD13585-GM3R
G247-10149-01
U5

ADDR: 0X6A

OUT0_P / OUT0_N	R124 / R125	C244 / C245	50MHZ
OUT1_P / OUT1_N	R126 / R127	C246 / C247	50MHZ
OUT2_P / OUT2_N	R128 / R129	C248 / C249	50MHZ
OUT6_P / OUT6_N	R136 / R137	C256 / C257	156.25MHZ
OUT7_P / OUT7_N	R138 / R139	C258 / C259	156.25MHZ
OUT8_P / OUT8_N	R140 / R141	C260 / C261	156.25MHZ
OUT9_P / OUT9_N	R142 / R143	C262 / C263	156.25MHZ
OUT10_P / OUT10_N	R144 / R145	C264 / C265	156.25MHZ

R152 / R153 : 150OHM : SI5332_OUT0_P / SI5332_OUT0_N
R154 / R155 : 150OHM : SI5332_OUT1_P / SI5332_OUT1_N
R156 / R157 : 150OHM : SI5332_OUT2_P / SI5332_OUT2_N
R164 / R165 : 150OHM : SI5332_OUT6_P / SI5332_OUT6_N
R166 / R167 : 150OHM : SI5332_OUT7_P / SI5332_OUT7_N
R168 / R169 : 150OHM : SI5332_OUT8_P / SI5332_OUT8_N
R170 / R171 : 150OHM : SI5332_OUT9_P / SI5332_OUT9_N
R172 / R173 : 150OHM : SI5332_OUT10_P / SI5332_OUT10_N

SI5332_XA — R133 1MOHM NO_ASSY=TRUE — SI5332_XB

48MHZ
G131-10062-01
Y1
XTAL

C231 NO_ASSY=TRUE 15PF COG 0201
C236 15PF 50V COG 0201 NO_ASSY=TRUE

ISOLATED POWER ISLAND — SI53XX_VDDA3V3 — L29 600OHM — VOLTAGE=3.3 XF3R3V

C268	C274	C269	C271	C273	C275
25V 1UF X6S 0402	25V 1UF X6S 0402	25V 1UF X6S 0402	25V 1UF X6S 0402	25V 10UF X6S 0805	6.3V 47UF X5R 0805 NO_ASSY=TRUE

PLACE EACH 1UF CAP CLOSE TO PIN

ISOLATED POWER ISLAND — SI53XX_VDDO3V3 — L30 600OHM — VOLTAGE=3.3 XF3R3V

C240	C241	C242	C243	C270	C272	C276	C277
25V 1UF X6S 0402	25V 1UF X6S 0402	25V 1UF X6S 0402	25V 1UF X6S 0402	25V 1UF X6S 0402	25V 1UF X6S 0402	25V 10UF 6.3V X6S 0805	NO_ASSY=TRUE 47UF X5R 0805

PLACE EACH 1UF CAP CLOSE TO PIN

CLOCK

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G241-10011-01
U6

COME_LPC_FRAME_N — R887 33OHM — R_COME_LPC_FRAME_N — PT9A_0
COME_LPC_SERIRQ — R1043 33OHM — R_COME_LPC_SERIRQ — PT9B_0 / PT9C_0
COME_LPC_AD0 — R888 33OHM — R_COME_LPC_AD0 — PT10A_0
COME_LPC_AD1 — R889 33OHM — R_COME_LPC_AD1 — PT10B_0
COME_LPC_AD2 — R890 33OHM — R_COME_LPC_AD2 — PT11A_0
COME_LPC_AD3 — R891 33OHM — R_COME_LPC_AD3 — PT11B_0
TPM_LPC_RESET_N — R1044 33OHM — R_TPM_LPC_RESET_N — PT11C_0
TPM_LPCPD_L — R1045 33OHM — R_TPM_LPCPD_L — PT11D_0
XP3R3V_MAC_PG — PT12A_0
COME_CPLD_TDI — R838 33OHM — R_CPLD_TDO — PT12C_0/TDO
CPLD_TDI — PT12D_0/TDI
COME_TYPE0 — PT13A_0
BRG_COME_GPIO_3 — PT13B_0
IPROC_LED1_CLK — PT13C_0
LED1_CLK — PT13D_0
IPROC_LED1_DATA — PT16A_0
LED1_DATA — PT16B_0
CPLD_TCK — PT16C_0/TCK/TEST_CLK
CPLD_TMS — PT16D_0/TMS
COME_CPLD_LPC_CLK — PT17A_0/PCLKT0_1
COME_TYPE2 — PT17B_0/PCLKC0_1
BRG_COME_BIOS_DIS1_N — PT17C_0
CB_PWR5_5R0V_PG — PT17D_0

VOLTAGE=3.3
XP3R3V
R839 4.7KOHM COME_CPLD_TDI

BANK0

PT18A_0 — PSU2_ON_N
PT18B_0 — CB_PWR6_1R0V_PG
PT18C_0/SCL/PCLKT0_0 — LM75_1_INT_N
PT18D_0/SDA/PCLKC0_0 — PSU2_PWR_OK
PT19A_0 — PSU2_ALERT_N
PT19B_0 — PSU1_ALERT_N
PT19C_0 — COME_WAKE_N
PT19D_0 — SYS_RESET_N
PT20A_0 — CB_RESET_N
PT20B_0
PT20C_0/JTAGENB — CPLD_JTAGENB — R496 4.7KOHM
PT20D_0/PROGRAMN — CPLD_PROGRAM_N — R497 4.7KOHM — R498 4.7KOHM NO_ASSY=TRUE
PT21A_0
PT21B_0
PT21C_0 — PSU2_PRESENT_N
PT21D_0
PT22A_0 — COME_TYPE1
PT22B_0 — BRG_COME_GPIO_2
PT22C_0 — BRG_COME_GPIO_0
PT22D_0 — UCD_LATE_CNTRL_C
PT23A_0 — BRG_COME_GPIO_1
PT23B_0 — CB_PWR8_3R3V_MDIO_PG
PT24A_0 — R_PWR8_XP3R3V_MDIO_EN — R1826 33OHM — PWR8_XP3R3V_MDIO_EN
PT24B_0 — R_UCD90160_RST_N — R1066 33OHM — UCD90160_RST_N
PT24C_0/INITN — CPLD_INIT_N — R499 4.7KOHM NO_ASSY=TRUE
PT24D_0/DONE — CPLD_DONE — R500 4.7KOHM NO_ASSY=TRUE

VOLTAGE=3.3
XP3R3V

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LCMXO2-2000HC-4FTG256C
G241-10011-01
U6

COME_SUS_STAT_N
COME_PWR_BTN_N
COME_BRG_WDT
BRG_THRM_N
COME_SUS_S4_N
COME_SUS_S5_N
COME_PWROK
BRG_COME_BIOS_DIS0_N
COME_SUS_S3_N
CB_PWR4_0R8V_AVS_PG
SFP+_7_RX_LOS
RTC_CLEAR
LM75_2_INT_N
BRG_THRMTRIP_N
SFP+_7_RATESEL
SFP+_8_TX_DISABLE
TLV_EEPROM_WP
PCA9548_1_RST_N
LM75_3_INT_N
SFP+_7_TX_DISABLE
SFP+_8_TX_FAULT
SFP+_8_MOD_ABS
CPLD_10HZ_CLK
CPLD_WDO_RESERVED
CPLD_WDI_RESERVED
XP12R0V_COME_PG

R121 33OHM

BANK1

PR1A_1 — SFP+_8_RX_LOS
PR1B_1
PR1C_1 — SFP+_7_TX_FAULT
PR1D_1 — BID_EEPROM_WP
PR2A_1
PR2B_1
PR2C_1 — SFP+_7_MOD_ABS
PR2D_1
PR3A_1 — FAN1_FG
PR3B_1 — SFP+_5_MOD_ABS
PR3C_1 — SFP+_8_RATESEL
PR3D_1 — SFP+_6_MOD_ABS
PR4A_1 — SFP+_6_TX_FAULT
PR4B_1 — FAN2_FG
PR4C_1 — SFP+_5_RATESEL
PR4D_1
PR5A_1 — SFP+_5_TX_DISABLE
PR5B_1 — FAN3_FG
PR5C_1 — R_FAN1_PWM — R874 33OHM — FAN1_PWM
PR5D_1 — R_FAN2_PWM — R873 33OHM — FAN2_PWM
PR6A_1 — TPM_SPI_PIRQ_N
PR6B_1 — SFP+_5_TX_FAULT
PR6C_1 — R_FAN3_PWM — R875 33OHM — FAN3_PWM
PR6D_1
PR7A_1/PCLKT1_0
PR7B_1/PCLKC1_0
PR7C_1
PR7D_1

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CPLD

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G241-10011-01
U6

BANK2

SFP+_2_RX_LOS — PB3A_2 (P4)
SFP+_1_TX_DISABLE — PB3B_2 (T4)
IPROC_GPIO0 — PB3C_2 (T2)
IPROC_GPIO3 — PB3D_2 (R3)

CPLD_SPI_CS_N — R902 33OHM — R_CPLD_SPI_CS_N — PB5A_2/CSSPIN (R5)
SFP+_1_MOD_ABS — PB5B_2 (P5)

SFP+_2_TX_FAULT — PB6A_2 (T5)
PCA9548_SCL1 — R892 33OHM — R_PCA9548_SCL1 — PB6B_2 (R6)
SFP+_1_TX_FAULT — PB6C_2 (T3)
IPROC_GPIO2 — PB6D_2 (R4)

CPLD_SPI_CLK — R901 33OHM — R_CPLD_SPI_CLK — PB8A_2/MCLK/CCLK (P6)
CPLD_SPI_MISO — PB8B_2/SO/SPISO (T6)
SFP+_1_RX_LOS — PB8C_2 (N6)
SFP+_2_MOD_ABS — PB8D_2 (L7)

PHY_LOS3 — PB9A_2 (R7)
PHY_LOS2 — PB9B_2 (P7)
SFP+_2_TX_DISABLE — PB9C_2 (M7)
PHY_LOS1 — PB9D_2 (N7)

BCM56277_RST_N — R1047 33OHM — R_BCM56277_RST_N — PB11A_2/PCLKT2_0 (T7)
BCM56277_IPROC_RST_N — R1048 33OHM — R_BCM56277_IPROC_RST_N — PB11B_2/PCLKC2_0 (R8)
PB11C_2 (M8)
PB11D_2 (L8)

PB12A_2 (P8)
PCA9548_SDA1 — R893 33OHM — R_PCA9548_SDA1 — PB12B_2 (T8)
PHY_LOS0 — PB12C_2 (N8)
SFP+_1_RATESEL — PB12D_2 (L9)

3 OF 5

PB16A_2/PCLKT2_1 — R_BIOS_SPI_WP1_N (T9) — R1069 33OHM — BIOS_SPI_WP1_N
PB16B_2/PCLKC2_1 — SFP+_4_MOD_ABS (P9)
PB16C_2 (M8) — SFP+_4_RATESEL
PB16D_2 (N9) — SFP+_3_TX_FAULT

PB18A_2 (R9) — SFP+_5_RX_LOS
PB18B_2 — R_BIOS_SPI_WP0_N (T10) — R1068 33OHM — BIOS_SPI_WP0_N
PB18C_2 (M9) — SFP+_4_RX_LOS
PB18D_2 (L10) — SFP+_6_RATESEL

PB19A_2 (P10) — SFP+_3_MOD_ABS
PB19B_2 (R10) — SFP+_3_RATESEL
PB19C_2 (N10) — SFP+_4_TX_FAULT
PB19D_2 (M10) — SFP+_6_TX_DISABLE

PB21A_2 (T11)
PB21B_2 (P11) — SFP+_3_RX_LOS
PB21C_2 (M11) — SFP+_3_TX_DISABLE
PB21D_2 (N11) — SFP+_4_TX_DISABLE

PB22A_2 (R11)
PB22B_2 (T12) — R_GPHY_LED_RESET_L — R1046 33OHM — GPHY_LED_RESET_L
PB22C_2 (R12) — R_PCA9548_0_RST_N — R1063 33OHM — PCA9548_0_RST_N
PB22D_2 (T14) — R_PEWAKE1_N — R1070 33OHM — PEWAKE1_N

PB24A_2 (P12) — SFP+_6_RX_LOS
PB24B_2 (T13) — R_BIOS0_ON — R1059 33OHM — BIOS0_ON

PB25A_2/SN (R12) — R_BIOS_ENABLE — R1058 33OHM — BIOS_ENABLE
PB25B_2/SI/SISPI (P13) — R_CPLD_SPI_MOSI — R900 33OHM — CPLD_SPI_MOSI
PB25C_2 (T15) — R_PWR4_XP0R8V_AVS_EN — R1654 33OHM — PWR4_XP0R8V_AVS_EN
PB25D_2 (R14) — R_SATA_PCIE_SEL1 — R1067 33OHM — SATA_PCIE_SEL1

W25Q64JVSSIQ
G221-10006-01
U56

VOLTAGE=3.3 XP3R3V

R1103 4.7KOHM CPLD_SPI_CS_N → CS*
R1104 4.7KOHM CPLD_SPI_CLK → CLK
DI/IO0 — CPLD_SPI_MOSI
DO/IO1 — R1105 33OHM — CPLD_SPI_MISO
WP*/IO2 — R1106 4.7KOHM
HOLD*/IO3 — R1107 4.7KOHM
R1108 4.7KOHM NO ASSY=TRUE

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G221-10006-01
W25Q64JVSSIQ
U56
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VCC
GND
C133 0.1UF 25V X6S 0201

LCMXO2-2000HC-4FTG256C
G241-10011-01
U6

CHIP_RST_OUT_L — PL11A_3 (L1)
AVS2 — PL11B_3 (L3)
AVS3 — PL11C_3 (K4)
SFP+_2_RATESEL — PL11D_3 (L5)

BCM56277_PCIE_WAKE_N — R1050 33OHM — R_BCM56277_PCIE_WAKE_N — PL12A_3/PCLKT3_0 (L2)
BCM56277_PE_RST_N — R1049 33OHM — R_BCM56277_PE_RST_N — PL12B_3/PCLKC3_0 (M5)
AVS0 — PL12C_3 (K5)
LM75_4_INT_N — PL12D_3 (L4)

FAN_LED_AMBER — PL6A_4 (G1)
XP12R0V_COME_EN — R1120 33OHM — R_XP12R0V_COME_EN — PL6B_4 (H5)
USB_PWR1_EN — R1055 33OHM — R_USB_PWR1_EN — PL6C_4 (H6)
CB_PWR1_3R3V_PG — PL6D_4 (J6)

USB_PWR2_EN — R1056 33OHM — R_USB_PWR2_EN — PL7A_4 (H5)
SYS_LED_AMBER — PL7B_4 (H6)
SI5332_OE_N — R1057 33OHM — R_SI5332_OE_N — PL7C_4/PCLKT4_0 (J1)
CB_PWR3_0R8V_PG — PL7D_4/PCLKC4_0 (J3)

B50282_2_RST_N — R1052 33OHM — R_B50282_2_RST_N — PL1A_5/L_GPLLT_FB (D3)
I210_WAKE_N — R1064 33OHM — R_I210_WAKE_N — PL1B_5/L_GPLLC_FB (D1)
B50282_3_RST_N — R1053 33OHM — R_B50282_3_RST_N — PL1C_5 (B1)
PWR5_XP5R0V_EN — R1655 33OHM — R_PWR5_XP5R0V_EN — PL1D_5 (C2)

B50282_1_RST_N — R1051 33OHM — R_B50282_1_RST_N — PL2A_5/L_GPLLT_IN (E2)
I210_RST_N — R1065 33OHM — R_I210_RST_N — PL2B_5/L_GPLLC_IN (E3)
PWR6_XP1R0V_EN — R1656 33OHM — R_PWR6_XP1R0V_EN — PL2C_5 (C1)
XP3R3V_MAC_EN — R1121 33OHM — R_XP3R3V_MAC_EN — PL2D_5 (D2)

ALARM_LED_GREEN — PL3A_5/PCLKT5_0 (E1)
ALARM_LED_AMBER — PL3B_5/PCLKC5_0 (F2)
B50282_2_INT_N — PL3C_5 (F4)
PSU1_ON_N — PL3D_5 (G6)

NC (A2)

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BANK3

PL13A_3 (M3) — CB_PWR7_1R2V_PG
PL13B_3 (N3) — LED1 GREEN — DS10 — R819 1KOHM
PL13C_3 (N2) — LED2 GREEN — DS11 — R820 1KOHM
PL13D_3 (P1) — LED3 GREEN — DS12 — R821 1KOHM
PL14A_3 (M6) — LED4 GREEN — DS13 — R822 1KOHM
PL14B_3 (N3) — PHY_LOS4
PL14C_3 (P1) — IPROC_GPIO1
PL14D_3 (P2) — R_PWR7_XP1R2V_EN — R1657 33OHM — PWR7_XP1R2V_EN

BANK4

PL9A_4 (J2) — R_SW_PCIE_REFCLK_EN — R1054 33OHM — SW_PCIE_REFCLK_EN
PL9B_4 (K1) — R_PWR2_XP1R8V_EN — R1652 33OHM — PWR2_XP1R8V_EN
PL9C_4 (H5) — CB_PWR2_1R8V_PG
PL9D_4 (J4) — AVS1

PL10A_4 (K3) — BCM56277_PCIE_INT_N
PL10B_4 (K2) — R_PWR3_XP0R8V_EN — R1653 33OHM — PWR3_XP0R8V_EN
PL10C_4 (J5) — 10GPHY_LED_RESET_L
PL10D_4 (K6)

BANK5

PL4A_5 (F3) — PSU_LED_AMBER
PL4B_5 (F1) — PSU_LED_GREEN
PL4C_5 (G5) — PSU1_PWR_OK
PL4D_5 (G4) — B50282_1_INT_N

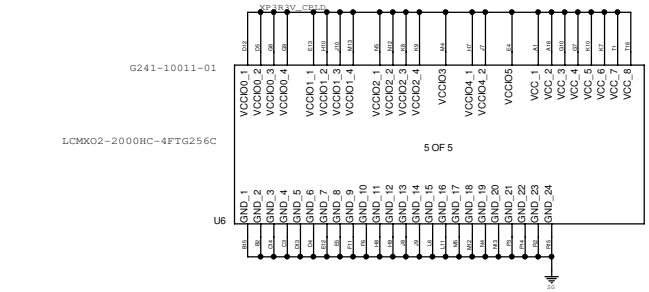
PL5A_5 (G2) — FAN_LED_GREEN
PL5B_5 (G3) — SYS_LED_GREEN
PL5C_5 (F5) — PSU1_PRESENT_N
PL5D_5 (H6) — B50282_3_INT_N

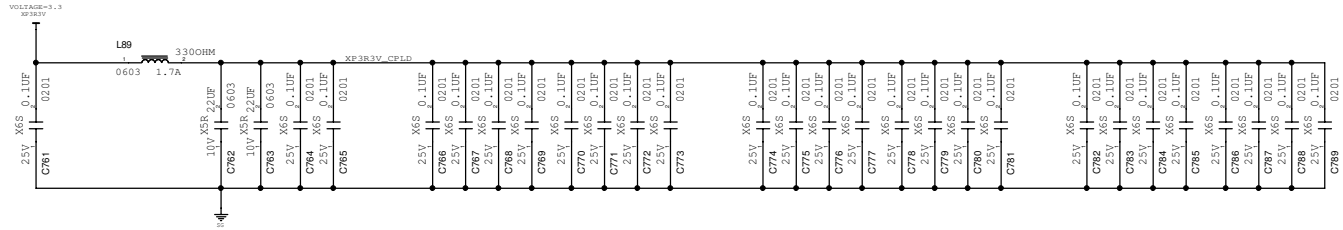
VOLTAGE=3.3 XP3R3V

CPLD

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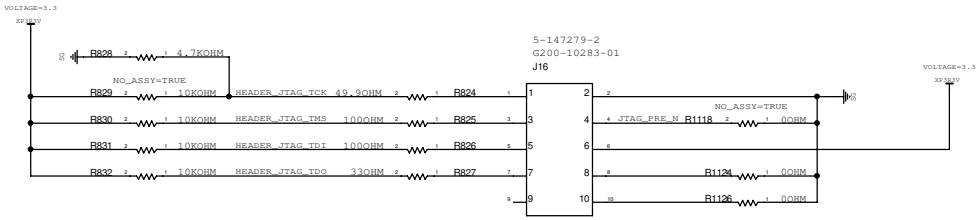


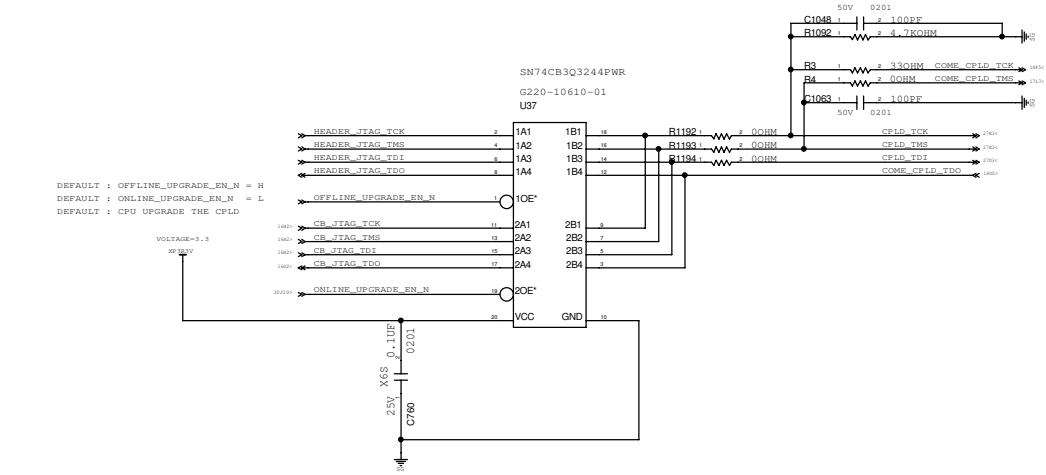


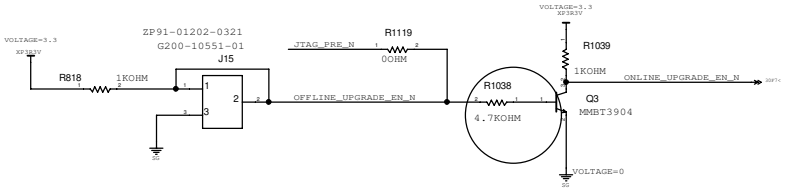
CPLD

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JTAG

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BCM56277A1KFSBG
G227-10245-01
U1

SYS_RST_L
IPROC_EXT_RST_N
TEST_GD_L
PCIE_RC_MODE
TEST_DG
TEST_BOA
BYPASS_IHOST_PWRUP_SEQ
IHOST_PLL_BYPASS_CLOCK_SEL
DIRECT_SCAN
CORE_PLL_BYP
XG_SERDES_PLL_BYP
XG_MASTER_PLL_BYP
TS_PLL_BYP
IPROC_PLL_BYP
PWRUP_RSB_OVERRIDE
PLL_LOCK_OVERRIDE
USB_ID
USB_VBUS_OVC
USB_VBUS_PPC
USB_VBUS_PRESENT
QSPI_QUAD_LANE
QSPI_DUAL_LANE
QSPI_ADDR_BPC_MODE
QSPI_4BYTE_ADDR
BOOT_DEV2
BOOT_DEV1
BOOT_DEV0
CHIP_TEST_MODE3
CHIP_TEST_MODE2
CHIP_TEST_MODE1
CHIP_TEST_MODE0
AVS3
AVS2
AVS1
AVS0
RESCAL_2_REXT
RESCAL_1_REXT
RESCAL_0_REXT
JTCE1
JTCE0
JTDI
JTDO
JTMS
JTCK
JTRST_L
IPROC_LED1_DATA
IPROC_LED1_CLK
IPROC_LED0_DATA
IPROC_LED0_CLK
TP33 TP32 TP31 TP30 TP29 TP28 TP27 TP26 TP25 TP24 TP23 TP22 TP21 TP20 TP19 TP18 TP17 TP16 TP15 TP14 TP13 TP12 TP11 TP10 TP9 TP8 TP7 TP6 TP5 TP4 TP3
PHY_LOS4
PHY_LOS3
PHY_LOS2
PHY_LOS1
PHY_LOS0
IPROC_TS_GPIO5
IPROC_TS_GPIO4
IPROC_TS_GPIO3
IPROC_TS_GPIO2
IPROC_TS_GPIO1
IPROC_TS_GPIO0
IPROC_GPIO9
IPROC_GPIO8
IPROC_GPIO7
IPROC_GPIO6
IPROC_GPIO5
IPROC_GPIO4
IPROC_GPIO3
IPROC_GPIO2
IPROC_GPIO1
IPROC_GPIO0
CHIP_RST_OUT_L
NC_BGA

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R260 4.7KOHM NO_ASSY=TRUE
R239 0OHM
BCM56277_RST_N
BCM56277_IPROC_RST_N
R240 0OHM
R580 4.7KOHM
R228 4.7KOHM PCIE_RC_MODE
R541 4.7KOHM
R120 4.7KOHM
R229 4.7KOHM NO_ASSY=TRUE
R230 4.7KOHM NO_ASSY=TRUE
R231 4.7KOHM NO_ASSY=TRUE
R232 4.7KOHM NO_ASSY=TRUE
R233 4.7KOHM NO_ASSY=TRUE
R234 4.7KOHM NO_ASSY=TRUE
R235 4.7KOHM NO_ASSY=TRUE
R876 33OHM
R877 33OHM
R878 33OHM
R879 33OHM
R236 0.53KOHM 1%
R237 0.53KOHM 1%
R238 0.53KOHM 1%
R268 4.7KOHM NO_ASSY=TRUE
R269 4.7KOHM NO_ASSY=TRUE
R250 4.7KOHM
R251 4.7KOHM
R252 4.7KOHM
R253 4.7KOHM NO_ASSY=TRUE
R254 4.7KOHM
R836 4.7KOHM
R255 4.7KOHM
R256 4.7KOHM NO_ASSY=TRUE
R241 33OHM LED1_DATA
R812 0OHM IPROC_LED1_DATA
R242 33OHM LED1_CLK
R1154 0OHM IPROC_LED1_CLK
R243 33OHM NO_ASSY=TRUE
R244 33OHM NO_ASSY=TRUE
R245 4.7KOHM
R246 4.7KOHM NO_ASSY=TRUE
R247 4.7KOHM NO_ASSY=TRUE
R248 4.7KOHM
R249 4.7KOHM
R262 0OHM SFP+_1_RX_LOS
R267 0OHM SFP+_5_RX_LOS
R1083 33OHM IPROC_GPIO3
R1084 33OHM IPROC_GPIO2
R1085 33OHM IPROC_GPIO1
R1086 33OHM IPROC_GPIO0
R1087 33OHM CHIP_RST_OUT_L
R261 4.7KOHM

VOLTAGE=3.3 XF3R3V

JTCE[1:0], JTAG TEST CONTROLLER ENABLE:
00, FUNCTIONAL MODE/ARM CORTEX-A72 DEBUG MODE

QSPI_QUAD_LANE: 1,IPROC_QSPI USING 4-BIT PARALLEL INTERFACE
0,IPROC_QSPI USING A SERIAL INTERFACE

QSPI_DUAL_LANE: 1,IPROC_QSPI USING 2-BIT PARALLEL INTERFACE
0,IPROC_QSPI USING A SERIAL INTERFACE

QSPI_ADDR_BPC_MODE: 1,COMMANDS, ADDRESSES, AND DATA ARE SENT IN PARALLEL
0,COMMANDS AND ADDRESSES ARE SENT SERIALLY, DATA IS SENT IN PAREALLEL

QSPI_4BYTE_ADDR: 1,QSPI FLASH IS OPERATING IN A 4-BYTE ADDRESS MODE.
0, QSPI FLASH IS OPERATING IN A 3-BYTE ADDRESS MODE

BOOT_DEV[2:0]: 001: LOAD ALL NECESSARY CODE FROM THE NAND FLASH ATTACHED TO THE NAND INTERFACE AND BEGIN EXECUTION
000: LOAD ALL NECESSARY CODE FROM QSPI FLASH ATTACHED TO THE IPROC_QSPI INTERFACE AND BEGIN EXECUTION.

CORE VOLTAGE	AVS3	AVS2	AVS1	AVS0
0.800V	0	0	0	1
0.825V	0	0	1	0
0.850V	0	1	0	0
0.875V	1	0	0	0

PLACE RESISTORS CLOSE TO PINS.

BCM56277_IPROC_1

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ACA-SPI-006-K02
G210-10031-01
J2

C310 25V X6S 0.1UF 0201

R257 4.7KOHM BCM56277_QSPI_CS_N
R258 4.7KOHM BCM56277_QSPI_SCK
R259 4.7KOHM BCM56277_QSPI_RST_N

VCC, CS*, CLK, RESET*, NC_1, NC_2, NC_3, NC_4, DI/IO0, DO/IO1, WP*/IO2, HOLD*/IO3, NC_5, NC_6, NC_7, GND

R263 4.7KOHM BCM56277_QSPI_MOSI
R264 4.7KOHM BCM56277_QSPI_MISO
R265 4.7KOHM BCM56277_QSPI_WP_N
R266 4.7KOHM BCM56277_QSPI_HOLD_N

G221-10139-01
JU2

BCM56277A1KFSBG
G227-10245-01
U1

USB2_ID0
R945 100OHM R283 100OHM USB2_TXRTUNE
R542 4.7KOHM USB2_DP
R566 4.7KOHM USB2_DM

BCM56277_UART1_SOUT → IPROC_UART1_SOUT
BCM56277_UART1_SIN → IPROC_UART1_SIN
R567 4.7KOHM
R494 4.7KOHM

BCM56277_UART0_SOUT → IPROC_UART0_SOUT
BCM56277_UART0_SIN → IPROC_UART0_SIN
R495 4.7KOHM IPROC_UART0_RTS_N
R531 4.7KOHM IPROC_UART0_CTS_N

BCM56277_QSPI_MOSI → IPROC_QSPI_MOSI
BCM56277_QSPI_MISO → IPROC_QSPI_MISO
BCM56277_QSPI_SCK R1093 33OHM → IPROC_QSPI_SCK
BCM56277_QSPI_CS_N → IPROC_QSPI_CS_N
BCM56277_QSPI_HOLD_N → IPROC_QSPI_HOLD_N
BCM56277_QSPI_WP_N → IPROC_QSPI_WP_N

IPROC_SPI_MOSI
IPROC_SPI_MISO
IPROC_SPI_SCK
IPROC_SPI_SS_N

R271 4.7KOHM PCA9548_SDA6 R1088 33OHM IPROC_BSC3_SDA
R272 4.7KOHM PCA9548_SCL6 R1089 33OHM IPROC_BSC3_SCL
R273 4.7KOHM IPROC_BSC2_SDA
R274 4.7KOHM IPROC_BSC2_SCL
R275 4.7KOHM IPROC_BSC1_SDA
R276 4.7KOHM IPROC_BSC1_SCL
R277 4.7KOHM BCM56277_I2C_SDA R1090 33OHM IPROC_BSC0_SDA
R278 4.7KOHM BCM56277_I2C_SCL R1091 33OHM IPROC_BSC0_SCL
R279 4.7KOHM NO_ASSY=TRUE BSC_SA_1
R280 4.7KOHM NO_ASSY=TRUE BSC_SA_0
R281 100OHM
R282 100OHM

ADDR: 1000 100
0X44

IPROC_MDIO3 R880 0OHM GIG_MDIO3 R303 4.7KOHM
IPROC_MDC3 R284 33OHM GIG_MDC3 R304 4.7KOHM
IPROC_MDIO2 R881 0OHM GIG_MDIO2 R305 4.7KOHM
IPROC_MDC2 R285 33OHM GIG_MDC2 R306 4.7KOHM
IPROC_MDIO1 R882 0OHM GIG_MDIO1 R307 4.7KOHM
IPROC_MDC1 R286 33OHM GIG_MDC1 R308 4.7KOHM
IPROC_MDIO0 R883 0OHM GIG_MDIO0 R309 4.7KOHM
IPROC_MDC0 R287 33OHM GIG_MDC0 R310 4.7KOHM

TP_R_16, TP_R_13, TP_R_12, TP_R_11, TP_R_10, TP_R_9, TP_R_8, TP_R_7, TP_R_6, TP_R_5, TP_R_4, TP_R_3, TP_R_2, TP_R_1, TP_R_0

PVTMON5_VDAC PVTMON5_VDAC C1049 0.1UF NO ASSY=TRUE 25V 0402
PVTMON5_ADC PVTMON5_ADC C1050 0.1UF NO ASSY=TRUE 25V 0402

PVTMON4_VDAC, PVTMON4_ADC, PVTMON3_VDAC, PVTMON3_ADC, PVTMON1_VDAC, PVTMON1_ADC, PVTMON0_VDAC, PVTMON0_ADC

NAND_FLASH_AD7, NAND_FLASH_AD6, NAND_FLASH_AD5, NAND_FLASH_AD4, NAND_FLASH_AD3, NAND_FLASH_AD2, NAND_FLASH_AD1, NAND_FLASH_AD0
NAND_FLASH_CLE, NAND_FLASH_ALE
NAND_FLASH_WP_N, NAND_FLASH_RE_N, NAND_FLASH_WE_N
NAND_FLASH_RB_N
NAND_FLASH_CE1_N, NAND_FLASH_CE0_N
NAND_FLASH_ECC_LEVEL3, NAND_FLASH_ECC_LEVEL2, NAND_FLASH_ECC_LEVEL1, NAND_FLASH_ECC_LEVEL0
NAND_FLASH_PAGE1, NAND_FLASH_PAGE0

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BCM56277_IPROC_2

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G227-10245-01
U1

BCM56277_XG_MASTER_PLL_REFCLK_P
BCM56277_XG_MASTER_PLL_REFCLK_N
BCM56277_XG_SERDES_PLL_REFCLK_P
BCM56277_XG_SERDES_PLL_REFCLK_N
BCM56277_IPROC_PLL_REFCLK_P
BCM56277_IPROC_PLL_REFCLK_N
BCM56277_PCIE_REFCLK_P
BCM56277_PCIE_REFCLK_N
BCM56277_TSC0_REFCLK_P
BCM56277_TSC0_REFCLK_N
BCM56277_TSC3_REFCLK_P
BCM56277_TSC3_REFCLK_N

XG_MASTER_PLL_REFCLK_P
XG_MASTER_PLL_REFCLK_N
XG_SERDES_PLL_REFCLK_P
XG_SERDES_PLL_REFCLK_N
IPROC_PLL_FREFP
IPROC_PLL_FREFN
PCIE_REFCLKP
PCIE_REFCLKN
TSC0_REFCLKP
TSC0_REFCLKN
TSC1_REFCLKP
TSC1_REFCLKN
TSC2_REFCLKP
TSC2_REFCLKN
TSC3_REFCLKP
TSC3_REFCLKN
TSC_IPROC_REFCLKP
TSC_IPROC_REFCLKN
CORE_PLL_REFCLKP
CORE_PLL_REFCLKN
TS_PLL_FREFP
TS_PLL_FREFN

XG_SERDES_PLL_BYP_CLK25
XG_MASTER_PLL_BYP_CLK25
XG_SERDES_PLL_BYP_CLK125
XG_MASTER_PLL_BYP_CLK125
USB_PHY_BYP_CLK
IHOST_PLL_BYP_CLK
DDR_PHY_BYP_CLK

XG_MASTER_PLL_TESTP
XG_MASTER_PLL_TESTN
XG_SERDES_PLL_TESTP
XG_SERDES_PLL_TESTN
IPROC_PLL_TESTP
IPROC_PLL_TESTN
PCIE_TESTP
PCIE_TESTN
TSC2_PTESTP
TSC2_PTESTN
TSC1_TESTP
TSC1_TESTN
TSC0_TESTP
TSC0_TESTN
TSC_IPROC_TESTP
TSC_IPROC_TESTN
CORE_PLL_TESTP
CORE_PLL_TESTN
TS_PLL_TESTP
TS_PLL_TESTN

L1_RCVRD_CLK
L1_RCVRD_CLK_VALID
L1_RCVRD_CLK_BKUP
L1_RCVRD_CLK_VALID_BKUP

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R270 100OHM
R312 1% 100OHM
R1122 200OHM
SG

BCM56277A1KFSBG
G227-10245-01
U1

DDR_AD0
DDR_AD1
DDR_AD2
DDR_AD3
DDR_AD4
DDR_AD5
DDR_AD6
DDR_AD7
DDR_AD8
DDR_AD9
DDR_AD10
DDR_AD11
DDR_AD12
DDR_AD13
DDR_AD14_WE_N
DDR_AD15_CAS_N
DDR_AD16_RAS_N
DDR_BA0
DDR_BA1
DDR_BG0
DDR_BG1
DDR_CS0_N
DDR_CKE
DDR_ODT
DDR_RST_N
DDR_ACT_N
DDR_PAR
DDR_ALERT0_N
DDR_ZQ

DDR_DQS0_P
DDR_DQS0_N
DDR_DBI0
DDR_DQ0
DDR_DQ1
DDR_DQ2
DDR_DQ3
DDR_DQ4
DDR_DQ5
DDR_DQ6
DDR_DQ7
DDR_DQS1_P
DDR_DQS1_N
DDR_DBI1
DDR_DQ8
DDR_DQ9
DDR_DQ10
DDR_DQ11
DDR_DQ12
DDR_DQ13
DDR_DQ14
DDR_DQ15
DDR_DQS2_P
DDR_DQS2_N
DDR_DBI2
DDR_DQ16
DDR_DQ17
DDR_DQ18
DDR_DQ19
DDR_DQ20
DDR_DQ21
DDR_DQ22
DDR_DQ23
DDR_DQS3_P
DDR_DQS3_N
DDR_DBI3
DDR_DQ24
DDR_DQ25
DDR_DQ26
DDR_DQ27
DDR_DQ28
DDR_DQ29
DDR_DQ30
DDR_DQ31
DDR_DQS4_P
DDR_DQS4_N
DDR_DBI4
DDR_DQ32
DDR_DQ33
DDR_DQ34
DDR_DQ35
DDR_CK_P
DDR_CK_N

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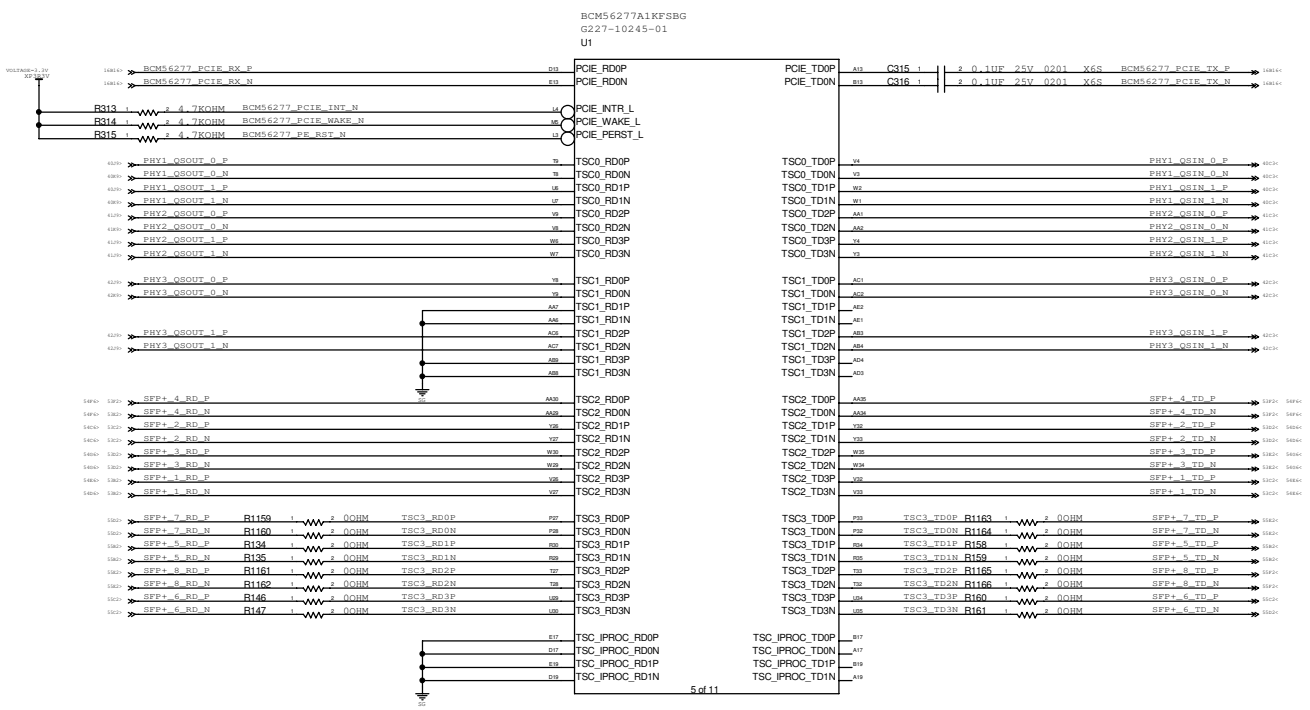
BCM56277_PCIE_REFCLK_P R311 100OHM BCM56277_PCIE_REFCLK_N

BCM5277_REFCLK

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A2 (420X594)

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G227-10245-01
U1

R1182 499KOHM
R316 6.04KOHM 0.5% AF6
R1183 499KOHM
R317 6.04KOHM 0.5% AD11
R1184 499KOHM
R318 6.04KOHM 0.5% AF10

GPHY0_RDAC
GPHY1_RDAC
GPHY2_RDAC

GPHY0_TVCO AG5
GPHY1_TVCO AG10
GPHY2_TVCO AG10

C_P25_CH0_TDP	AL1	GPHY0_PHY0_CH0_TDP	GPHY0_PHY2_CH0_TDP	AR3	C_P27_CH0_TDP
C_P25_CH0_TDN	AK1	GPHY0_PHY0_CH0_TDN	GPHY0_PHY2_CH0_TDN	AP3	C_P27_CH0_TDN
C_P25_CH1_TDP	AL2	GPHY0_PHY0_CH1_TDP	GPHY0_PHY2_CH1_TDP	AK4	C_P27_CH1_TDP
C_P25_CH1_TDN	AK2	GPHY0_PHY0_CH1_TDN	GPHY0_PHY2_CH1_TDN	AJ4	C_P27_CH1_TDN
C_P25_CH2_TDP	AN1	GPHY0_PHY0_CH2_TDP	GPHY0_PHY2_CH2_TDP	AP4	C_P27_CH2_TDP
C_P25_CH2_TDN	AM1	GPHY0_PHY0_CH2_TDN	GPHY0_PHY2_CH2_TDN	AN4	C_P27_CH2_TDN
C_P25_CH3_TDP	AR1	GPHY0_PHY0_CH3_TDP	GPHY0_PHY2_CH3_TDP	AL5	C_P27_CH3_TDP
C_P25_CH3_TDN	AP1	GPHY0_PHY0_CH3_TDN	GPHY0_PHY2_CH3_TDN	AK5	C_P27_CH3_TDN
C_P26_CH0_TDP	AL3	GPHY0_PHY1_CH0_TDP	GPHY0_PHY3_CH0_TDP	AN6	C_P28_CH0_TDP
C_P26_CH0_TDN	AK3	GPHY0_PHY1_CH0_TDN	GPHY0_PHY3_CH0_TDN	AM6	C_P28_CH0_TDN
C_P26_CH1_TDP	AN3	GPHY0_PHY1_CH1_TDP	GPHY0_PHY3_CH1_TDP	AL6	C_P28_CH1_TDP
C_P26_CH1_TDN	AM3	GPHY0_PHY1_CH1_TDN	GPHY0_PHY3_CH1_TDN	AK6	C_P28_CH1_TDN
C_P26_CH2_TDP	AN2	GPHY0_PHY1_CH2_TDP	GPHY0_PHY3_CH2_TDP	AR5	C_P28_CH2_TDP
C_P26_CH2_TDN	AM2	GPHY0_PHY1_CH2_TDN	GPHY0_PHY3_CH2_TDN	AP5	C_P28_CH2_TDN
C_P26_CH3_TDP	AR2	GPHY0_PHY1_CH3_TDP	GPHY0_PHY3_CH3_TDP	AN5	C_P28_CH3_TDP
C_P26_CH3_TDN	AP2	GPHY0_PHY1_CH3_TDN	GPHY0_PHY3_CH3_TDN	AM5	C_P28_CH3_TDN
C_P29_CH0_TDP	AP8	GPHY1_PHY0_CH0_TDP	GPHY1_PHY2_CH0_TDP	AR9	C_P31_CH0_TDP
C_P29_CH0_TDN	AP9	GPHY1_PHY0_CH0_TDN	GPHY1_PHY2_CH0_TDN	AP9	C_P31_CH0_TDN
C_P29_CH1_TDP	AK7	GPHY1_PHY0_CH1_TDP	GPHY1_PHY2_CH1_TDP	AK10	C_P31_CH1_TDP
C_P29_CH1_TDN	AJ7	GPHY1_PHY0_CH1_TDN	GPHY1_PHY2_CH1_TDN	AJ10	C_P31_CH1_TDN
C_P29_CH2_TDP	AP7	GPHY1_PHY0_CH2_TDP	GPHY1_PHY2_CH2_TDP	AM10	C_P31_CH2_TDP
C_P29_CH2_TDN	AN7	GPHY1_PHY0_CH2_TDN	GPHY1_PHY2_CH2_TDN	AL10	C_P31_CH2_TDN
C_P29_CH3_TDP	AL8	GPHY1_PHY0_CH3_TDP	GPHY1_PHY2_CH3_TDP	AP10	C_P31_CH3_TDP
C_P29_CH3_TDN	AN8	GPHY1_PHY0_CH3_TDN	GPHY1_PHY2_CH3_TDN	AN10	C_P31_CH3_TDN
C_P30_CH0_TDP	AN9	GPHY1_PHY1_CH0_TDP	GPHY1_PHY3_CH0_TDP	AK12	C_P32_CH0_TDP
C_P30_CH0_TDN	AM9	GPHY1_PHY1_CH0_TDN	GPHY1_PHY3_CH0_TDN	AL12	C_P32_CH0_TDN
C_P30_CH1_TDP	AL9	GPHY1_PHY1_CH1_TDP	GPHY1_PHY3_CH1_TDP	AP11	C_P32_CH1_TDP
C_P30_CH1_TDN	AK9	GPHY1_PHY1_CH1_TDN	GPHY1_PHY3_CH1_TDN	AR11	C_P32_CH1_TDN
C_P30_CH2_TDP	AP8	GPHY1_PHY1_CH2_TDP	GPHY1_PHY3_CH2_TDP	AK11	C_P32_CH2_TDP
C_P30_CH2_TDN	AP8	GPHY1_PHY1_CH2_TDN	GPHY1_PHY3_CH2_TDN	AL11	C_P32_CH2_TDN
C_P30_CH3_TDP	AN8	GPHY1_PHY1_CH3_TDP	GPHY1_PHY3_CH3_TDP	AH11	C_P32_CH3_TDP
C_P30_CH3_TDN	AM8	GPHY1_PHY1_CH3_TDN	GPHY1_PHY3_CH3_TDN	AJ11	C_P32_CH3_TDN
C_P33_CH0_TDP	AR12	GPHY2_PHY0_CH0_TDP	GPHY2_PHY2_CH0_TDP	AN15	C_P35_CH0_TDP
C_P33_CH0_TDN	AP12	GPHY2_PHY0_CH0_TDN	GPHY2_PHY2_CH0_TDN	AM15	C_P35_CH0_TDN
C_P33_CH1_TDP	AN12	GPHY2_PHY0_CH1_TDP	GPHY2_PHY2_CH1_TDP	AR15	C_P35_CH1_TDP
C_P33_CH1_TDN	AM12	GPHY2_PHY0_CH1_TDN	GPHY2_PHY2_CH1_TDN	AP15	C_P35_CH1_TDN
C_P33_CH2_TDP	AM13	GPHY2_PHY0_CH2_TDP	GPHY2_PHY2_CH2_TDP	AP16	C_P35_CH2_TDP
C_P33_CH2_TDN	AL13	GPHY2_PHY0_CH2_TDN	GPHY2_PHY2_CH2_TDN	AN16	C_P35_CH2_TDN
C_P33_CH3_TDP	AP13	GPHY2_PHY0_CH3_TDP	GPHY2_PHY2_CH3_TDP	AM16	C_P35_CH3_TDP
C_P33_CH3_TDN	AN13	GPHY2_PHY0_CH3_TDN	GPHY2_PHY2_CH3_TDN	AM17	C_P35_CH3_TDN
C_P34_CH0_TDP	AL15	GPHY2_PHY1_CH0_TDP	GPHY2_PHY3_CH0_TDP	AL17	C_P36_CH0_TDP
C_P34_CH0_TDN	AK15	GPHY2_PHY1_CH0_TDN	GPHY2_PHY3_CH0_TDN	AK17	C_P36_CH0_TDN
C_P34_CH1_TDP	AR14	GPHY2_PHY1_CH1_TDP	GPHY2_PHY3_CH1_TDP	AL18	C_P36_CH1_TDP
C_P34_CH1_TDN	AP14	GPHY2_PHY1_CH1_TDN	GPHY2_PHY3_CH1_TDN	AK18	C_P36_CH1_TDN
C_P34_CH2_TDP	AN14	GPHY2_PHY1_CH2_TDP	GPHY2_PHY3_CH2_TDP	AN17	C_P36_CH2_TDP
C_P34_CH2_TDN	AM14	GPHY2_PHY1_CH2_TDN	GPHY2_PHY3_CH2_TDN	AN18	C_P36_CH2_TDN
C_P34_CH3_TDP	AL14	GPHY2_PHY1_CH3_TDP	GPHY2_PHY3_CH3_TDP	AR17	C_P36_CH3_TDP
C_P34_CH3_TDN	AK14	GPHY2_PHY1_CH3_TDN	GPHY2_PHY3_CH3_TDN	AP17	C_P36_CH3_TDN

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G227-10245-01
U1

R1185 499KOHM
R319 6.04KOHM 0.5% AF26
R1186 499KOHM
R320 6.04KOHM 0.5% AD25
R1187 499KOHM
R321 6.04KOHM 0.5% AF30

GPHY3_RDAC
GPHY4_RDAC
GPHY5_RDAC

GPHY3_TVCO AG26
GPHY4_TVCO AD26
GPHY5_TVCO AG30

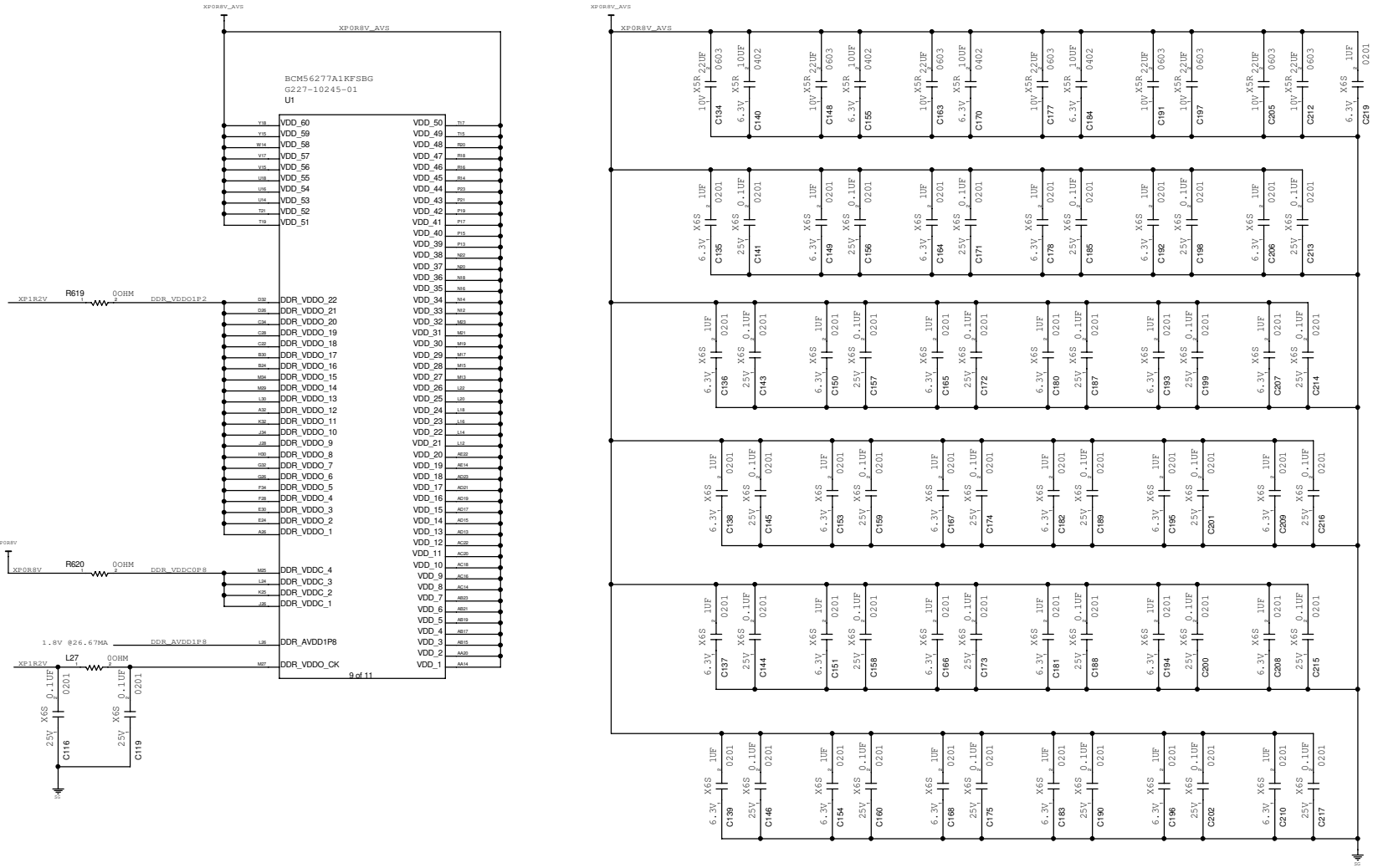
C_P40_CH0_TDP	AR24	GPHY3_PHY0_CH0_TDP	GPHY3_PHY2_CH0_TDP	AN21	C_P38_CH0_TDP
C_P40_CH0_TDN	AP24	GPHY3_PHY0_CH0_TDN	GPHY3_PHY2_CH0_TDN	AM21	C_P38_CH0_TDN
C_P40_CH1_TDP	AN24	GPHY3_PHY0_CH1_TDP	GPHY3_PHY2_CH1_TDP	AR21	C_P38_CH1_TDP
C_P40_CH1_TDN	AM24	GPHY3_PHY0_CH1_TDN	GPHY3_PHY2_CH1_TDN	AP21	C_P38_CH1_TDN
C_P40_CH2_TDP	AM23	GPHY3_PHY0_CH2_TDP	GPHY3_PHY2_CH2_TDP	AP20	C_P38_CH2_TDP
C_P40_CH2_TDN	AL23	GPHY3_PHY0_CH2_TDN	GPHY3_PHY2_CH2_TDN	AN20	C_P38_CH2_TDN
C_P40_CH3_TDP	AP23	GPHY3_PHY0_CH3_TDP	GPHY3_PHY2_CH3_TDP	AM22	C_P38_CH3_TDP
C_P40_CH3_TDN	AN23	GPHY3_PHY0_CH3_TDN	GPHY3_PHY2_CH3_TDN	AM19	C_P38_CH3_TDN
C_P39_CH0_TDP	AL21	GPHY3_PHY1_CH0_TDP	GPHY3_PHY3_CH0_TDP	AR18	C_P37_CH0_TDP
C_P39_CH0_TDN	AK21	GPHY3_PHY1_CH0_TDN	GPHY3_PHY3_CH0_TDN	AP18	C_P37_CH0_TDN
C_P39_CH1_TDP	AR22	GPHY3_PHY1_CH1_TDP	GPHY3_PHY3_CH1_TDP	AL19	C_P37_CH1_TDP
C_P39_CH1_TDN	AP22	GPHY3_PHY1_CH1_TDN	GPHY3_PHY3_CH1_TDN	AK19	C_P37_CH1_TDN
C_P39_CH2_TDP	AN22	GPHY3_PHY1_CH2_TDP	GPHY3_PHY3_CH2_TDP	AN19	C_P37_CH2_TDP
C_P39_CH2_TDN	AM22	GPHY3_PHY1_CH2_TDN	GPHY3_PHY3_CH2_TDN	AM18	C_P37_CH2_TDN
C_P39_CH3_TDP	AL22	GPHY3_PHY1_CH3_TDP	GPHY3_PHY3_CH3_TDP	AR19	C_P37_CH3_TDP
C_P39_CH3_TDN	AK22	GPHY3_PHY1_CH3_TDN	GPHY3_PHY3_CH3_TDN	AP19	C_P37_CH3_TDN
C_P44_CH0_TDP	AR30	GPHY4_PHY0_CH0_TDP	GPHY4_PHY2_CH0_TDP	AR27	C_P42_CH0_TDP
C_P44_CH0_TDN	AP30	GPHY4_PHY0_CH0_TDN	GPHY4_PHY2_CH0_TDN	AP27	C_P42_CH0_TDN
C_P44_CH1_TDP	AJ29	GPHY4_PHY0_CH1_TDP	GPHY4_PHY2_CH1_TDP	AK26	C_P42_CH1_TDP
C_P44_CH1_TDN	AK29	GPHY4_PHY0_CH1_TDN	GPHY4_PHY2_CH1_TDN	AJ26	C_P42_CH1_TDN
C_P44_CH2_TDP	AP29	GPHY4_PHY0_CH2_TDP	GPHY4_PHY2_CH2_TDP	AM26	C_P42_CH2_TDP
C_P44_CH2_TDN	AN29	GPHY4_PHY0_CH2_TDN	GPHY4_PHY2_CH2_TDN	AL26	C_P42_CH2_TDN
C_P44_CH3_TDP	AL28	GPHY4_PHY0_CH3_TDP	GPHY4_PHY2_CH3_TDP	AP28	C_P42_CH3_TDP
C_P44_CH3_TDN	AK28	GPHY4_PHY0_CH3_TDN	GPHY4_PHY2_CH3_TDN	AN28	C_P42_CH3_TDN
C_P43_CH0_TDP	AN27	GPHY4_PHY1_CH0_TDP	GPHY4_PHY3_CH0_TDP	AK24	C_P41_CH0_TDP
C_P43_CH0_TDN	AM27	GPHY4_PHY1_CH0_TDN	GPHY4_PHY3_CH0_TDN	AL24	C_P41_CH0_TDN
C_P43_CH1_TDP	AL27	GPHY4_PHY1_CH1_TDP	GPHY4_PHY3_CH1_TDP	AP25	C_P41_CH1_TDP
C_P43_CH1_TDN	AK27	GPHY4_PHY1_CH1_TDN	GPHY4_PHY3_CH1_TDN	AR25	C_P41_CH1_TDN
C_P43_CH2_TDP	AR28	GPHY4_PHY1_CH2_TDP	GPHY4_PHY3_CH2_TDP	AK25	C_P41_CH2_TDP
C_P43_CH2_TDN	AP28	GPHY4_PHY1_CH2_TDN	GPHY4_PHY3_CH2_TDN	AL25	C_P41_CH2_TDN
C_P43_CH3_TDP	AN28	GPHY4_PHY1_CH3_TDP	GPHY4_PHY3_CH3_TDP	AH25	C_P41_CH3_TDP
C_P43_CH3_TDN	AM28	GPHY4_PHY1_CH3_TDN	GPHY4_PHY3_CH3_TDN	AJ25	C_P41_CH3_TDN
C_P48_CH0_TDP	AL35	GPHY5_PHY0_CH0_TDP	GPHY5_PHY2_CH0_TDP	AR33	C_P46_CH0_TDP
C_P48_CH0_TDN	AK35	GPHY5_PHY0_CH0_TDN	GPHY5_PHY2_CH0_TDN	AP33	C_P46_CH0_TDN
C_P48_CH1_TDP	AL34	GPHY5_PHY0_CH1_TDP	GPHY5_PHY2_CH1_TDP	AK32	C_P46_CH1_TDP
C_P48_CH1_TDN	AK34	GPHY5_PHY0_CH1_TDN	GPHY5_PHY2_CH1_TDN	AJ32	C_P46_CH1_TDN
C_P48_CH2_TDP	AN35	GPHY5_PHY0_CH2_TDP	GPHY5_PHY2_CH2_TDP	AP32	C_P46_CH2_TDP
C_P48_CH2_TDN	AM35	GPHY5_PHY0_CH2_TDN	GPHY5_PHY2_CH2_TDN	AN32	C_P46_CH2_TDN
C_P48_CH3_TDP	AR35	GPHY5_PHY0_CH3_TDP	GPHY5_PHY2_CH3_TDP	AL31	C_P46_CH3_TDP
C_P48_CH3_TDN	AP35	GPHY5_PHY0_CH3_TDN	GPHY5_PHY2_CH3_TDN	AK31	C_P46_CH3_TDN
C_P47_CH0_TDP	AL33	GPHY5_PHY1_CH0_TDP	GPHY5_PHY3_CH0_TDP	AN30	C_P45_CH0_TDP
C_P47_CH0_TDN	AK33	GPHY5_PHY1_CH0_TDN	GPHY5_PHY3_CH0_TDN	AM30	C_P45_CH0_TDN
C_P47_CH1_TDP	AN33	GPHY5_PHY1_CH1_TDP	GPHY5_PHY3_CH1_TDP	AL30	C_P45_CH1_TDP
C_P47_CH1_TDN	AM33	GPHY5_PHY1_CH1_TDN	GPHY5_PHY3_CH1_TDN	AK30	C_P45_CH1_TDN
C_P47_CH2_TDP	AN34	GPHY5_PHY1_CH2_TDP	GPHY5_PHY3_CH2_TDP	AR31	C_P45_CH2_TDP
C_P47_CH2_TDN	AM34	GPHY5_PHY1_CH2_TDN	GPHY5_PHY3_CH2_TDN	AP31	C_P45_CH2_TDN
C_P47_CH3_TDP	AR34	GPHY5_PHY1_CH3_TDP	GPHY5_PHY3_CH3_TDP	AN31	C_P45_CH3_TDP
C_P47_CH3_TDN	AP34	GPHY5_PHY1_CH3_TDN	GPHY5_PHY3_CH3_TDN	AM31	C_P45_CH3_TDN

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Document Number:	Document Version:
Assembly P/N:	Date:
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CELESTICA - PROPRIETARY
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Assembly P/N:	Date:
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USE PURSUANT TO COMPANY INSTRUCTIONS

BCM56277A1KFSBG
G227-10245-01
U1

0.8V @201.6MA	TSC_IPROC_RTVDD_0P8	E18	TSC_IPROC_RTVDD_0P8_4	TSC3_RTVDD0P8_4	P35	TSC3_RTVDD0P8 0.8V @201.6MA
		D18	TSC_IPROC_RTVDD_0P8_3	TSC3_RTVDD0P8_3	P34	
		B18	TSC_IPROC_RTVDD_0P8_2	TSC3_RTVDD0P8_2	P30	
		A18	TSC_IPROC_RTVDD_0P8_1	TSC3_RTVDD0P8_1	P29	
0.8V @42MA	TSC_IPROC_PVDD_0P8	GB1	TSC_IPROC_PVDD_0P8	TSC3_PVDD0P8	P25	TSC3_PVDD0P8 0.8V @42MA
0.8V @42MA	PCIE_RTVDD_0P8	F13	PCIE_RTVDD_0P8_2	TSC2_TVDD1P2_2	AB27	TSC2_TVDD1P2 1.2V @84MA
		C13	PCIE_RTVDD_0P8_1	TSC2_TVDD1P2_1	AB26	
0.8V @201.6MA	PCIE_PVDD_0P8	G15	PCIE_PVDD_0P8	TSC2_RTVDD0P8_5	Y25	TSC2_RTVDD0P8 0.8V @742MA
				TSC2_RTVDD0P8_4	W25	
				TSC2_RTVDD0P8_3	V25	
				TSC2_RTVDD0P8_2	AA33	
				TSC2_RTVDD0P8_1	AA32	
				TSC2_PVDD1P8	AC28	TSC2_PVDD1P8 1.8V @0.7MA
				TSC2_PVDD0P8	AC29	TSC2_PVDD0P8 0.8V @137.2MA
1.8V @70MA	XG_SERDES_PLL_AVDD1P8	T2	XG_SERDES_PLL_AVDD1P8_2	TSC0_1_RTVDD0P8_7	W5	TSC0_1_RTVDD0P8 0.8V @403.2MA
		R2	XG_SERDES_PLL_AVDD1P8_1	TSC0_1_RTVDD0P8_6	V2	
				TSC0_1_RTVDD0P8_5	V1	
1.8V @70MA	XG_MASTER_PLL_AVDD1P8	AG34	XG_MASTER_PLL_AVDD1P8_2	TSC0_1_RTVDD0P8_4	AE4	
		AF34	XG_MASTER_PLL_AVDD1P8_1	TSC0_1_RTVDD0P8_3	AE3	
				TSC0_1_RTVDD0P8_2	AC5	
1.8V @50MA	CORE_PLL_AVDD1P8	Y17	CORE_PLL_AVDD1P8	TSC0_1_RTVDD0P8_1	AA5	
1.8V @50MA	IPROC_PLL_AVDD1P8	AC29	IPROC_PLL_AVDD1P8	TSC1_PVDD0P8	AB11	TSC1_PVDD0P8 0.8V @42MA
1.8V @70MA	IHOST_PLL_VDD1P8	R32	IHOST_PLL_AVDD1P8	TSC0_PVDD0P8	T11	TSC0_PVDD0P8 0.8V @42MA
1.8V @50MA	TS_PLL_AVDD1P8	Y23	TS_PLL_AVDD1P8	GPHY5_PLL_AVDD1P8	AE29	GPHY5_PLL_AVDD1P8 1.8V @12.44MA
				GPHY4_PLL_AVDD1P8	AE27	GPHY4_PLL_AVDD1P8 1.8V @12.44MA
VOLTAGE=3V				GPHY3_PLL_AVDD1P8	AC28	GPHY3_PLL_AVDD1P8 1.8V @12.44MA
XP5R0V				GPHY2_PLL_AVDD1P8	AC6	GPHY2_PLL_AVDD1P8 1.8V @12.44MA
3.3V @32MA	USB2_VDD33	G18	USB2_AVDD3P3	GPHY1_PLL_AVDD1P8	AE6	GPHY1_PLL_AVDD1P8 1.8V @12.44MA
XP5R0V R322	±20KOHM R1151	±10KOHM J20	USB2_VBUS0	GPHY0_PLL_AVDD1P8	AE7	GPHY0_PLL_AVDD1P8 1.8V @12.44MA
	XP3R3V_MDIO_01	J15	MDIO_1_VDDO			
		H13	MDIO_0_VDDO			
0.8V @3.6MA	RESCAL_VDD0P8	W20	RESCAL_2_VDD0P8	GPHY5_PLL_AVDD0P85	AG29	GPHY5_PLL_AVDD0P85 0.8V @29.75MA
		N7	RESCAL_1_VDD0P8	GPHY4_PLL_AVDD0P85	AD27	GPHY4_PLL_AVDD0P85 0.8V @29.75MA
		G17	RESCAL_0_VDD0P8	GPHY3_PLL_AVDD0P85	AF27	GPHY3_PLL_AVDD0P85 0.8V @29.75MA
				GPHY2_PLL_AVDD0P85	AF9	GPHY2_PLL_AVDD0P85 0.8V @29.75MA
1.8V @2.4MA	PVTMON_AVDD1P8	AC3	PVTMON5_AVDD1P8	GPHY1_PLL_AVDD0P85	AD9	GPHY1_PLL_AVDD0P85 0.8V @29.75MA
		N6	PVTMON4_AVDD1P8	GPHY0_PLL_AVDD0P85	AG6	GPHY0_PLL_AVDD0P85 0.8V @29.75MA
		AA19	PVTMON3_AVDD1P8			
		P9	PVTMON1_AVDD1P8	GPHY5_BIAS_AVDD1P8	AF29	GPHY5_BIAS_AVDD1P8 1.8V @12.44MA
		G16	PVTMON0_AVDD1P8	GPHY4_BIAS_AVDD1P8	AE26	GPHY4_BIAS_AVDD1P8 1.8V @12.44MA
XP1R8V				GPHY3_BIAS_AVDD1P8	AG27	GPHY3_BIAS_AVDD1P8 1.8V @12.44MA
				GPHY2_BIAS_AVDD1P8	AG9	GPHY2_BIAS_AVDD1P8 1.8V @12.44MA
XP1R8V		U20	VDDO1P8_2	GPHY1_BIAS_AVDD1P8	AE9	GPHY1_BIAS_AVDD1P8 1.8V @12.44MA
		T23	VDDO1P8_1	GPHY0_BIAS_AVDD1P8	AF7	GPHY0_BIAS_AVDD1P8 1.8V @12.44MA
				GPHY_AVDD3P3_1_4	AM35	GPHY_AVDD3P3_1 3.3V @833.64MA
XP3R3V_DUT		AF24	VDDO33_1_5	GPHY_AVDD3P3_1_3	AL32	
		AF12	VDDO33_1_4	GPHY_AVDD3P3_1_2	AL29	
		AE23	VDDO33_1_3	GPHY_AVDD3P3_1_1	AK29	
		AE18	VDDO33_1_2	GPHY_AVDD3P3_0_4	AM11	GPHY_AVDD3P3_0 3.3V @833.64MA
		AE13	VDDO33_1_1	GPHY_AVDD3P3_0_3	AL7	
		N2	VDDO33_0_15	GPHY_AVDD3P3_0_2	AL4	
		M9	VDDO33_0_14	GPHY_AVDD3P3_0_1	AK16	
		M7	VDDO33_0_13	GPHY_AVDD1P8_1_4	AJ35	GPHY_AVDD1P8_1 1.8V @287.01MA
		L5	VDDO33_0_12	GPHY_AVDD1P8_1_3	AJ34	
		K10	VDDO33_0_11	GPHY_AVDD1P8_1_2	AJ33	
		J3	VDDO33_0_10	GPHY_AVDD1P8_1_1	AJ31	
		H24	VDDO33_0_9	GPHY_AVDD1P8_0_4	AJ5	GPHY_AVDD1P8_0 1.8V @287.01MA
		H9	VDDO33_0_8	GPHY_AVDD1P8_0_3	AJ3	
		G1	VDDO33_0_7	GPHY_AVDD1P8_0_2	AJ2	
		F8	VDDO33_0_6	GPHY_AVDD1P8_0_1	AJ1	
		E11	VDDO33_0_5			
		D4	VDDO33_0_4	GPHY_AVDD0P85_1_4	AH30	GPHY_AVDD0P85_1 0.8V @6.3MA
		C9	VDDO33_0_3	GPHY_AVDD0P85_1_3	AH29	
		B2	VDDO33_0_2	GPHY_AVDD0P85_1_2	AH28	
		A7	VDDO33_0_1	GPHY_AVDD0P85_1_1	AH27	
				GPHY_AVDD0P85_0_4	AH9	GPHY_AVDD0P85_0 0.8V @6.3MA
XP0R8V_AVS_VSP		V18	VDD_SENSE	GPHY_AVDD0P85_0_3	AH8	
XP0R8V_AVS_VSN		V19	GND_SENSE	GPHY_AVDD0P85_0_2	AH7	
				GPHY_AVDD0P85_0_1	AH6	

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L39 330OHM XP1R8V GPHY_AVDD1P8_0 0603 1.7A
C317 0.1UF 25V X6S 0201
C319 47UF 6.3V X5R 0805
C321 0.1UF 25V X6S 0201
C323 0.1UF 25V X6S 0201
C325 0.1UF 25V X6S 0201
C118 1UF 25V X6S 0201
C327 0.1UF 25V X6S 0201
C123 1UF 6.3V X6S 0201

L41 330OHM XP1R8V GPHY_AVDD1P8_1 0603 1.7A
C329 0.1UF 25V X6S 0201
C331 47UF 6.3V X5R 0805
C333 0.1UF 25V X6S 0201
C335 0.1UF 25V X6S 0201
C337 0.1UF 25V X6S 0201
C339 1UF 6.3V X6S 0201

L40 26OHM XP3R3V_DUT GPHY_AVDD3P3_0 0603 6A
C318 0.1UF 25V X6S 0201
C320 100UF 6.3V X5R 1206
C322 1UF 6.3V X5R 0402
C117 0.1UF 25V X6S 0201
C324 0.1UF 25V X6S 0201
C326 0.1UF 25V X6S 0201
C1071 1UF 6.3V X6S 0201
C328 0.1UF 25V X6S 0201
C1072 1UF 6.3V X6S 0201
C120 0.1UF 25V X6S 0201
C1073 1UF 6.3V X6S 0201

L42 26OHM XP3R3V_DUT GPHY_AVDD3P3_1 0603 6A
C330 0.1UF 25V X6S 0201
C332 100UF 6.3V X5R 1206
C121 1UF 6.3V X5R 0402
C334 0.1UF 25V X6S 0201
C122 0.1UF 25V X6S 0201
C1074 1UF 6.3V X6S 0201
C336 0.1UF 25V X6S 0201
C338 0.1UF 25V X6S 0201
C1075 1UF 6.3V X6S 0201
C340 0.1UF 25V X6S 0201
C1076 1UF 6.3V X6S 0201

L43 600OHM XP1R8V GPHY_PLL_AVDD1P8 0603 1.3A
C343 0.1UF 25V X6S 0201
C353 10UF 6.3V X5R 0402
C361 0.1UF 25V X6S 0201

L44 600OHM XP1R8V GPHY_PLL_AVDD1P8 0603 1.3A
C344 0.1UF 25V X6S 0201
C354 10UF 6.3V X5R 0402
C362 0.1UF 25V X6S 0201

L45 600OHM XP1R8V GPHY_BIAS_AVDD1P8 0603 1.3A
C345 0.1UF 25V X6S 0201
C355 10UF 6.3V X5R 0402
C363 0.1UF 25V X6S 0201

L46 600OHM XP1R8V GPHY_BIAS_AVDD1P8 0603 1.3A
C346 0.1UF 25V X6S 0201
C356 10UF 6.3V X5R 0402
C364 0.1UF 25V X6S 0201

L47 600OHM XP0R8V zGPHY0_PLL_AVDD0P85 0603 1.3A
C347 0.1UF 25V X6S 0201
C357 10UF 6.3V X5R 0402
C365 0.1UF 25V X6S 0201

L48 600OHM XP0R8V zGPHY3_PLL_AVDD0P85 0603 1.3A
C348 0.1UF 25V X6S 0201
C358 10UF 6.3V X5R 0402
C366 0.1UF 25V X6S 0201

L49 600OHM XP0R8V GPHY_AVDD0P85_0 0603 1.3A
C349 0.1UF 25V X6S 0201
C359 10UF 6.3V X5R 0402
C524 0.1UF 25V X6S 0201
C367 1UF 6.3V X6S 0201
C1125 0.1UF 25V X6S 0201
C528 0.1UF 25V X6S 0201
C527 0.1UF 25V X6S 0201

L50 600OHM XP1R8V GPHY_PLL_AVDD1P8 0603 1.3A
C370 0.1UF 25V X6S 0201
C380 10UF 6.3V X5R 0402
C388 0.1UF 25V X6S 0201

L51 600OHM XP1R8V GPHY_PLL_AVDD1P8 0603 1.3A
C371 0.1UF 25V X6S 0201
C381 10UF 6.3V X5R 0402
C389 0.1UF 25V X6S 0201

L52 600OHM XP1R8V GPHY_BIAS_AVDD1P8 0603 1.3A
C372 0.1UF 25V X6S 0201
C382 10UF 6.3V X5R 0402
C390 0.1UF 25V X6S 0201

L53 600OHM XP1R8V zGPHY4_BIAS_AVDD1P8 0603 1.3A
C373 0.1UF 25V X6S 0201
C383 10UF 6.3V X5R 0402
C391 0.1UF 25V X6S 0201

L54 600OHM XP0R8V zGPHY1_PLL_AVDD0P85 0603 1.3A
C374 0.1UF 25V X6S 0201
C384 10UF 6.3V X5R 0402
C392 0.1UF 25V X6S 0201

L55 600OHM XP0R8V zGPHY4_PLL_AVDD0P85 0603 1.3A
C375 0.1UF 25V X6S 0201
C385 10UF 6.3V X5R 0402
C393 0.1UF 25V X6S 0201

L56 600OHM XP0R8V GPHY_AVDD0P85_1 0603 1.3A
C376 0.1UF 25V X6S 0201
C386 10UF 6.3V X5R 0402
C394 0.1UF 25V X6S 0201
C128 1UF 25V X6S 0201
C129 0.1UF 25V X6S 0201
C130 0.1UF 25V X6S 0201
C131 1UF 25V X6S 0201

L57 600OHM XP1R8V GPHY_PLL_AVDD1P8 0603 1.3A
C397 0.1UF 25V X6S 0201
C403 10UF 6.3V X5R 0402
C409 0.1UF 25V X6S 0201

L58 600OHM XP1R8V GPHY_PLL_AVDD1P8 0603 1.3A
C398 0.1UF 25V X6S 0201
C404 10UF 6.3V X5R 0402
C410 0.1UF 25V X6S 0201

L59 600OHM XP1R8V GPHY_BIAS_AVDD1P8 0603 1.3A
C399 0.1UF 25V X6S 0201
C405 10UF 6.3V X5R 0402
C411 0.1UF 25V X6S 0201

L60 600OHM XP1R8V zGPHY5_BIAS_AVDD1P8 0603 1.3A
C400 0.1UF 25V X6S 0201
C406 10UF 6.3V X5R 0402
C412 0.1UF 25V X6S 0201

L61 600OHM XP0R8V zGPHY2_PLL_AVDD0P85 0603 1.3A
C401 0.1UF 25V X6S 0201
C407 10UF 6.3V X5R 0402
C413 0.1UF 25V X6S 0201

L62 600OHM XP0R8V zGPHY5_PLL_AVDD0P85 0603 1.3A
C402 0.1UF 25V X6S 0201
C408 10UF 6.3V X5R 0402
C414 0.1UF 25V X6S 0201

XP3R3V_DUT
C341 1UF 6.3V X6S 0201
C342 0.1UF 25V X6S 0201
C350 6800PF 25V X7R 0201
C351 1UF 6.3V X6S 0201
C352 0.1UF 25V X6S 0201
C360 6800PF 25V X7R 0201
C368 1UF 6.3V X6S 0201
C369 0.1UF 25V X6S 0201
C377 6800PF 25V X7R 0201
C378 1UF 6.3V X6S 0201
C379 0.1UF 25V X6S 0201
C387 6800PF 25V X7R 0201
C395 1UF 6.3V X6S 0201
C396 0.1UF 25V X6S 0201

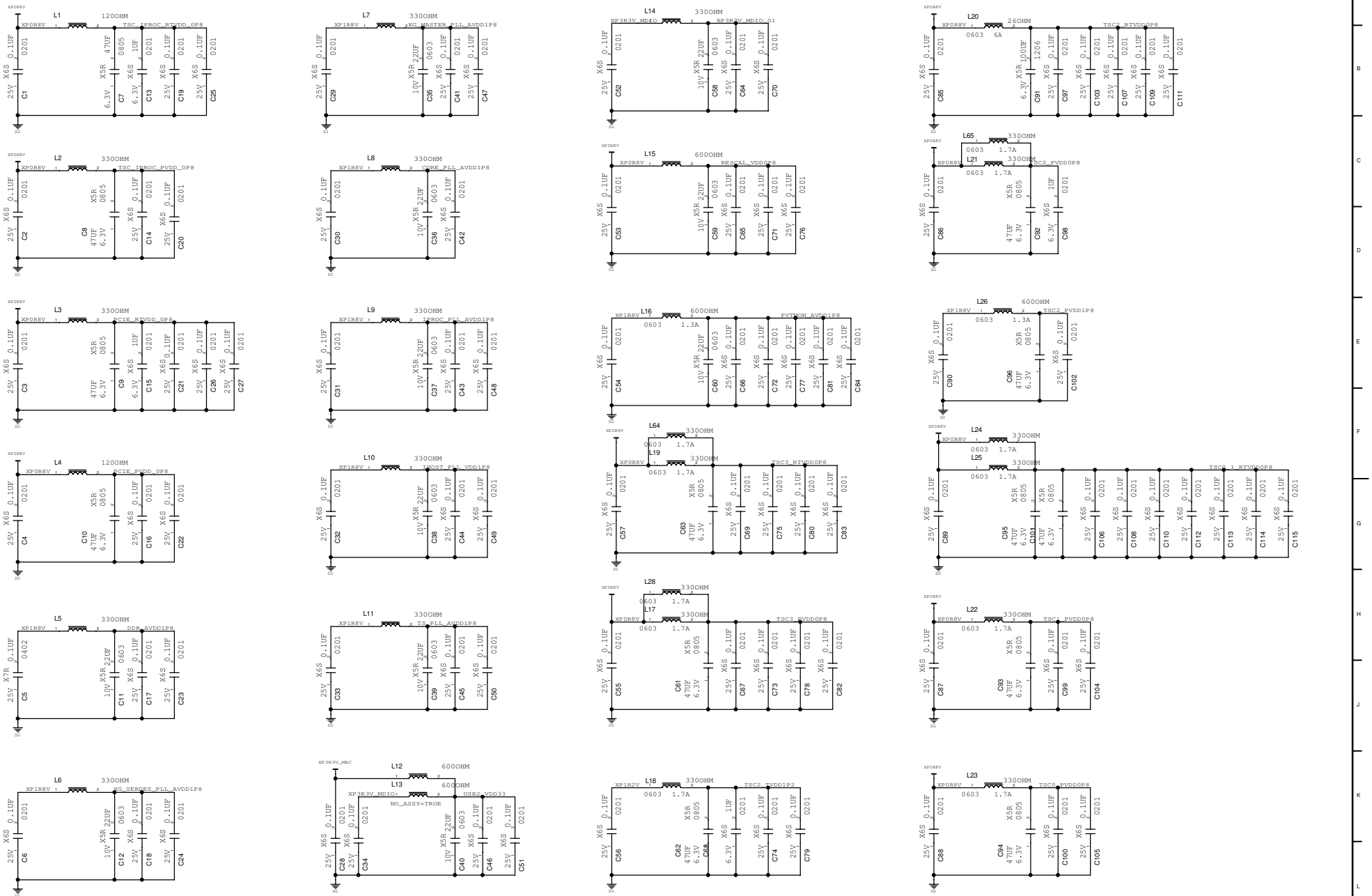
XP3R3V_MAC
L90 26OHM 0603 6A

BCM56277_POWER

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BCM56277A1KFSBG
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U1

A12	GND_1	GND_102	AH26
A14	GND_2	GND_103	AJ6
A16	GND_3	GND_104	AJ9
A20	GND_4	GND_105	AJ12
AA3	GND_5	GND_106	AJ27
AA4	GND_6	GND_107	AJ28
AA8	GND_7	GND_108	AJ30
AA9	GND_8	GND_109	AK13
AA10	GND_9	GND_110	AK23
AA13	GND_10	GND_111	AL16
AA22	GND_11	GND_112	AL20
AA23	GND_12	GND_113	AM4
AA24	GND_13	GND_114	AM7
AA25	GND_14	GND_115	AM29
AA26	GND_15	GND_116	AM30
AA27	GND_16	GND_117	AN11
AA28	GND_17	GND_118	AN25
AA31	GND_18	GND_119	B12
AB1	GND_19	GND_120	B14
AB2	GND_20	GND_121	B16
AB5	GND_21	GND_122	B20
AB6	GND_22	GND_123	C12
AB7	GND_23	GND_124	C14
AB10	GND_24	GND_125	C16
AB12	GND_25	GND_126	C17
AB13	GND_26	GND_127	C18
AB24	GND_27	GND_128	C19
AB25	GND_28	GND_129	C20
AB28	GND_29	GND_130	D12
AB29	GND_30	GND_131	D14
AB30	GND_31	GND_132	D16
AB31	GND_32	GND_133	D20
AB32	GND_33	GND_134	D21
AB33	GND_34	GND_135	D22
AB34	GND_35	GND_136	E12
AB35	GND_36	GND_137	E14
AC3	GND_37	GND_138	E16
AC4	GND_38	GND_139	E20
AC8	GND_39	GND_140	E22
AC9	GND_40	GND_141	F12
AC10	GND_41	GND_142	F14
AC11	GND_42	GND_143	F15
AC12	GND_43	GND_144	F16
AC13	GND_44	GND_145	F17
AC24	GND_45	GND_146	F18
AC25	GND_46	GND_147	F19
AC26	GND_47	GND_148	F20
AC27	GND_48	GND_149	F22
AC28	GND_49	GND_150	G12
AC32	GND_50	GND_151	G14
AD1	GND_51	GND_152	G20
AD2	GND_52	GND_153	G22
AD5	GND_53	GND_154	G23
AD6	GND_54	GND_155	H15
AD7	GND_55	GND_156	H16
AD8	GND_56	GND_157	H20
AD12	GND_57	GND_158	H21
AD24	GND_58	GND_159	H22
AD28	GND_59	GND_160	J16
AD29	GND_60	GND_161	J17
AD32	GND_61	GND_162	J18
AE5	GND_62	GND_163	J21
AE6	GND_63	GND_164	K19
AE10	GND_64	GND_165	K21
AE11	GND_65	GND_166	N24
AE12	GND_66	GND_167	N25
AE24	GND_67	GND_168	N26
AE25	GND_68	GND_169	N27
AE29	GND_69	GND_170	N28
AE30	GND_70	GND_171	N29
AE31	GND_71	GND_172	N30
AE32	GND_72	GND_173	N31
AE33	GND_73	GND_174	N32
AE34	GND_74	GND_175	N33
AE35	GND_75	GND_176	N34
AF1	GND_76	GND_177	N35
AF2	GND_77	GND_178	P1
AF3	GND_78	GND_179	P2
AF4	GND_79	GND_180	P3
AF5	GND_80	GND_181	P4
AF6	GND_81	GND_182	P5
AF11	GND_82	GND_183	P24
AF25	GND_83	GND_184	P26
AF28	GND_84	GND_185	P31
AF31	GND_85	GND_186	R5
AG4	GND_86	GND_187	R7
AG7	GND_87	GND_188	R8
AG11	GND_88	GND_189	R9
AG25	GND_89	GND_190	R10
AG31	GND_90	GND_191	R11
AH1	GND_91	GND_192	R12
AH5	GND_92	GND_193	R24
AH6	GND_93	GND_194	R26
AH8	GND_94	GND_195	R27
AH9	GND_95	GND_196	R28
AH10	GND_96	GND_197	R31
AH26	GND_97	GND_198	R32
AH31	GND_98	GND_199	R33
AH32	GND_99	GND_200	T5
AH33	GND_100	GND_201	T6
AH34	GND_101	GND_202	T7

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T10	GND_203	GND_304	AJ24
T12	GND_204	GND_305	B8
T13	GND_205	GND_306	B21
T24	GND_206	GND_307	B27
T26	GND_207	GND_308	B33
T29	GND_208	GND_309	C3
T30	GND_209	GND_310	C21
T31	GND_210	GND_311	C25
T34	GND_211	GND_312	C31
T35	GND_212	GND_313	D10
U1	GND_213	GND_314	D33
U2	GND_214	GND_315	D28
U3	GND_215	GND_316	D29
U4	GND_216	GND_317	D35
U5	GND_217	GND_318	E5
U8	GND_218	GND_319	E27
U9	GND_219	GND_320	E33
U10	GND_220	GND_321	F24
U13	GND_221	GND_322	F25
U22	GND_222	GND_323	F31
U23	GND_223	GND_324	G7
U24	GND_224	GND_325	G12
U25	GND_225	GND_326	G25
U26	GND_226	GND_327	G27
U27	GND_227	GND_328	G29
U29	GND_228	GND_329	G35
U31	GND_229	GND_330	H2
U32	GND_230	GND_331	H14
U33	GND_231	GND_332	H25
V5	GND_232	GND_333	H26
V6	GND_233	GND_334	H27
V7	GND_234	GND_335	H29
V10	GND_235	GND_336	H34
V13	GND_236	GND_337	J9
V20	GND_237	GND_338	J24
V21	GND_238	GND_339	J25
V24	GND_239	GND_340	J27
V28	GND_240	GND_341	J31
V29	GND_241	GND_342	K4
V30	GND_242	GND_343	K18
V31	GND_243	GND_344	K24
V34	GND_244	GND_345	K26
V35	GND_245	GND_346	K29
W3	GND_246	GND_347	K35
W4	GND_247	GND_348	L13
W8	GND_248	GND_349	L15
W9	GND_249	GND_350	L17
W10	GND_250	GND_351	L19
W11	GND_251	GND_352	L21
W12	GND_252	GND_353	L23
W13	GND_253	GND_354	L25
W21	GND_254	GND_355	L27
W24	GND_255	GND_356	L29
W26	GND_256	GND_357	L33
W27	GND_257	GND_358	M6
W28	GND_258	GND_359	M8
W31	GND_259	GND_360	M12
W32	GND_260	GND_361	M14
W33	GND_261	GND_362	M16
Y1	GND_262	GND_363	M18
Y2	GND_263	GND_364	M20
Y5	GND_264	GND_365	M22
Y6	GND_265	GND_366	M24
Y7	GND_266	GND_367	M26
Y10	GND_267	GND_368	M31
Y13	GND_268	GND_369	N1
Y21	GND_269	GND_370	N5
Y22	GND_270	GND_371	N9
Y24	GND_271	GND_372	N13
Y28	GND_272	GND_373	N15
Y29	GND_273	GND_374	N17
Y30	GND_274	GND_375	N19
Y31	GND_275	GND_376	N21
Y34	GND_276	GND_377	N23
Y35	GND_277	GND_378	P12
A1	GND_278	GND_379	P14
A33	GND_279	GND_380	P16
A39	GND_280	GND_381	P18
A35	GND_281	GND_382	P20
AA15	GND_282	GND_383	P22
AA18	GND_283	GND_384	R13
AA21	GND_284	GND_385	R15
AB16	GND_285	GND_386	R17
AB18	GND_286	GND_387	R19
AB20	GND_287	GND_388	R21
AB22	GND_288	GND_389	R23
AC15	GND_289	GND_390	T14
AC17	GND_290	GND_391	T16
AC19	GND_291	GND_392	T18
AC21	GND_292	GND_393	T20
AC23	GND_293	GND_394	T22
AD14	GND_294	GND_395	U15
AD16	GND_295	GND_396	U17
AD18	GND_296	GND_397	U19
AD20	GND_297	GND_398	U21
AD22	GND_298	GND_399	V14
AG12	GND_299	GND_400	V16
AG24	GND_300	GND_401	W15
AH12	GND_301	GND_402	W18
AH24	GND_302	GND_403	Y14
AH28	GND_303	GND_404	Y16

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QSGMII B50282 PHY 1

VOLTAGE=3.3 XF3R3V
MIIM0 PHY_ADDRESS:00001

B50282C1KFBG
G227-10066-01
U8
B50282C1KFBG

PHYA_4 PHYA_3 PHYA_2 PHYA_1 PHYA_0 PHYA_REV

PHY1_QSIN_1_P C652 ≠0.1UF 25V 0201 X6S RDP1
PHY1_QSIN_1_N C653 ≠0.1UF 25V 0201 X6S RDN1
PHY1_QSIN_0_P C654 ≠0.1UF 25V 0201 X6S RDP0
PHY1_QSIN_0_N C655 ≠0.1UF 25V 0201 X6S RDN0

LED7_2* LED7_1* LED7_0*
LED6_2* LED6_1* LED6_0*
LED5_2* LED5_1* LED5_0*
LED4_2* LED4_1* LED4_0*
LED3_2* LED3_1* LED3_0*
LED2_2* LED2_1* LED2_0*
LED1_2* LED1_1* LED1_0*
LED0_3* LED0_2* LED0_1* LED0_0*

B50282_1_CRNT_LED_EN → CRNT_LED_EN
B50282_1_FDX → FDX
B50282_1_ANEN → ANEN
B50282_1_SPD0 → SPD0
B50282_1_SUPER_I → SUPER_I
B50282_1_HUB → HUB

TVCO_2 TVCO_1
TEST_1 TEST_0

SCLK_TYPE_1 SCLK_TYPE_0
SCLK_FREQ_1 SCLK_FREQ_0
MDIO_LVL MDIO_SEL_2 MDIO_SEL_1
MDIO_2 MDC_2
MDIO_1 MDC_1
REFCLKP REFCLKN
RESET*

SCLK_TYPE[1:0] = 01 SINGLE-END CMOS
00 CML DIFFERENTIAL CLOCK
10 CRYSTAL
11 NA
SCLK_FREQ[1:0] = 00 25MHZ
01 125MHZ
10 312.5MHZ
11 156.25MHZ

MDIO_LVL = 1 OVDDMDIO OPERATE AT 2.5V OR 3.3V
0 OVDDMDIO OPERATE AT 1.2V

B50282_1_REFCLK_P B50282_1_REFCLK_N B50282_1_RST_N

TDP_7_3 TDN_7_3 TDP_7_2 TDN_7_2 TDP_7_1 TDN_7_1 TDP_7_0 TDN_7_0
TDP_6_3 TDN_6_3 TDP_6_2 TDN_6_2 TDP_6_1 TDN_6_1 TDP_6_0 TDN_6_0
TDP_5_3 TDN_5_3 TDP_5_2 TDN_5_2 TDP_5_1 TDN_5_1 TDP_5_0 TDN_5_0
TDP_4_3 TDN_4_3 TDP_4_2 TDN_4_2 TDP_4_1 TDN_4_1 TDP_4_0 TDN_4_0
TDP_3_3 TDN_3_3 TDP_3_2 TDN_3_2 TDP_3_1 TDN_3_1 TDP_3_0 TDN_3_0
TDP_2_3 TDN_2_3 TDP_2_2 TDN_2_2 TDP_2_1 TDN_2_1 TDP_2_0 TDN_2_0
TDP_1_3 TDN_1_3 TDP_1_2 TDN_1_2 TDP_1_1 TDN_1_1 TDP_1_0 TDN_1_0
TDP_0_3 TDN_0_3 TDP_0_2 TDN_0_2 TDP_0_1 TDN_0_1 TDP_0_0 TDN_0_0

TDP1 TDN1 TDP0 TDN0 → PHY1_QSOUT_1_P, PHY1_QSOUT_1_N, PHY1_QSOUT_0_P, PHY1_QSOUT_0_N

LOC_REC_CLK_2 LOC_REC_CLK_1
REC_CLK_2 REC_CLK_1
TDI TDO TMS TCK TRST → PHY1_TDI, PHY1_TDO, PHY1_TMS, PHY1_TCK, PHY1_TRST
INTERRUPT* → B50282_1_INT_N
RDAC_2 RDAC_1

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B50282C1KFBG
G227-10066-01
U8
B50282C1KFBG

CLKVDD BIASVDD_1 BIASVDD_2 OVDDMDIO OVDDJTAG_1 OVDDJTAG_2
AVDDH_1 AVDDH_2 AVDDH_3 AVDDH_4 AVDDH_5 AVDDH_6 AVDDH_7 AVDDH_8
OVDD_1 OVDD_2 OVDD_3 OVDD_4 OVDD_5 OVDD_6 OVDD_7 OVDD_8
QTVDD_1 QTVDD_2 QTVDD_3 QTVDD_4
QRVDD_1 QRVDD_2 QRVDD_3 QRVDD_4
QPVDD_1 QPVDD_2
DVDD_1 DVDD_2 DVDD_3 DVDD_4 DVDD_5 DVDD_6 DVDD_7
AVDDL_1 AVDDL_2 AVDDL_3 AVDDL_4 AVDDL_5 AVDDL_6
PLLVDD_1 PLLVDD_2

GND_1 … GND_51

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L83 1.3A 0.10HM ≠600OHM
L81 1.3A 0.10HM ≠600OHM
L80 1.3A 0.10HM ≠600OHM
L82 1.3A 0.10HM ≠600OHM

C658 C665 C672 C679 C685 C690 C695 C699 0.1UF 25V X7R 0402; 10UF 6.3V X5R 0402
C659 C666 C673 0.1UF 25V X7R 0402
C660 C667 C674 C680 C686 C691 C696 C700 0.1UF 25V X7R 0402
C661 C668 C676 C681 C687 0.1UF 25V X7R 0402; 10UF 6.3V X5R 0402
C662 1UF 25V X6S 0402; C669 10UF 6.3V X5R 0402
C657 C664 C671 C678 C684 C689 C692 0.1UF 25V X7R 0402
C656 C663 C670 C677 C683 C688 C693 0.1UF 25V X7R 0402; C697 10UF 6.3V X5R 0402
C675 10UF 6.3V X5R 0402; C682 22UF 10V X5R 0603
C694 10UF 6.3V X5R 0402; C698 22UF 10V X5R 0603

VOLTAGE=3.3 XF3R3V
VOLTAGE=1.0V XF1R0V

R720 R721 R722 R723 R724 R733 4.7KOHM
R744 R746 R748 R750 R752 R754 4.7KOHM NO_ASSY=TRUE
R745 R747 R749 R751 R753 R755 1KOHM NO_ASSY=TRUE
R726 R727 R728 R729 R730 R731 R732 ≠4.7KOHM NO_ASSY=TRUE
R734 R735 ≠33OHM
R725 ≠4.7KOHM
R736 R737 R739 R741 R743 4.7KOHM NO_ASSY=TRUE
R738 6.04KOHM
R178 499OHM
R740 6.04KOHM
R179 499OHM
R742 4.7KOHM

B50282_1

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QSGMII B50282 PHY 2

VOLTAGE=3.3 XP383V

MIIM0 PHY_ADDRESS:01010

NO_ASSY=TRUE

4.7KOHM R756 R757 R758 R759 R760 R769

B50282C1KFBG
G227-10066-01
U25
B50282C1KFBG

PHYA_4 PHYA_3 PHYA_2 PHYA_1 PHYA_0 PHYA_REV

PHY2_QSIN_1_P C701 0.1UF 0201 X6S 25V RDP1
PHY2_QSIN_1_N C702 0.1UF 0201 X6S 25V RDN1
PHY2_QSIN_0_P C703 0.1UF 0201 X6S 25V RDP0
PHY2_QSIN_0_N C704 0.1UF 0201 X6S 25V RDN0

LED7_2* LED7_1* LED7_0*
LED6_2* LED6_1* LED6_0*
LED5_2* LED5_1* LED5_0*
LED4_2* LED4_1* LED4_0*
LED3_2* LED3_1* LED3_0*
LED2_2* LED2_1* LED2_0*
LED1_2* LED1_1* LED1_0*
LED0_3* LED0_2* LED0_1* LED0_0*

B50282_2_CRNT_LED_EN CRNT_LED_EN
B50282_2_FDX FDX
B50282_2_ANEN ANEN
B50282_2_SPD0 SPD0
B50282_2_SUPER_I SUPER_I
B50282_2_HUB HUB

TVCO_2 TVCO_1 TEST_1 TEST_0

VOLTAGE=3.3 XP383V

4.7KOHM R780 R782 R784 R786 R788 R790

1KOHM R781 R783 R785 R787 R789 R791

NO_ASSY=TRUE

SCLK_TYPE[1:0] = 01 SINGLE-END CMOS
00 CML DIFFERENTIAL CLOCK
10 CRYSTAL
11 NA

SCLK_FREQ[1:0] = 00 25MHZ
01 125MHZ
10 312.5MHZ
11 156.25MHZ

MDIO_LVL = 1 OVDDMDIO OPERATE AT 2.5V OR 3.3V
0 OVDDMDIO OPERATE AT 1.2V

SCLK_TYPE_1 SCLK_TYPE_0 SCLK_FREQ_1 SCLK_FREQ_0 MDIO_LVL MDIO_SEL_2 MDIO_SEL_1

MDIO_2 MDC_2

GIG_MDIO1 R770 233OHM MDIO_1
GIG_MDC1 R771 233OHM MDC_1

B50282_2_REFCLK_P REFCLKP
B50282_2_REFCLK_N REFCLKN

B50282_2_RST_N RESET*
R761 24.7KOHM

1 OF 2

TDP_7_3 TDN_7_3 TDP_7_2 TDN_7_2 TDP_7_1 TDN_7_1 TDP_7_0 TDN_7_0
TDP_6_3 TDN_6_3 TDP_6_2 TDN_6_2 TDP_6_1 TDN_6_1 TDP_6_0 TDN_6_0
TDP_5_3 TDN_5_3 TDP_5_2 TDN_5_2 TDP_5_1 TDN_5_1 TDP_5_0 TDN_5_0
TDP_4_3 TDN_4_3 TDP_4_2 TDN_4_2 TDP_4_1 TDN_4_1 TDP_4_0 TDN_4_0
TDP_3_3 TDN_3_3 TDP_3_2 TDN_3_2 TDP_3_1 TDN_3_1 TDP_3_0 TDN_3_0
TDP_2_3 TDN_2_3 TDP_2_2 TDN_2_2 TDP_2_1 TDN_2_1 TDP_2_0 TDN_2_0
TDP_1_3 TDN_1_3 TDP_1_2 TDN_1_2 TDP_1_1 TDN_1_1 TDP_1_0 TDN_1_0
TDP_0_3 TDN_0_3 TDP_0_2 TDN_0_2 TDP_0_1 TDN_0_1 TDP_0_0 TDN_0_0

TDP1 TDP0 TDN1 TDN0
PHY2_QSOUT_1_P PHY2_QSOUT_0_P PHY2_QSOUT_1_N PHY2_QSOUT_0_N

LOC_REC_CLK_2 LOC_REC_CLK_1 REC_CLK_2 REC_CLK_1

TDI TDO TMS TCK TRST INTERRUPT* RDAC_2 RDAC_1
PHY2_TDI PHY2_TDO PHY2_TMS PHY2_TCK PHY2_TRST B50282_2_INT_N

R772 4.7KOHM R773 4.7KOHM R775 4.7KOHM R777 4.7KOHM R779 4.7KOHM

R774 6.04KOHM R776 6.04KOHM R778 4.7KOHM
R1180 499OHM R1181 499OHM NO_ASSY=TRUE

B50282C1KFBG
G227-10066-01
U25
B50282C1KFBG

DNC_1 DNC_2 DNC_3 DNC_4 DNC_5 DNC_6 DNC_7 DNC_8 DNC_9 DNC_10 DNC_11 DNC_12 DNC_13 DNC_14 DNC_15 DNC_16 DNC_17 DNC_18 DNC_19

GND_1 GND_2 GND_3 GND_4 GND_5 GND_6 GND_7 GND_8 GND_9 GND_10 GND_11 GND_12 GND_13 GND_14 GND_15 GND_16 GND_17 GND_18 GND_19 GND_20 GND_21 GND_22 GND_23 GND_24 GND_25 GND_26 GND_27 GND_28 GND_29 GND_30 GND_31 GND_32 GND_33 GND_34 GND_35 GND_36 GND_37 GND_38 GND_39 GND_40 GND_41 GND_42 GND_43 GND_44 GND_45 GND_46 GND_47 GND_48 GND_49 GND_50 GND_51

2 OF 2

CLKVDD BIASVDD_1 BIASVDD_2 OVDDMDIO OVDDJTAG_1 OVDDJTAG_2

AVDDH_1 AVDDH_2 AVDDH_3 AVDDH_4 AVDDH_5 AVDDH_6 AVDDH_7 AVDDH_8

OVDD_1 OVDD_2 OVDD_3 OVDD_4 OVDD_5 OVDD_6 OVDD_7 OVDD_8

QTVDD_1 QTVDD_2 QTVDD_3 QTVDD_4

QRVDD_1 QRVDD_2 QRVDD_3 QRVDD_4

QPVDD_1 QPVDD_2

DVDD_1 DVDD_2 DVDD_3 DVDD_4 DVDD_5 DVDD_6 DVDD_7

AVDDL_1 AVDDL_2 AVDDL_3 AVDDL_4 AVDDL_5 AVDDL_6

PLLVDD_1 PLLVDD_2

L87 L85 L84 L86 1.3A 0.1OHM 2600OHM

C707 C714 C721 C728 C734 C739 C744 0.1UF X7R 0402 25V
C748 10UF X5R 0402 6.3V
C708 C715 C722 0.1UF X7R 0402 25V
C709 C716 C723 C729 C735 C740 C745 0.1UF X7R 0402 25V
C749 0.1UF X7R 0402 25V
C710 C717 C725 C730 0.1UF X7R 0402 25V
C736 10UF X5R 0402 6.3V
C711 1UF X6S 0402 25V
C718 10UF X5R 0402 6.3V
C706 C713 C720 C727 C733 C738 C741 0.1UF X7R 0402 25V
C705 C712 C719 C726 C732 C737 C742 0.1UF X7R 0402 25V
C746 10UF X5R 0402 6.3V

VOLTAGE=3.3 XP383V
VOLTAGE=1.0V XP180V

C724 10UF X5R 0402 6.3V
C731 22UF X5R 0603 10V
C743 10UF X5R 0402 6.3V
C747 22UF X5R 0603 10V

B50282_2

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QSGMII B50282 PHY 3

VOLTAGE=3.3 XF3R3V
MIIM0 PHY_ADDRESS:10011
NO_ASSY=TRUE
R684 4.7KOHM
R685 4.7KOHM
R686 4.7KOHM
R687 4.7KOHM
R688 4.7KOHM
R699 4.7KOHM

B50282C1KFBG
G227-10066-01
U9
B50282C1KFBG

PHY3_QSIN_1_P C603 0.1UF 0201 X6S 25V
PHY3_QSIN_1_N C604 0.1UF 0201 X6S 25V
PHY3_QSIN_0_P C605 0.1UF 0201 X6S 25V
PHY3_QSIN_0_N C606 0.1UF 0201 X6S 25V

PHYA_4 PHYA_3 PHYA_2 PHYA_1 PHYA_0 PHYA_REV
RDP1 RDN1 RDP0 RDN0

LED7_2* LED7_1* LED7_0*
LED6_2* LED6_1* LED6_0*
LED5_2* LED5_1* LED5_0*
LED4_2* LED4_1* LED4_0*
LED3_2* LED3_1* LED3_0*
LED2_2* LED2_1* LED2_0*
LED1_2* LED1_1* LED1_0*
LED0_3* LED0_2* LED0_1* LED0_0*

TDP_7_3 TDN_7_3 TDP_7_2 TDN_7_2 TDP_7_1 TDN_7_1 TDP_7_0 TDN_7_0
TDP_6_3 TDN_6_3 TDP_6_2 TDN_6_2 TDP_6_1 TDN_6_1 TDP_6_0 TDN_6_0
TDP_5_3 TDN_5_3 TDP_5_2 TDN_5_2 TDP_5_1 TDN_5_1 TDP_5_0 TDN_5_0
TDP_4_3 TDN_4_3 TDP_4_2 TDN_4_2 TDP_4_1 TDN_4_1 TDP_4_0 TDN_4_0
TDP_3_3 TDN_3_3 TDP_3_2 TDN_3_2 TDP_3_1 TDN_3_1 TDP_3_0 TDN_3_0
TDP_2_3 TDN_2_3 TDP_2_2 TDN_2_2 TDP_2_1 TDN_2_1 TDP_2_0 TDN_2_0
TDP_1_3 TDN_1_3 TDP_1_2 TDN_1_2 TDP_1_1 TDN_1_1 TDP_1_0 TDN_1_0
TDP_0_3 TDN_0_3 TDP_0_2 TDN_0_2 TDP_0_1 TDN_0_1 TDP_0_0 TDN_0_0

TDP1 TDN1 TDP0 TDN0
LOC_REC_CLK_2 LOC_REC_CLK_1
REC_CLK_2 REC_CLK_1
TDI TDO TMS TCK TRST INTERRUPT* RDAC_2 RDAC_1

1 OF 2

VOLTAGE=3.3 XF3R3V
R708 4.7KOHM R710 4.7KOHM R712 4.7KOHM R714 4.7KOHM R716 4.7KOHM R718 4.7KOHM
R709 1KOHM R711 1KOHM R713 1KOHM R715 1KOHM R717 1KOHM R719 1KOHM
NO_ASSY=TRUE

B50282_3_CRNT_LED_EN CRNT_LED_EN
B50282_3_FDX FDX
B50282_3_ANEN ANEN
B50282_3_SPD0 SPD0
B50282_3_SUPER_I SUPER_I
B50282_3_HUB HUB

TVCO_2 TVCO_1 TEST_1 TEST_0

SCLK_TYPE[1:0] = 01 SINGLE-END CMOS
00 CML DIFFERENTIAL CLOCK
10 CRYSTAL
11 NA
SCLK_FREQ[1:0] = 00 25MHZ
01 125MHZ
10 312.5MHZ
11 156.25MHZ
MDIO_LVL = 1 OVDDMDIO OPERATE AT 2.5V OR 3.3V
0 OVDDMDIO OPERATE AT 1.2V

R690 24.7KOHM SCLK_TYPE_1
R691 24.7KOHM SCLK_TYPE_0
R692 24.7KOHM SCLK_FREQ_1
R693 24.7KOHM SCLK_FREQ_0
R694 24.7KOHM MDIO_LVL
R695 24.7KOHM MDIO_SEL_2
R696 24.7KOHM MDIO_SEL_1
MDIO_2 MDC_2
GIG_MDIO2 R697 233OHM MDIO_1
GIG_MDC2 R698 233OHM MDC_1
B50282_3_REFCLK_P REFCLKP
B50282_3_REFCLK_N REFCLKN
B50282_3_RST_N RESET*
R689 24.7KOHM

R700 4.7KOHM R701 4.7KOHM R703 4.7KOHM R705 4.7KOHM R707 4.7KOHM
PHY3_TDI PHY3_TDO PHY3_TMS PHY3_TCK PHY3_TRST
B50282_3_INT_N
R702 6.04KOHM R176 499OHM R704 6.04KOHM R177 499OHM R706 4.7KOHM

B50282C1KFBG
G227-10066-01
U9
B50282C1KFBG

CLKVDD BIASVDD_1 BIASVDD_2 OVDDMDIO OVDDJTAG_1 OVDDJTAG_2
AVDDH_1 AVDDH_2 AVDDH_3 AVDDH_4 AVDDH_5 AVDDH_6 AVDDH_7 AVDDH_8
OVDD_1 OVDD_2 OVDD_3 OVDD_4 OVDD_5 OVDD_6 OVDD_7 OVDD_8
QTVDD_1 QTVDD_2 QTVDD_3 QTVDD_4
QRVDD_1 QRVDD_2 QRVDD_3 QRVDD_4
QPVDD_1 QPVDD_2
DVDD_1 DVDD_2 DVDD_3 DVDD_4 DVDD_5 DVDD_6 DVDD_7
AVDDL_1 AVDDL_2 AVDDL_3 AVDDL_4 AVDDL_5 AVDDL_6
PLLVDD_1 PLLVDD_2
GND_1 ... GND_51

2 OF 2

VOLTAGE=3.3 XF3R3V
L79 1.3A 0.1OHM 2600OHM
C609 C616 C623 C630 C636 C641 C646 C650 0.1UF 25V X7R 0402 ... 10UF 6.3V X5R 0402
C610 C617 0.1UF 25V X7R 0402
C624 0.1UF 25V X7R 0402

VOLTAGE=3.3 XF3R3V
C611 C618 C625 C631 C637 C642 C647 C651 0.1UF 25V X7R 0402

VOLTAGE=1.0V XF1R0V
L77 1.3A 0.1OHM 2600OHM
C612 C619 C627 C632 0.1UF 25V X7R 0402
C638 10UF 6.3V X5R 0402

VOLTAGE=1.0V XF1R0V
L76 1.3A 0.1OHM 2600OHM
C613 1UF 25V X6S 0402
C620 10UF 6.3V X5R 0402

VOLTAGE=1.0V XF1R0V
C608 C615 C622 C629 C635 C640 C643 0.1UF 25V X7R 0402
NO_ASSY=TRUE

VOLTAGE=1.0V XF1R0V
L78 1.3A 0.1OHM 2600OHM
C607 C614 C621 C628 C634 C639 C644 0.1UF 25V X7R 0402
C648 10UF 6.3V X5R 0402

VOLTAGE=3.3 XF3R3V
C626 10UF 6.3V X5R 0402
C633 22UF 10V X5R 0603

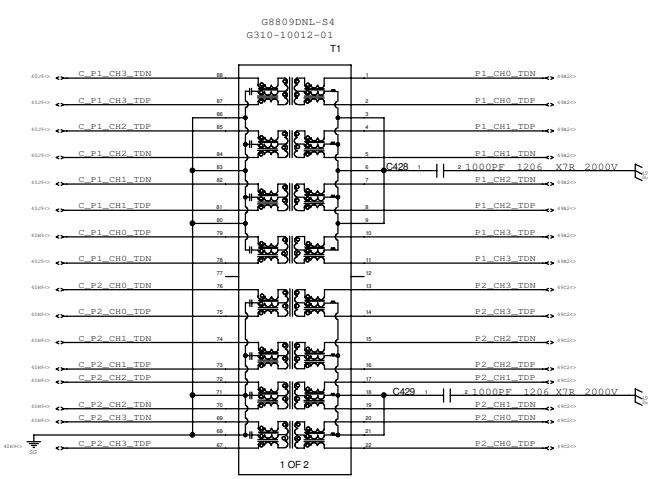
VOLTAGE=1.0V XF1R0V
C645 10UF 6.3V X5R 0402
C649 22UF 10V X5R 0603

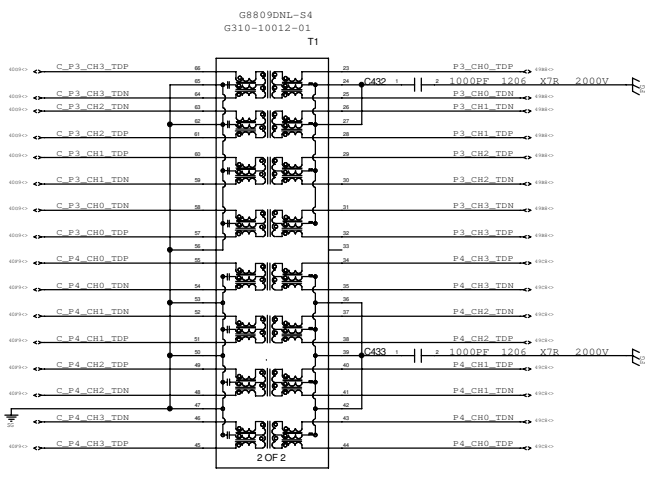
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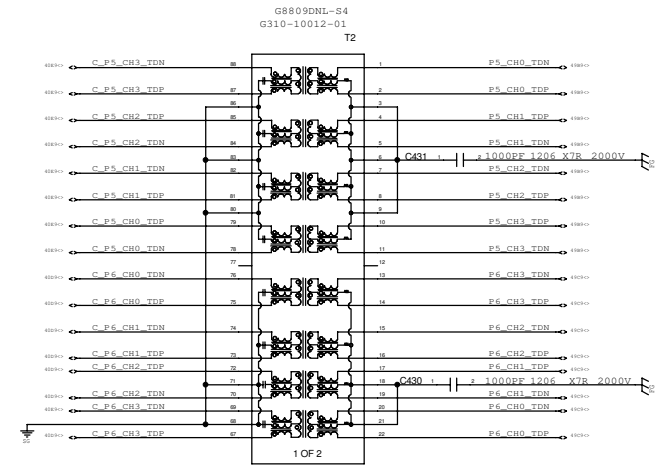
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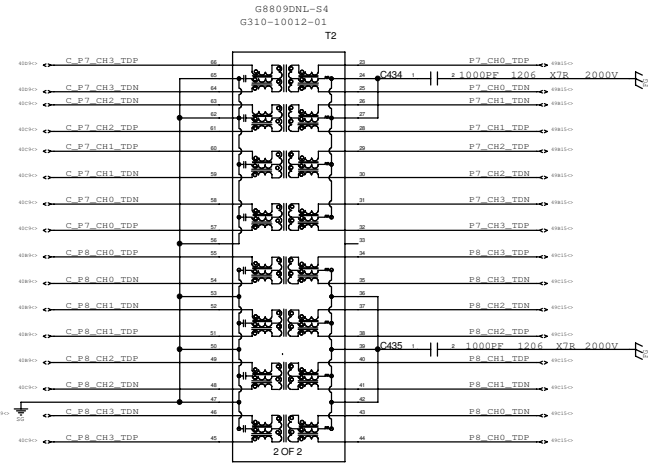
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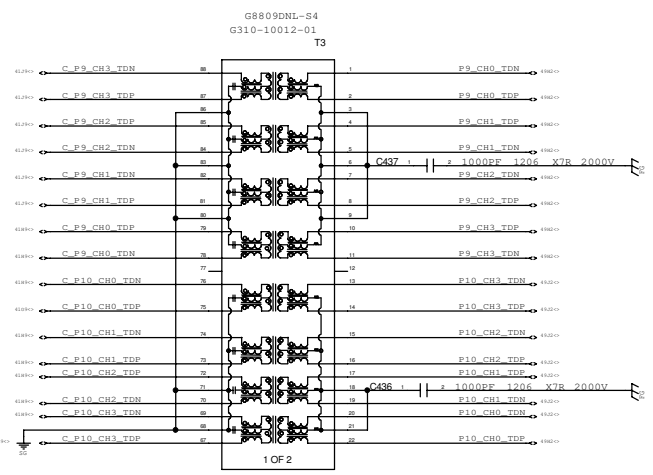


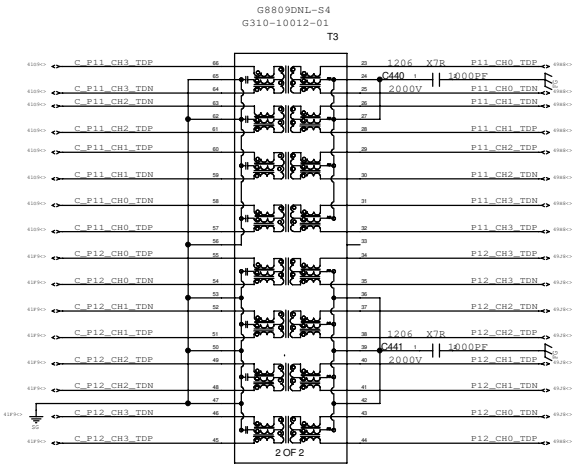


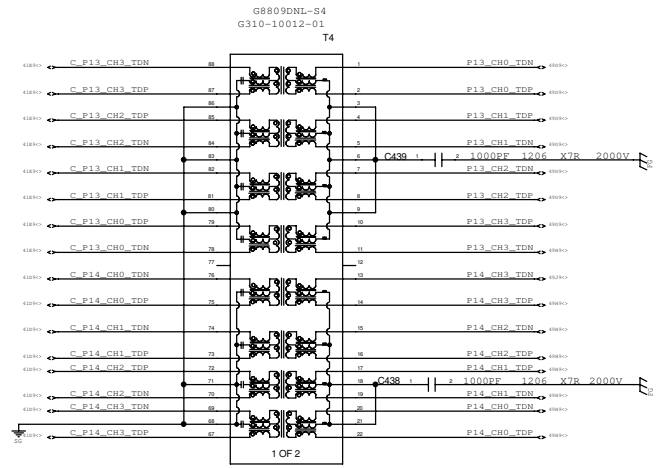
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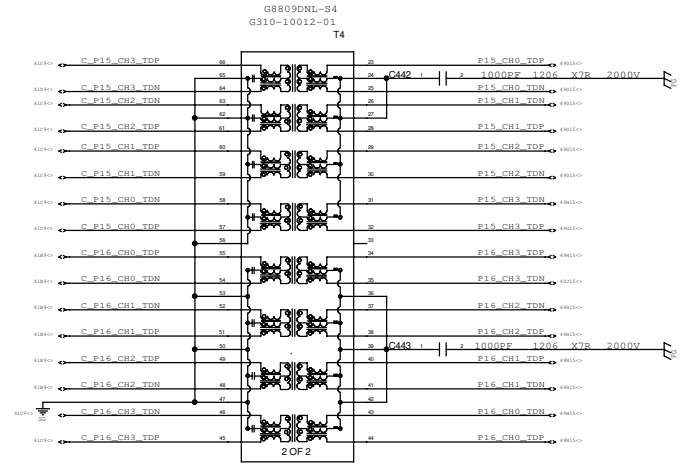
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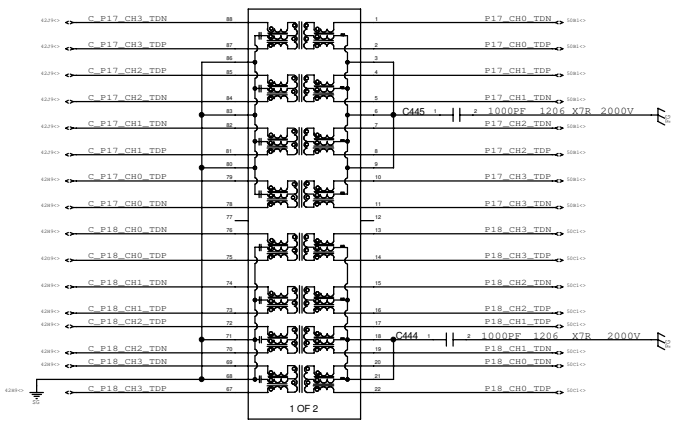


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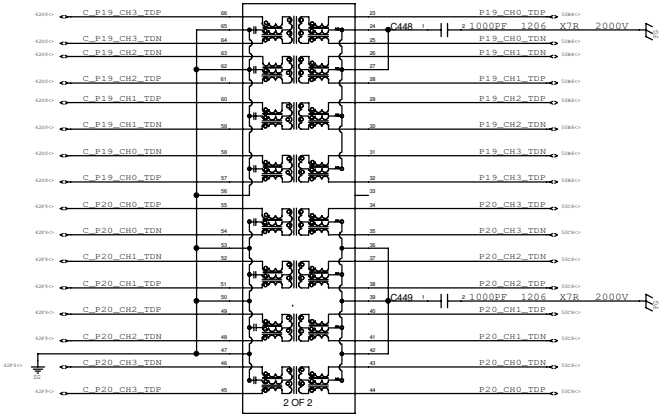
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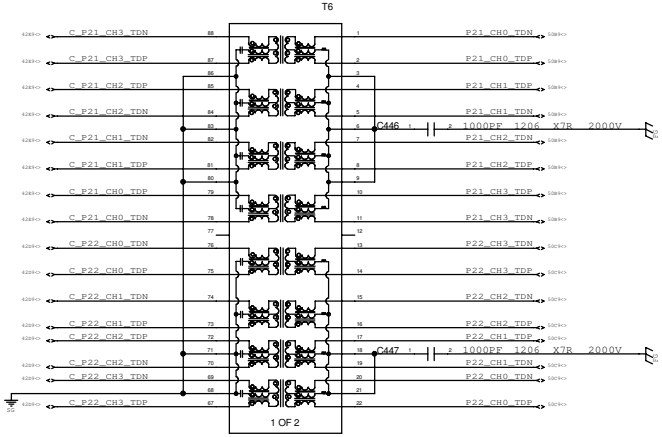
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G310-10012-01
T5



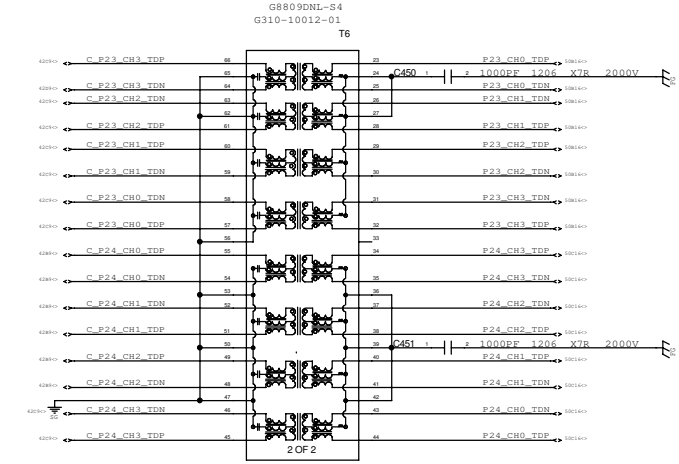
G8809DNL-S4
G310-10012-01
T5



G8809DNL-S4
G310-10012-01
T6



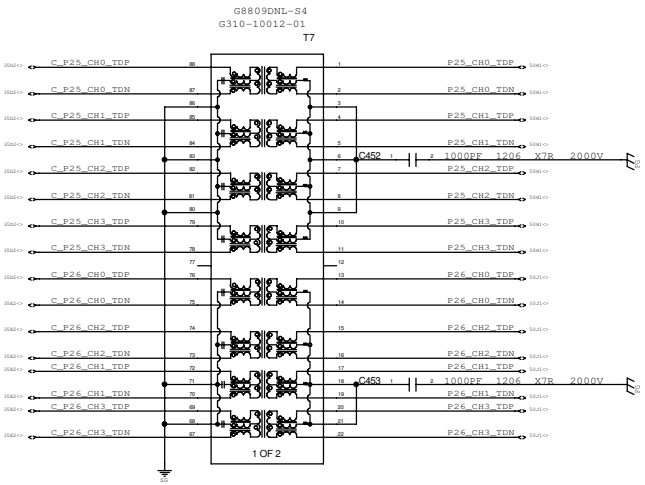
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G310-10012-01
T6

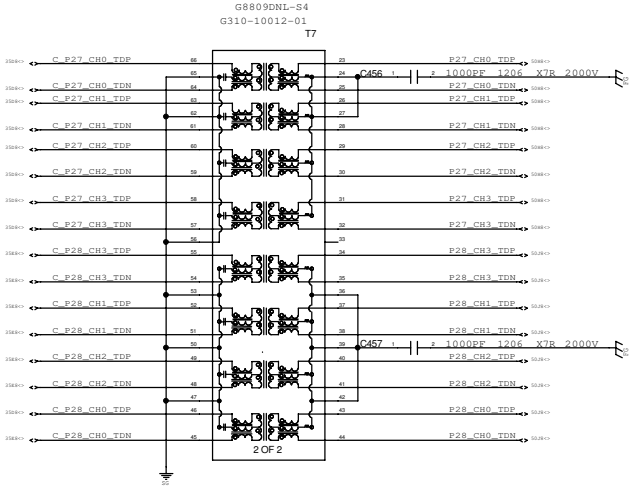


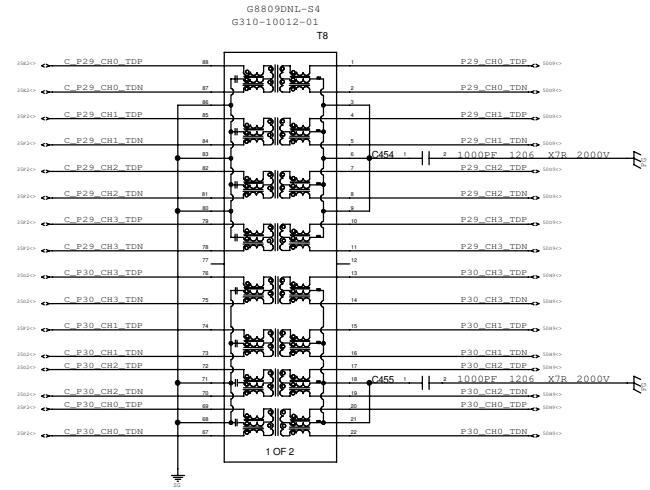
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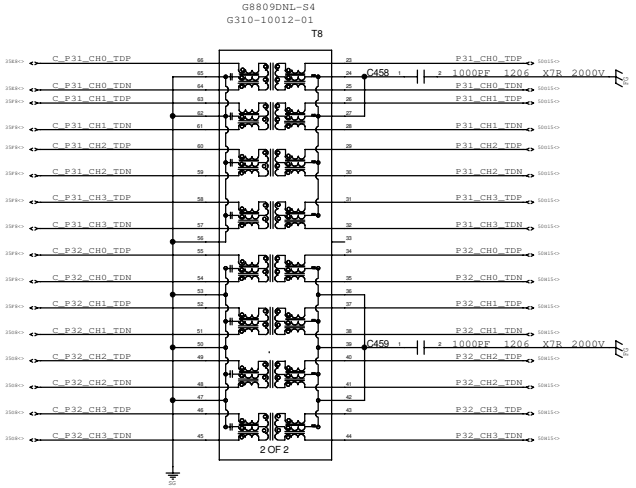
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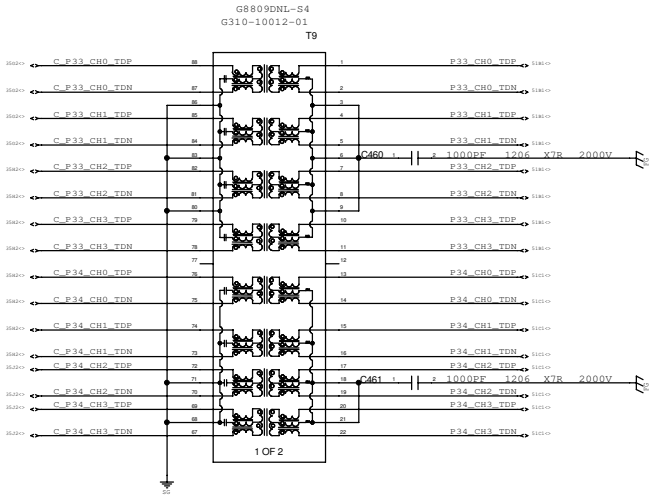


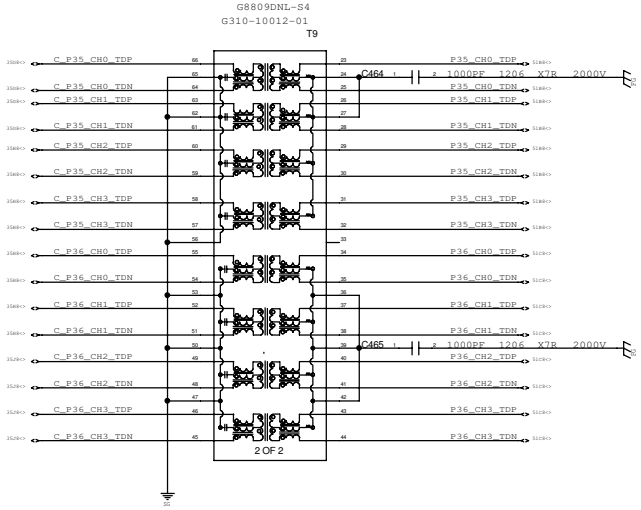


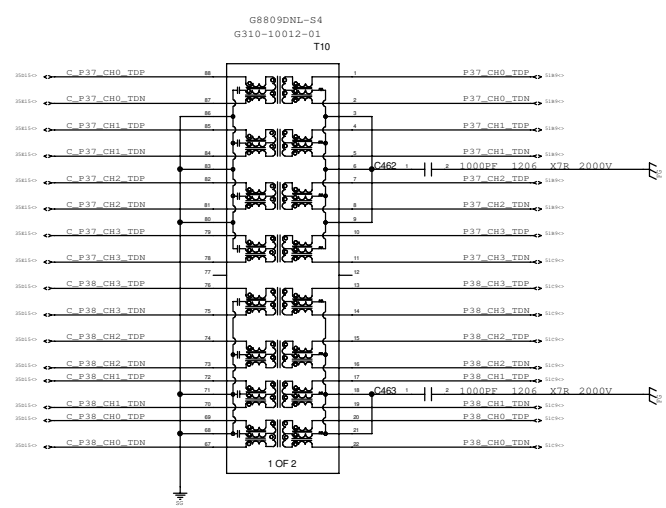
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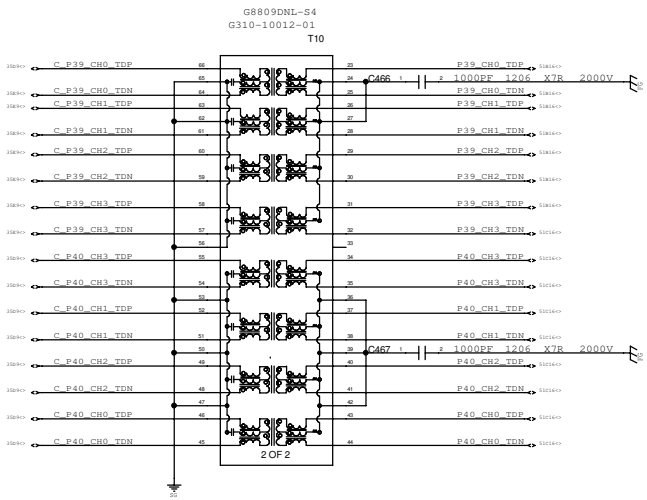
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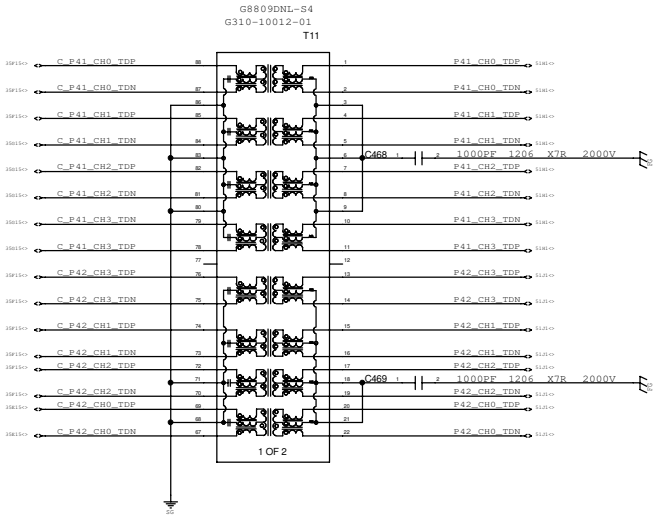


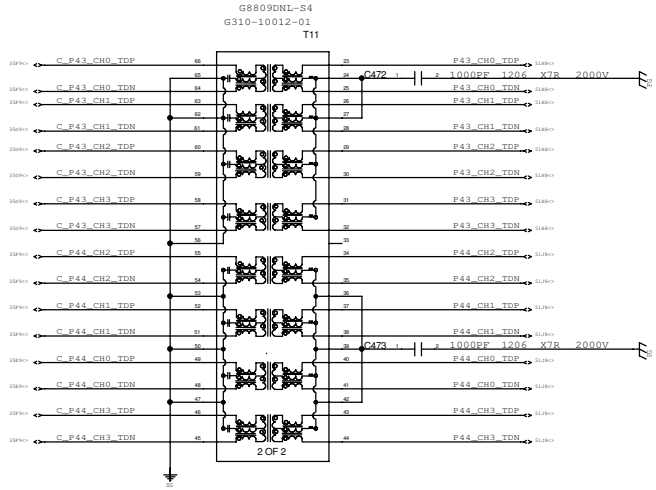


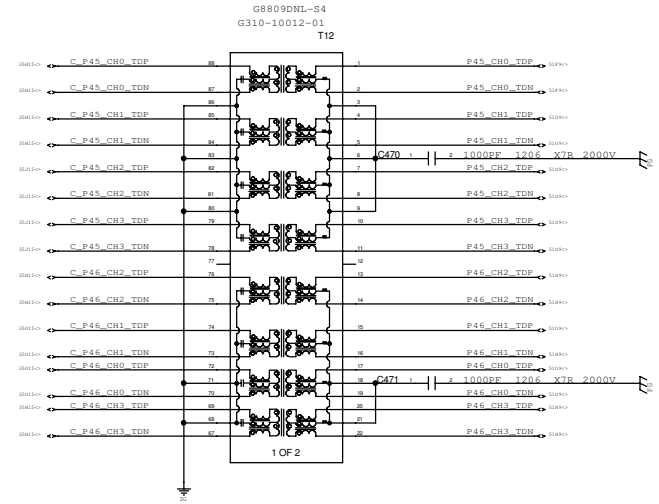
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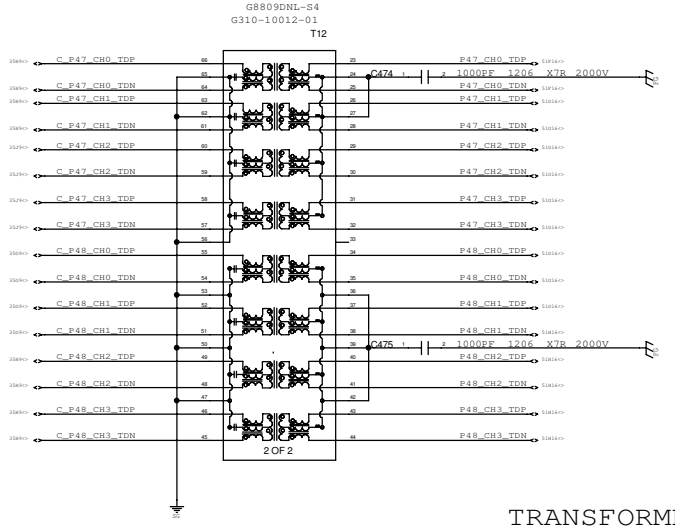
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Document Number:	Document Version:
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R3059-CN001-01
J3

P1_CH0_TDP
P1_CH0_TDN
P1_CH1_TDP
P1_CH2_TDP
P1_CH2_TDN
P1_CH1_TDN
P1_CH3_TDP
P1_CH3_TDN

P2_CH0_TDP
P2_CH0_TDN
P2_CH1_TDP
P2_CH2_TDP
P2_CH2_TDN
P2_CH1_TDN
P2_CH3_TDP
P2_CH3_TDN

P3_CH0_TDP
P3_CH0_TDN
P3_CH1_TDP
P3_CH2_TDP
P3_CH2_TDN
P3_CH1_TDN
P3_CH3_TDP
P3_CH3_TDN

P4_CH0_TDP
P4_CH0_TDN
P4_CH1_TDP
P4_CH2_TDP
P4_CH2_TDN
P4_CH1_TDN
P4_CH3_TDP
P4_CH3_TDN

P1_1000M_LED R32 1KOHM R_P1_1000M_LED — R_P1_100M_LED R58 1KOHM P1_100M_LED
P2_1000M_LED R33 1KOHM R_P2_1000M_LED — R_P2_100M_LED R59 1KOHM P2_100M_LED
P3_1000M_LED R34 1KOHM R_P3_1000M_LED — R_P3_100M_LED R60 1KOHM P3_100M_LED
P4_1000M_LED R53 1KOHM R_P4_1000M_LED — R_P4_100M_LED R61 1KOHM P4_100M_LED

XP3R3V_PORT
G
Y

GH1 GH2 GH3 GH4 GH5 GH6 GH7 GH8 GH9 GH10 GH11 GH12 GH13

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J3

P5_CH0_TDP
P5_CH0_TDN
P5_CH1_TDP
P5_CH2_TDP
P5_CH2_TDN
P5_CH1_TDN
P5_CH3_TDP
P5_CH3_TDN

P6_CH0_TDP
P6_CH0_TDN
P6_CH1_TDP
P6_CH2_TDP
P6_CH2_TDN
P6_CH1_TDN
P6_CH3_TDP
P6_CH3_TDN

P7_CH0_TDP
P7_CH0_TDN
P7_CH1_TDP
P7_CH2_TDP
P7_CH2_TDN
P7_CH1_TDN
P7_CH3_TDP
P7_CH3_TDN

P8_CH0_TDP
P8_CH0_TDN
P8_CH1_TDP
P8_CH2_TDP
P8_CH2_TDN
P8_CH1_TDN
P8_CH3_TDP
P8_CH3_TDN

P5_1000M_LED R62 1KOHM R_P5_1000M_LED — R_P5_100M_LED R365 1KOHM P5_100M_LED
P6_1000M_LED R63 1KOHM R_P6_1000M_LED — R_P6_100M_LED R366 1KOHM P6_100M_LED
P7_1000M_LED R64 1KOHM R_P7_1000M_LED — R_P7_100M_LED R367 1KOHM P7_100M_LED
P8_1000M_LED R65 1KOHM R_P8_1000M_LED — R_P8_100M_LED R368 1KOHM P8_100M_LED

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P9_CH0_TDP
P9_CH0_TDN
P9_CH1_TDP
P9_CH2_TDP
P9_CH2_TDN
P9_CH1_TDN
P9_CH3_TDP
P9_CH3_TDN

P10_CH0_TDP
P10_CH0_TDN
P10_CH1_TDP
P10_CH2_TDP
P10_CH2_TDN
P10_CH1_TDN
P10_CH3_TDP
P10_CH3_TDN

P11_CH0_TDP
P11_CH0_TDN
P11_CH1_TDP
P11_CH2_TDP
P11_CH2_TDN
P11_CH1_TDN
P11_CH3_TDP
P11_CH3_TDN

P12_CH0_TDP
P12_CH0_TDN
P12_CH1_TDP
P12_CH2_TDP
P12_CH2_TDN
P12_CH1_TDN
P12_CH3_TDP
P12_CH3_TDN

P9_1000M_LED R28 1KOHM R_P9_1000M_LED — R_P9_100M_LED R54 1KOHM P9_100M_LED
P10_1000M_LED R29 1KOHM R_P10_1000M_LED — R_P10_100M_LED R55 1KOHM P10_100M_LED
P11_1000M_LED R30 1KOHM R_P11_1000M_LED — R_P11_100M_LED R56 1KOHM P11_100M_LED
P12_1000M_LED R31 1KOHM R_P12_1000M_LED — R_P12_100M_LED R57 1KOHM P12_100M_LED

XP3R3V_PORT
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J3

P13_CH0_TDP
P13_CH0_TDN
P13_CH1_TDP
P13_CH2_TDP
P13_CH2_TDN
P13_CH1_TDN
P13_CH3_TDP
P13_CH3_TDN

P14_CH0_TDP
P14_CH0_TDN
P14_CH1_TDP
P14_CH2_TDP
P14_CH2_TDN
P14_CH1_TDN
P14_CH3_TDP
P14_CH3_TDN

P15_CH0_TDP
P15_CH0_TDN
P15_CH1_TDP
P15_CH2_TDP
P15_CH2_TDN
P15_CH1_TDN
P15_CH3_TDP
P15_CH3_TDN

P16_CH0_TDP
P16_CH0_TDN
P16_CH1_TDP
P16_CH2_TDP
P16_CH2_TDN
P16_CH1_TDN
P16_CH3_TDP
P16_CH3_TDN

P13_1000M_LED R66 1KOHM R_P13_1000M_LED — R_P13_100M_LED R369 1KOHM P13_100M_LED
P14_1000M_LED R67 1KOHM R_P14_1000M_LED — R_P14_100M_LED R370 1KOHM P14_100M_LED
P15_1000M_LED R68 1KOHM R_P15_1000M_LED — R_P15_100M_LED R371 1KOHM P15_100M_LED
P16_1000M_LED R194 1KOHM R_P16_1000M_LED — R_P16_100M_LED R372 1KOHM P16_100M_LED

XP3R3V_PORT
G
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R581 0OHM

VOLTAGE=3.3
XP3R3V
L88 330OHM 0603 1.7A
XP3R3V_PORT
C755 0.1UF X6S 25V 0201
C756 0.1UF X6S 25V 0402
C757 0.1UF X6S 25V 0201
C758 0.1UF X6S 25V 0201

2X8 RJ45_01-16 PORT

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R1116 0OHM

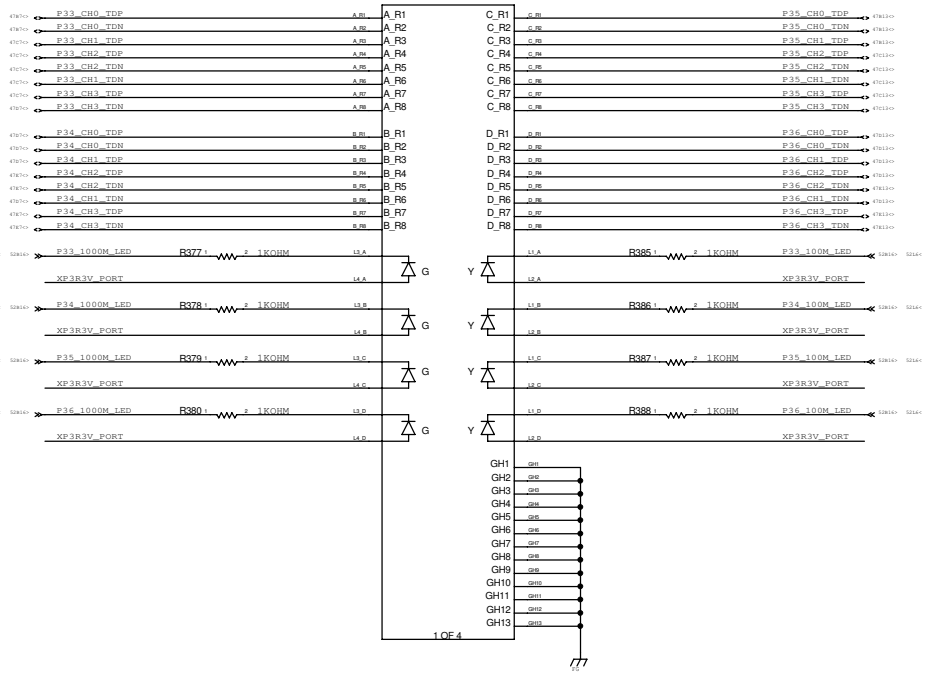
R1117 0OHM

2X8 RJ45_17-32 PORT

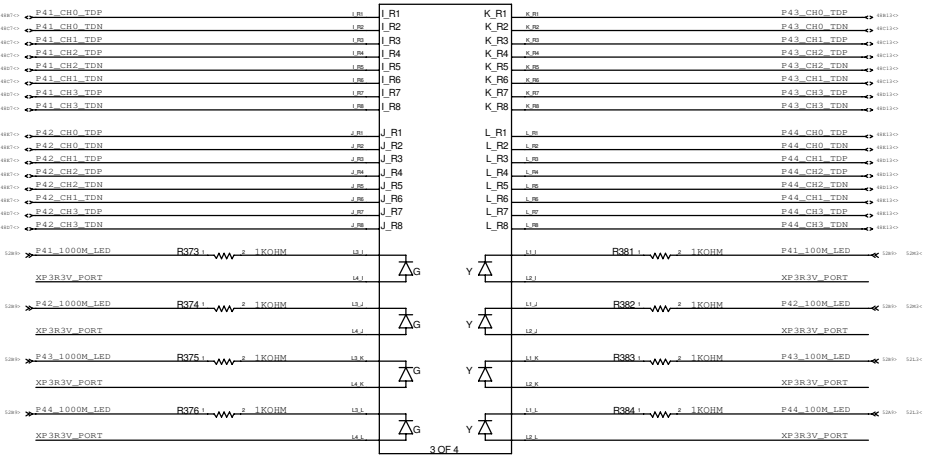
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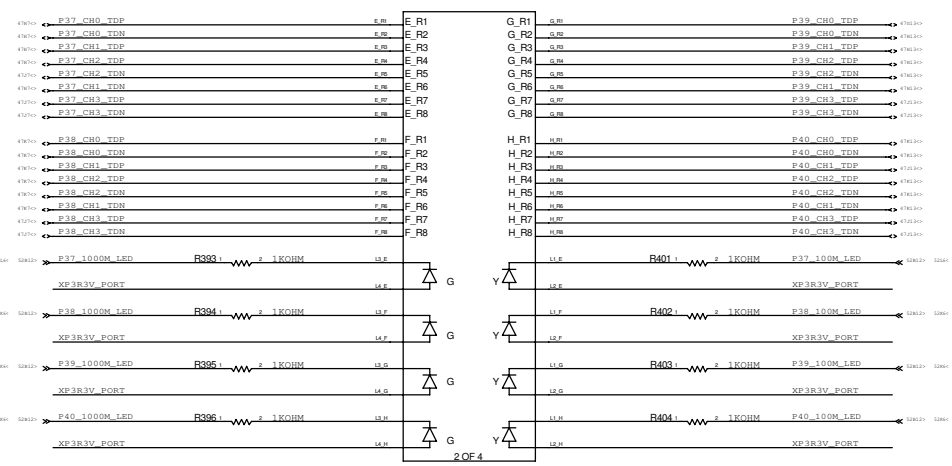
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J9



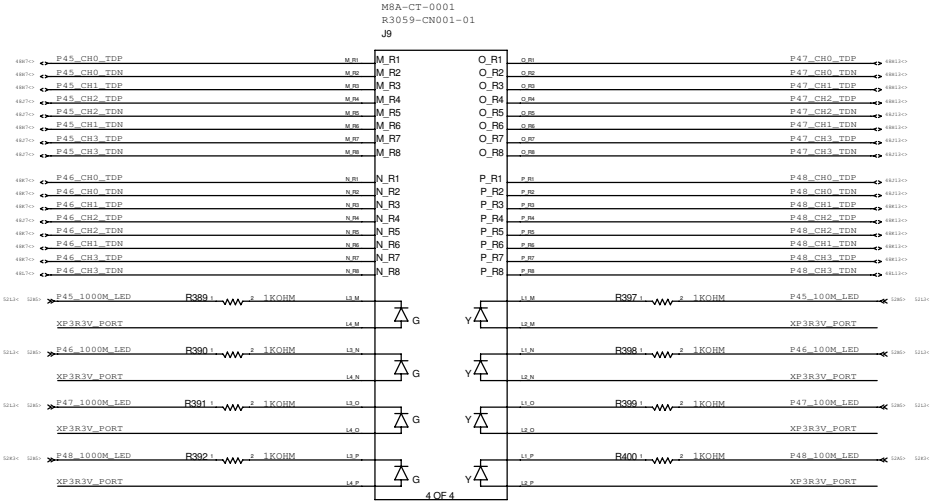
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R3059-CN001-01
J9

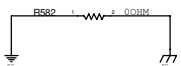


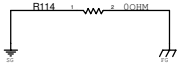
M8A-CT-0001
R3059-CN001-01
J9

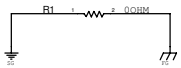


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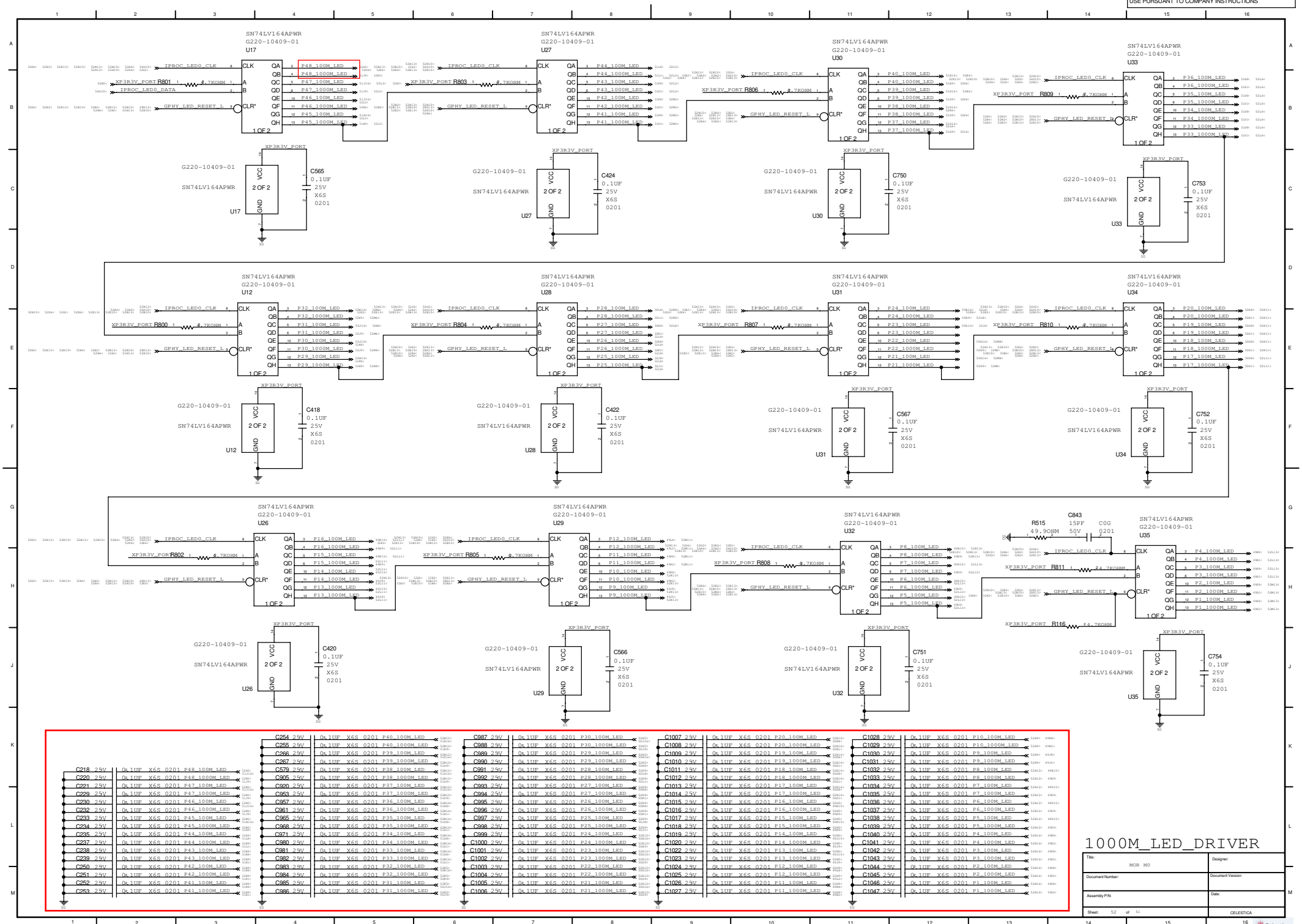




2X8 RJ45_33-48 PORT

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1-2007394-7
G200-11227-01
J1

VOLTAGE=3.3
XP3R3V

SFP+_1_RD_N
SFP+_1_RD_P
XP3R3V_RX1
XP3R3V_TX1
SFP+_1_TD_P
SFP+_1_TD_N
SFP+_2_RD_N
SFP+_2_RD_P
XP3R3V_RX2
XP3R3V_TX2
SFP+_2_TD_P
SFP+_2_TD_N
SFP+_3_RD_N
SFP+_3_RD_P
XP3R3V_RX3
XP3R3V_TX3
SFP+_3_TD_P
SFP+_3_TD_N
SFP+_4_RD_N
SFP+_4_RD_P
XP3R3V_RX4
XP3R3V_TX4
SFP+_4_TD_P
SFP+_4_TD_N

VEER_2_1 RD_N_1 RD_P_1 VEER_3_1 VCCR_1 VCCT_1 VEET_2_1 TD_P_1 TD_N_1 VEET_3_1
VEER_2_2 RD_N_2 RD_P_2 VEER_3_2 VCCR_2 VCCT_2 VEET_2_2 TD_P_2 TD_N_2 VEET_3_2
VEER_2_3 RD_N_3 RD_P_3 VEER_3_3 VCCR_3 VCCT_3 VEET_2_3 TD_P_3 TD_N_3 VEET_3_3
VEER_2_4 RD_N_4 RD_P_4 VEER_3_4 VCCR_4 VCCT_4 VEET_2_4 TD_P_4 TD_N_4 VEET_3_4
GH1 GH2 GH3 GH4 GH5 GH6 GH7 GH8 GH9

VEER_1_1 RS1_1 RX_LOS_1 RS0_1 MOD_ABS_1 SCL_1 SDA_1 TX_DISABLE_1 TX_FAULT_1 VEET_1_1
VEER_1_2 RS1_2 RX_LOS_2 RS0_2 MOD_ABS_2 SCL_2 SDA_2 TX_DISABLE_2 TX_FAULT_2 VEET_1_2
VEER_1_3 RS1_3 RX_LOS_3 RS0_3 MOD_ABS_3 SCL_3 SDA_3 TX_DISABLE_3 TX_FAULT_3 VEET_1_3
VEER_1_4 RS1_4 RX_LOS_4 RS0_4 MOD_ABS_4 SCL_4 SDA_4 TX_DISABLE_4 TX_FAULT_4 VEET_1_4
GH10 GH11 GH12 GH13 GH14 GH15 GH16 GH17 GH18

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SFP+_1_RATESEL R178 4.7KOHM
SFP+_1_RX_LOS R179 4.7KOHM
SFP+_1_MOD_ABS R181 4.7KOHM
SFP+_1_SCL R182 10KOHM
SFP+_1_SDA R183 10KOHM
SFP+_1_TX_DISABLE
SFP+_1_TX_FAULT R184 4.7KOHM
SFP+_2_RATESEL R185 4.7KOHM
SFP+_2_RX_LOS R186 4.7KOHM
SFP+_2_MOD_ABS R188 4.7KOHM
SFP+_2_SCL R189 10KOHM
SFP+_2_SDA R190 10KOHM
SFP+_2_TX_DISABLE
SFP+_2_TX_FAULT R191 4.7KOHM
SFP+_3_RATESEL R192 4.7KOHM
SFP+_3_RX_LOS R193 4.7KOHM
SFP+_3_MOD_ABS R195 4.7KOHM
SFP+_3_SCL R196 10KOHM
SFP+_3_SDA R197 10KOHM
SFP+_3_TX_DISABLE
SFP+_3_TX_FAULT R198 4.7KOHM
SFP+_4_RATESEL R199 4.7KOHM
SFP+_4_RX_LOS R200 4.7KOHM
SFP+_4_MOD_ABS R202 4.7KOHM
SFP+_4_SCL R203 10KOHM
SFP+_4_SDA R204 10KOHM
SFP+_4_TX_DISABLE
SFP+_4_TX_FAULT R205 4.7KOHM

VOLTAGE=3.3
XP3R3V

DS1 ORANGE/GREEN G170-10231-01
R_SFP1_LED_Y R486 1KOHM SFP1_LED_Y
R_SFP1_LED_G R219 1KOHM SFP1_LED_G
DS2 ORANGE/GREEN G170-10231-01
R_SFP2_LED_Y R487 1KOHM SFP2_LED_Y
R_SFP2_LED_G R220 1KOHM SFP2_LED_G
DS3 ORANGE/GREEN G170-10231-01
R_SFP3_LED_Y R488 1KOHM SFP3_LED_Y
R_SFP3_LED_G R221 1KOHM SFP3_LED_G
DS4 ORANGE/GREEN G170-10231-01
R_SFP4_LED_Y R489 1KOHM SFP4_LED_Y
R_SFP4_LED_G R222 1KOHM SFP4_LED_G

XP3R3V_TX1: L31 4.7UH 0.18OHM; C278 10UF 25V X6S 0805; C279 1UF 25V X6S 0402; C280 22UF 10V X5R 0603; C282 1UF 25V X6S 0402; C284 0.1UF 25V X6S 0201; R1135 10OHM; R1705 10OHM
XP3R3V_RX1: L32 4.7UH 0.18OHM; C281 22UF 10V X5R 0603; C283 1UF 25V X6S 0402; C285 0.1UF 25V X6S 0201; R1136 10OHM; R1707 10OHM
XP3R3V_TX2: L33 4.7UH 0.18OHM; C286 10UF 25V X6S 0805; C287 1UF 25V X6S 0402; C288 22UF 10V X5R 0603; C290 1UF 25V X6S 0402; C292 0.1UF 25V X6S 0201; R1137 10OHM; R206 10OHM
XP3R3V_RX2: L34 4.7UH 0.18OHM; C289 22UF 10V X5R 0603; C291 1UF 25V X6S 0402; C293 0.1UF 25V X6S 0201; R1138 10OHM; R207 10OHM
XP3R3V_TX3: L35 4.7UH 0.18OHM; C294 10UF 25V X6S 0805; C295 1UF 25V X6S 0402; C296 22UF 10V X5R 0603; C298 1UF 25V X6S 0402; C300 0.1UF 25V X6S 0201; R1139 10OHM; R208 10OHM
XP3R3V_RX3: L36 4.7UH 0.18OHM; C297 22UF 10V X5R 0603; C299 1UF 25V X6S 0402; C301 0.1UF 25V X6S 0201; R1140 10OHM; R209 10OHM
XP3R3V_TX4: L37 4.7UH 0.18OHM; C302 10UF 25V X6S 0805; C303 1UF 25V X6S 0402; C304 22UF 10V X5R 0603; C306 1UF 25V X6S 0402; C308 0.1UF 25V X6S 0201; R1141 10OHM; R226 10OHM
XP3R3V_RX4: L38 4.7UH 0.18OHM; C305 22UF 10V X5R 0603; C307 1UF 25V X6S 0402; C309 0.1UF 25V X6S 0201; R1142 10OHM; R227 10OHM

SFP+_49-52_PORT

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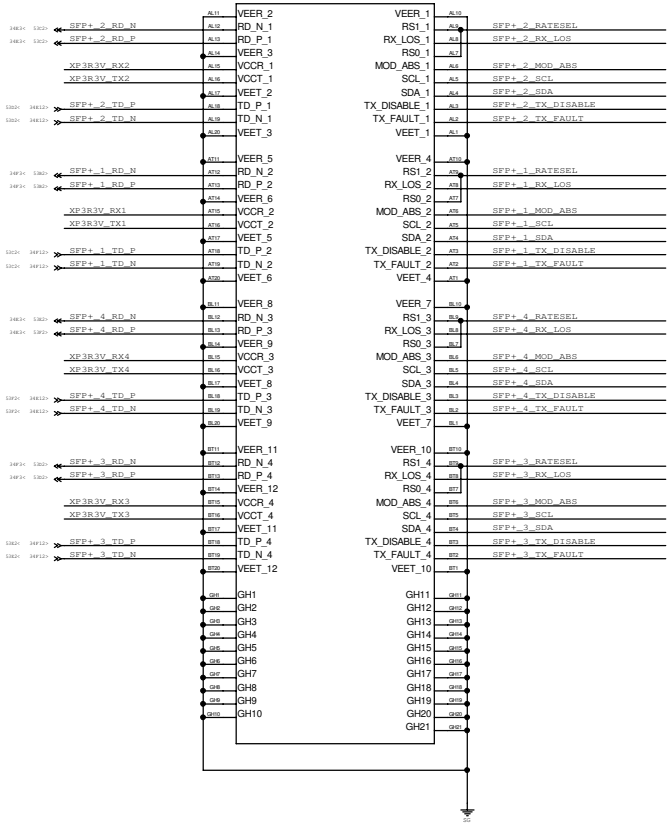
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G200-10814-01

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SFP+_49-52_PORT(COMPATIBLE)

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SFP+_5_RD_N	VEER_2_5	VEER_1_5	SFP+_5_RATESEL	R444	4.7KOHM
SFP+_5_RD_P	RD_N_5	RS1_5	SFP+_5_RX_LOS	R445	4.7KOHM
XP3R3V_RX5	RD_P_5	RX_LOS_5	SFP+_5_MOD_ABS	R447	4.7KOHM
XP3R3V_TX5	VEER_3_5	RS0_5	SFP+_5_SCL	R448	10KOHM
	VCCR_5	MOD_ABS_5	SFP+_5_SDA	R449	10KOHM
SFP+_5_TD_P	VCCT_5	SCL_5	SFP+_5_TX_DISABLE		
SFP+_5_TD_N	VEET_2_5	SDA_5	SFP+_5_TX_FAULT	R450	4.7KOHM
	VEET_3_5	TX_DISABLE_5			
		TX_FAULT_5			
	VEER_2_6	VEET_1_5			
SFP+_6_RD_N	RD_N_6	VEER_1_6	SFP+_6_RATESEL	R451	4.7KOHM
SFP+_6_RD_P	RD_P_6	RS1_6	SFP+_6_RX_LOS	R452	4.7KOHM
	VEER_3_6	RX_LOS_6			
XP3R3V_RX6	VCCR_6	RS0_6	SFP+_6_MOD_ABS	R454	4.7KOHM
XP3R3V_TX6	VCCT_6	MOD_ABS_6	SFP+_6_SCL	R455	10KOHM
	VEET_2_6	SCL_6	SFP+_6_SDA	R456	10KOHM
SFP+_6_TD_P	TD_P_6	SDA_6	SFP+_6_TX_DISABLE		
SFP+_6_TD_N	TD_N_6	TX_DISABLE_6	SFP+_6_TX_FAULT	R457	4.7KOHM
	VEET_3_6	TX_FAULT_6			
		VEET_1_6			
	VEER_2_7	VEER_1_7			
SFP+_7_RD_N	RD_N_7	RS1_7	SFP+_7_RATESEL	R458	4.7KOHM
SFP+_7_RD_P	RD_P_7	RX_LOS_7	SFP+_7_RX_LOS	R459	4.7KOHM
	VEER_3_7	RS0_7			
XP3R3V_RX7	VCCR_7	MOD_ABS_7	SFP+_7_MOD_ABS	R461	4.7KOHM
XP3R3V_TX7	VCCT_7	SCL_7	SFP+_7_SCL	R462	10KOHM
	VEET_2_7	SDA_7	SFP+_7_SDA	R463	10KOHM
SFP+_7_TD_P	TD_P_7	TX_DISABLE_7	SFP+_7_TX_DISABLE		
SFP+_7_TD_N	TD_N_7	TX_FAULT_7	SFP+_7_TX_FAULT	R464	4.7KOHM
	VEET_3_7	VEET_1_7			
	VEER_2_8	VEER_1_8			
SFP+_8_RD_N	RD_N_8	RS1_8	SFP+_8_RATESEL	R465	4.7KOHM
SFP+_8_RD_P	RD_P_8	RX_LOS_8	SFP+_8_RX_LOS	R466	4.7KOHM
	VEER_3_8	RS0_8			
XP3R3V_RX8	VCCR_8	MOD_ABS_8	SFP+_8_MOD_ABS	R468	4.7KOHM
XP3R3V_TX8	VCCT_8	SCL_8	SFP+_8_SCL	R469	10KOHM
	VEET_2_8	SDA_8	SFP+_8_SDA	R470	10KOHM
SFP+_8_TD_P	TD_P_8	TX_DISABLE_8	SFP+_8_TX_DISABLE		
SFP+_8_TD_N	TD_N_8	TX_FAULT_8	SFP+_8_TX_FAULT	R471	4.7KOHM
	VEET_3_8	VEET_1_8			
	GH19	GH29			
	GH20	GH30			
	GH21	GH31			
	GH22	GH32			
	GH23	GH33			
	GH24	GH34			
	GH25	GH35			
	GH26	GH36			
	GH27	GH37			
	GH28				

VOLTAGE=3.3

DS5 ORANGE/GREEN — R_SFP5_LED_Y — R521 — 1KOHM — SFP5_LED_Y
G170-10231-01 — R_SFP5_LED_G — R522 — 1KOHM — SFP5_LED_G

DS6 ORANGE/GREEN — R_SFP6_LED_Y — R571 — 1KOHM — SFP6_LED_Y
G170-10231-01 — R_SFP6_LED_G — R584 — 1KOHM — SFP6_LED_G

DS7 ORANGE/GREEN — R_SFP7_LED_Y — R585 — 1KOHM — SFP7_LED_Y
G170-10231-01 — R_SFP7_LED_G — R586 — 1KOHM — SFP7_LED_G

DS8 ORANGE/GREEN — R_SFP8_LED_Y — R647 — 1KOHM — SFP8_LED_Y
G170-10231-01 — R_SFP8_LED_G — R648 — 1KOHM — SFP8_LED_G

XP3R3V_TX5: L66 4.7UH 0.18OHM; C486 10UF 25V X6S 0805; C487 1UF 25V X6S 0402; C488 22UF 10V X5R 0603; C490 1UF 25V X6S 0402; C492 0.1UF 25V X6S 0201; R1143 10OHM; R442 10OHM; VOLTAGE=3.3
XP3R3V_RX5: L67 4.7UH 0.18OHM; C489 22UF 10V X5R 0603; C491 1UF 25V X6S 0402; C493 0.1UF 25V X6S 0201; R1144 10OHM; R443 10OHM; VOLTAGE=3.3

XP3R3V_TX6: L68 4.7UH 0.18OHM; C494 10UF 25V X6S 0805; C495 1UF 25V X6S 0402; C496 22UF 10V X5R 0603; C498 1UF 25V X6S 0402; C500 0.1UF 25V X6S 0201; R1145 10OHM; R472 10OHM; VOLTAGE=3.3
XP3R3V_RX6: L69 4.7UH 0.18OHM; C497 22UF 10V X5R 0603; C499 1UF 25V X6S 0402; C501 0.1UF 25V X6S 0201; R1146 10OHM; R473 10OHM; VOLTAGE=3.3

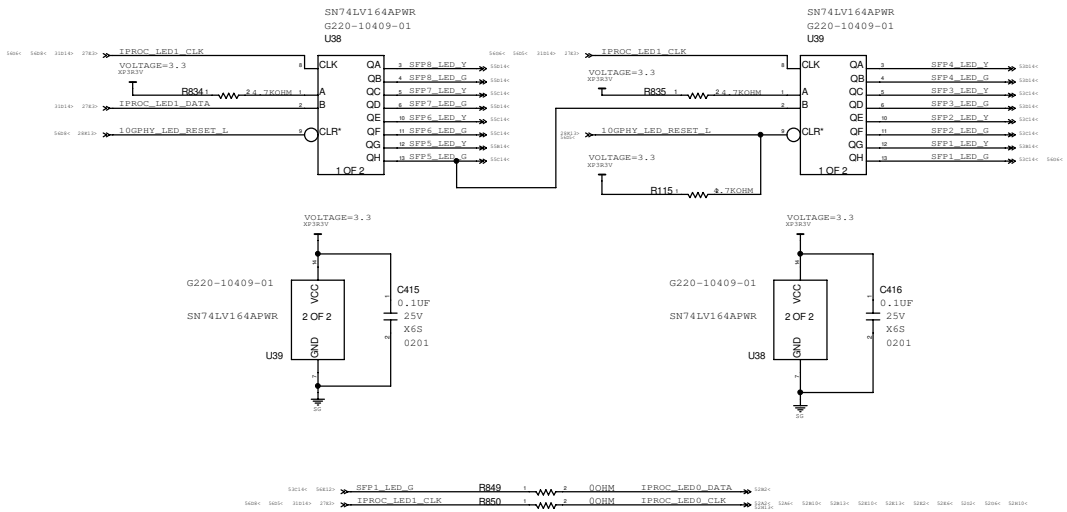
XP3R3V_TX7: L70 4.7UH 0.18OHM; C502 10UF 25V X6S 0805; C503 1UF 25V X6S 0402; C504 22UF 10V X5R 0603; C506 1UF 25V X6S 0402; C508 0.1UF 25V X6S 0201; R1147 10OHM; R474 10OHM; VOLTAGE=3.3
XP3R3V_RX7: L71 4.7UH 0.18OHM; C505 22UF 10V X5R 0603; C507 1UF 25V X6S 0402; C509 0.1UF 25V X6S 0201; R1148 10OHM; R475 10OHM; VOLTAGE=3.3

XP3R3V_TX8: L72 4.7UH 0.18OHM; C510 10UF 25V X6S 0805; C511 1UF 25V X6S 0402; C512 22UF 10V X5R 0603; C514 1UF 25V X6S 0402; C516 0.1UF 25V X6S 0201; R1149 10OHM; R492 10OHM; VOLTAGE=3.3
XP3R3V_RX8: L73 4.7UH 0.18OHM; C513 22UF 10V X5R 0603; C515 1UF 25V X6S 0402; C517 0.1UF 25V X6S 0201; R1150 10OHM; R493 10OHM; VOLTAGE=3.3

SFP+_53-56_PORT

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SP4060-08ATG
G229-10039-01
U44

MGMT_TRD_0_P
MGMT_TRD_0_N
MGMT_TRD_1_P
MGMT_TRD_1_N
IO1 IO2 IO3 IO4 IO8 IO7 IO6 IO5
1 OF 2
MGMT_TRD_2_P
MGMT_TRD_2_N
MGMT_TRD_3_P
MGMT_TRD_3_N

G229-10039-01
SP4060-08ATG
U44
VCC GND
2 OF 2

CM1224-04SO
G122-10172-01
U42

COME_USB1_DM
COME_USB1_DP
USB_PWR1

CM1224-04SO
G122-10172-01
U15

COME_USB2_DM
COME_USB2_DP
USB_PWR2

CM1224-04SO
G122-10172-01
U43

UART_TX
UART_RX

2035644017
G200-12981-01
J14

L105 600OHM 0603 1.3A
VOLTAGE=3.3
C476 C477 0.1UF 25V X6S 0201

MGMT_TRD_0_P
MGMT_TRD_0_N
MGMT_TRD_1_P
MGMT_TRD_1_N
MGMT_TRD_2_P
MGMT_TRD_2_N
MGMT_TRD_3_P
MGMT_TRD_3_N
COME_USB1_DP
COME_USB1_DM
COME_USB2_DP
COME_USB2_DM

R833 R858 R1094 R1095 R1096 R1097 R1098 R1099 33OHM
FAN_LED_GREEN
FAN_LED_AMBER
SYS_LED_GREEN
SYS_LED_AMBER
PSU_LED_GREEN
PSU_LED_AMBER
ALARM_LED_GREEN
ALARM_LED_AMBER

UART_TX
UART_RX
USB_PWR1
R1100 R1101 R1102 33OHM
MGMT_LINK
MGMT_LED_YELLOW
MGMT_LED_GREEN
USB_PWR2

COME_UART0_TX
COME_UART0_RX
BCM56277_UART0_SOUT
BCM56277_UART0_SIN
R436 R437 R438 R439 0OHM
NO_ASSY=TRUE
UART_TX
UART_RX

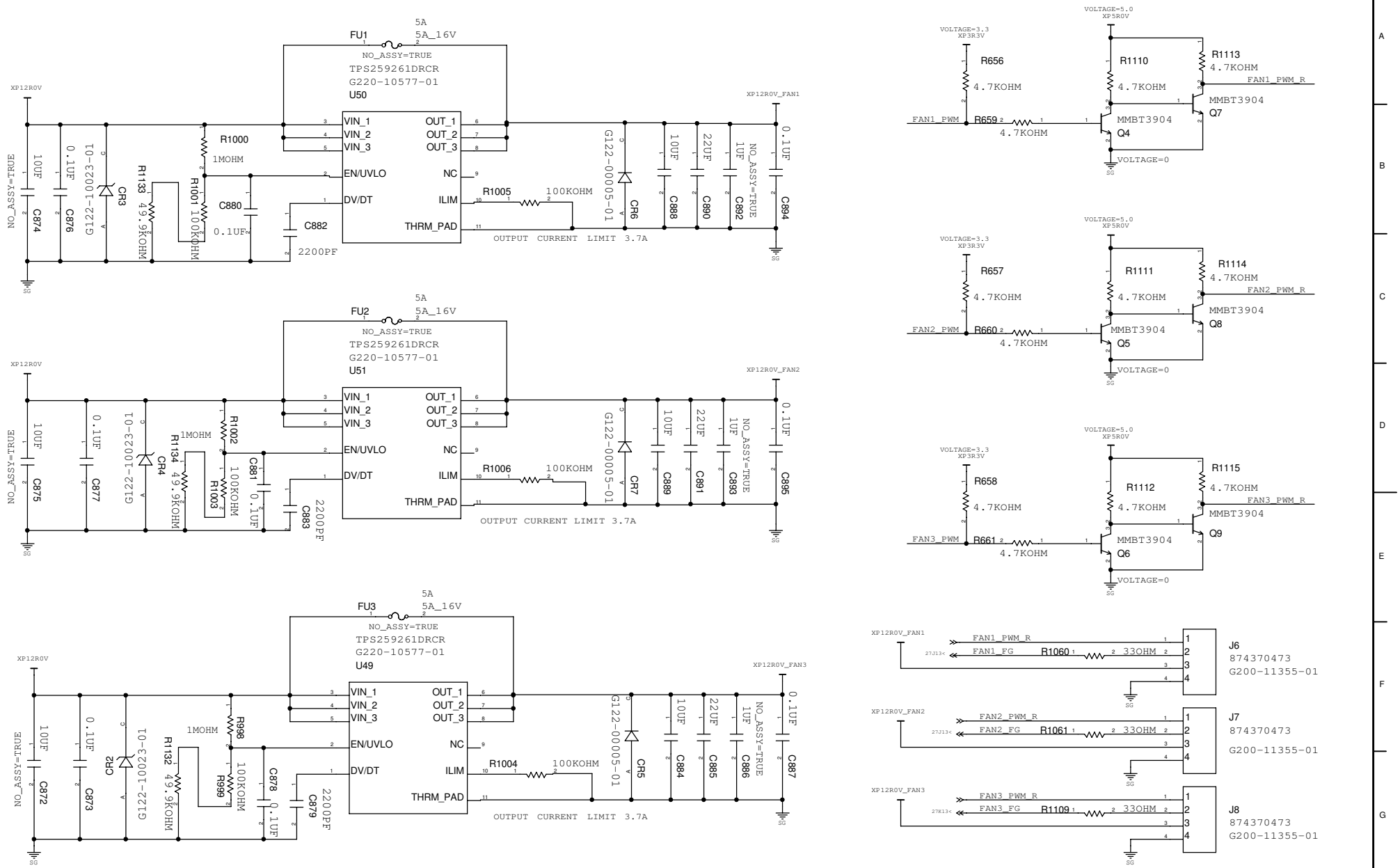
NCP382HD05A-AR2G
G222-10456-01
U14

VOLTAGE=3.3
R430 R431 R429 R432 4.7KOHM
USB_PWR1_EN
USB_PWR2_EN
NO_ASSY=TRUE
EN1 EN2 IN GND OUT1 OUT2 FLAG1* FLAG2*
VOLTAGE=5.0
C478 C479 C481 0.1UF 25V X6S 0201
R568 R569 10KOHM
USB_PWR1
USB_PWR2
USB1_FAULT_N
USB2_FAULT_N

IO_BOARD_CONNECTOR

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FAN CONNECTOR

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C210N3-15031-Y
G200-12056-01
J4

ADDR: XXXX X00
PMBUS:0X58
IPMI FRU:0X50

C210N3-15031-Y
G200-12056-01
J5

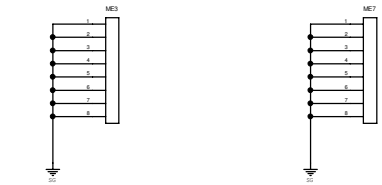
I2C ADDR :A1 A0

ADDR: XXXX X01
PMBUS:0X59
IPMI FRU:0X51

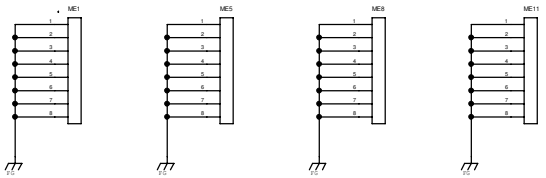
PSU CONNECTOR

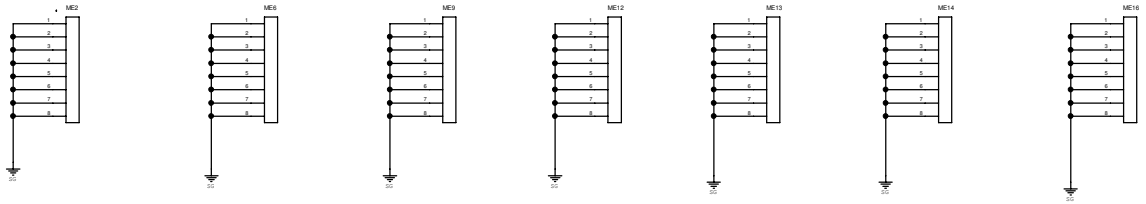
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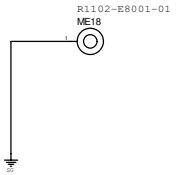


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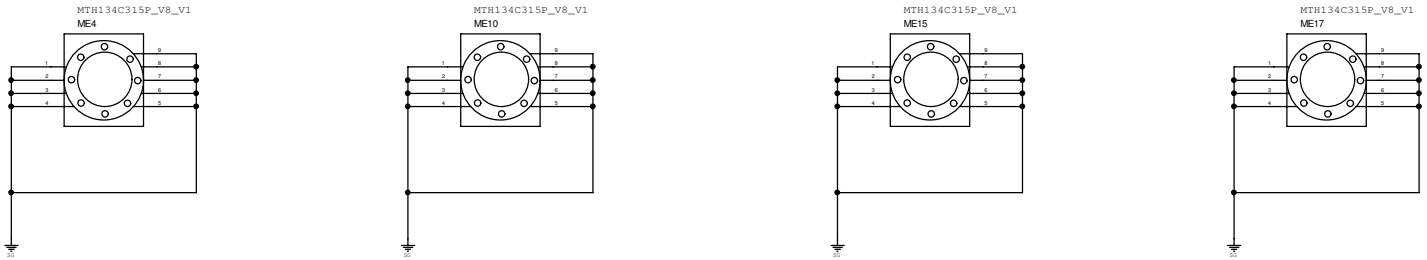




B



SSD NUT



C_FOR COME

MECH

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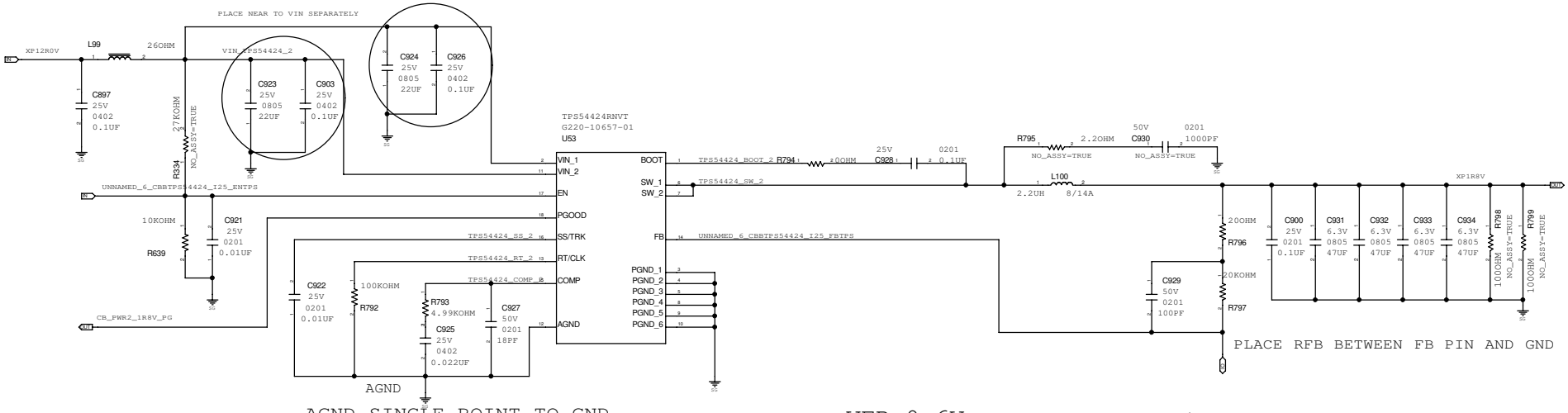
CHANGE LIST

PAGE/REV	CONTENT	PAGE/REV	CONTENT
PAGE 16/A2	NET I2C_SCL/SDA CHANGE TO PCA9548_SCL/SDA2	PAGE 20/A2	ADD TPM(U22) MODULE TO COME SPI INTERFACE(COME_SPI_XXX), ADD NET TPM_SPI_PIRQ_N TO U6.N16/6.21
PAGE 19/A2	ADD LINES FROM UCD90160 TO CPLD FOR WDT RESERVED	PAGE 64/A2	C9P3 MODIFED TO G104-0B101-FK
PAGE 20/A2	U21 PIN1 CONNECT TO GND BY 1KOHM	PAGE 10,18/A2	ADD POWER SWITCH MODULE FOR 12V OF COME/6.23
PAGE 21/A2	PIN51/56 POWER SUPPLY MODIFY,MPN CHANGED.	PAGE 11,37/A2	ADD POWER SWITCH MODULE FOR 3.3V OF BCM56277/6.23
PAGE 22/A2	U36 PIN(SCL/SDA) CONNECT TO COME_SMA_CK/DAT	PAGE XX/A2	L20/L40/L42/L90 CHANGED TO G191-10101-01 L2/L3/L5-11/L14/L17-19/L21-25/L28/L39/L41/L64-65/L88-89 CHANGE TO G191-10098-01 R992 CHANGED TO G155-04750-01/6.24
PAGE 23/A2	U3 I2C ADDRESS MODIFIED TO 0X53	PAGE 10,15/A2	R344/R346 CHANGED TO G155-11500-01,R345/R347 CHANGED TO G155-11000-01 R312 CHANGED TO G155-11000-01,ADD R1122/6.24 R988 CHANGED TO G155-11003-01,ADD R1127/6.25
PAGE 27-28/A2	CPLD PINS SWAP	PAGE 25/A2	ADD R1123 AS A OPTIONAL FOR U41/6.25
PAGE 30/A2	REDESIGN J16/6.25	PAGE 30/A2 PAGE 30/A2 PAGE 16/A2	REDESIGN Q3/6.25 R3 CHANGED TO G155-133R0-01,ADD C1048,C1063/6.30 R514 CHANGED TO G155-133R0-01/6.30
PAGE 31/A2	USB_VBUS_PRESENT CONNECTED TO GND	PAGE 18/A2	ADD R1171 FOR CPLD DEBUG/7.22
PAGE 32/A2	ADD CAPS TO PVTMON5_VDAC/ADC AS TEST POINT	PAGE 12/A2 PAGE 64/A2 PAGE 10/A2	R960 CHANGED TO G155-14991-Z1,ADD R1128/6.25 R64P3 CHANGED TO G155-13301-01,ADD R1129/6.25 R951 CHANGED TO G155-13301-01,ADD R1130/6.28
PAGE40-42/A2	SUPER_I STRAP TO "1"	PAGE 10/A2 PAGE 58/A2 PAGE 53/A2	R2 CHANGED TO G155-11003-01,ADD R1131/6.28 R999,R1001,R1003 CHANGED TO G155-11003-01,ADD R1132,R1133,R1134/6.28 R176-177,R206-209,R226-227 CHANGED TO G155-11R00-01,ADD R1135-1142/6.28
PAGE43-48/A2	TRANSFORMER MDI PAIRS SEQUENCE MODIFY	PAGE 55/A2 PAGE 49-51/A2 PAGE 11,12,37/A2	R442-443,R472-475,R492-493 CHANGED TO G155-11R00-01,ADD R1143-1150/6.28 R1,R114,R581-582,R1116-1117 CHANGED TO G157-100R0-01/6.28 R322,R944,R957 CHANGED TO G155-12002-01,ADD R1151-1153/6.28
PAGE 52/A2	ADD CAPS FOR LED DRIVER LINES	PAGE 32/A2 PAGE 30/A2 PAGE 28/A2	R284-287,R1093 CHANGED TO G155-133R0-01/6.29 R824 CHANGED TO G155-149R9-01,R825-826 CHANGED TO G155-11000-01/6.29 DELETE R812/6.29
PAGE 59/A2	PSU PWOK CONNECT TO GND	PAGE 12/A2 PAGE 34/A2 PAGE 20/A2	ADD C1065 FOR XP1R8V OUTPUT/7.8 ADD 2*10G KR:R1155-1166,C1066-1069/7.14 ADD COME_SPI_CLK BUFFER:U57,R1167-1170/7.15
PAGE 25/A2	R886 CHANGE TO 22OHM/5.18	PAGE 16/A2 PAGE 22/A2 PAGE 53/A2	DEPOP:R543,R544/7.15 MODIFY:R964-965 CHANGE TO G155-100R0-J0/7.15 MODIFY:R182-183,R189-190,R196-197,R203-204 CHANGE TO G155-11002-Z1/7.15
PAGE 26/A2	ADD R133 TO Y1/5.24	PAGE 55/A2 PAGE 31/A2 PAGE 17/A2	MODIFY:R448-449,R455-456,R462-463,R469-470 CHANGE TO G155-11002-Z1/7.15 MODIFY:ADD AN LINK LED1_CLK/DATA TO CPLD;R812,R1154 AS OPTIONAL RES/7.19 ADD:X1 FOR RTC BATTERY SELECT/7.22
PAGE 23/A2	U60 I2C ADDRESS MODIFIED TO 0X49/5.25	PAGE 19/A2 PAGE 21/A2 PAGE 26/A2	MODIFY:R1675-1676 CONNECT TO PCA9548_SCL2/DAT2/7.22 ADD:R1172,1173 FOR CRYSTAL TEST/7.22 ADD:R1174,1175 FOR CRYSTAL TEST/7.22
PAGE 31/A2	DELETE THE IPROC_LED0_CLK/DATA OF SW/5.28	PAGE 35/A2 PAGE 40-42/A2 PAGE 16/A2	ADD:R1182-1187 FOR BCM56277 RDAC/7.26 ADD:R1176-1181 FOR B50282 RDAC/7.26 ADD:R1188 FOR COME_SPI_CS_N/7.27
PAGE XX/A2	MODIFY:C944/R1009/C946-C949/L102/5.28	PAGE 20/A2 PAGE 37/A2 PAGE 48/A2	ADD:R1189-1120 FOR BIOS0/BIOS1_CS_N/7.27 ADD:C1071-1076 FOR BCM56277 GPHY_AVDD3P3_0/1/7.27 MODIFY:SWAP C_P41_CH3_P/N/7.27
PAGE XX/A2	L1P3,L95,L97,L99,L101 CHANGE TO G191-10101-01/5.31 L63,L75,L91,L92 CHANGE TO G191-10099-01/5.31 L1,L4 CHANGE TO G191-10100-01/5.31	PAGE 18/A2 PAGE 30/A2 PAGE 25/A2	ADD:R1191 FOR COME_CPLD_TDO/7.28 ADD:R1192-1194 FOR CPLD_TCK/TMS/TDI/7.28 MODIFY:R348-349,R354-355 CHANGE TO G155-100R0-J0/8.11
PAGE XX/A2	L13,L15,L16,L26,L29,L30,L43,L44,L45,L46,L47,L48,L49,L50,L51,L52,L53,L54,L55 L56,L57,L58,L59,L60,L61,L62,L76,L77,L78,L79,L80,L81,L82,L83,L84,L85,L86,L87 L105 CHANGE TO G191-10097-01/5.31	PAGE 25/A2 PAGE 25/A2 PAGE 21/A2	MODIFY:R352-353 CHANGE TO G155-120R0-01/8.11 MODIFY:R344,R346 CHANGE TO G155-11000-01/8.11 POP:R845/8.11
PAGE 34/A2	DELETE C132,C142,C147,C152,C161,C162,C169,C176,C179,C186,C203,C204/6.1		
PAGE 34/A2	ADD TSC2_0/2_T/RD_P/N LINK TO COME/6.1 DEL TSC2_0/2_T/RD_P/N LINK TO COME/6.24 ADD TSC3_1/3_T/RD_P/N LINK TO COME/6.24		
PAGE 10/A2	INSERT PAGE10,COVERT XP5R0V BOLCK FROM BLOCK TO NORMAL MODE/6.1		

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A3 (297X420)

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PLACE RFB BETWEEN FB PIN AND GND

AGND SINGLE POINT TO GND

TSS=10NF/8.3=1.2MS

VFB=0.6V	RFB=12.012/(VO-0.6)
VO=5.048V	RFB=2.7K
VO=3.317V	RFB=4.42K
VO=2.537V	RFB=6.2K
VO=1.801V	RFB=10K
VO=1.201V	RFB=20K

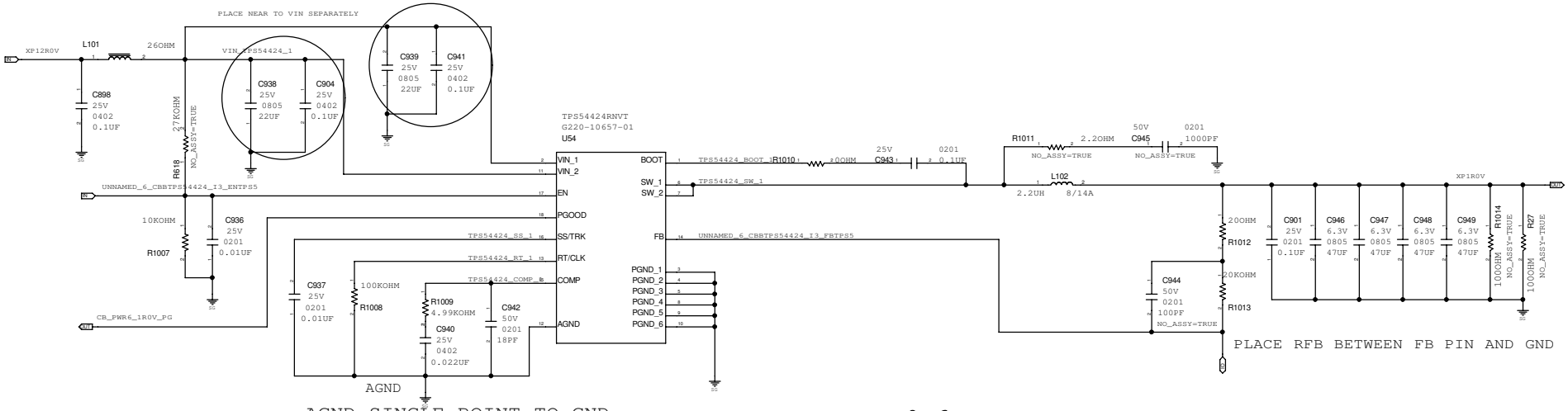
CBB_TPS54424

POWER RAIL	CBB_TPS54424
VIN	12V
VOUT	0.8~5V
SS	1.2MS
FSW	500KHZ
IMAX	4A
OCP	10A
OVP	108%*VOUT
RIPPLE	+/- 1%

CBB_TPS54424

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TSS=10NF/8.3=1.2MS

VFB=0.6V	RFB=12.012/(VO-0.6)
VO=5.048V	RFB=2.7K
VO=3.317V	RFB=4.42K
VO=2.537V	RFB=6.2K
VO=1.801V	RFB=10K
VO=1.201V	RFB=20K

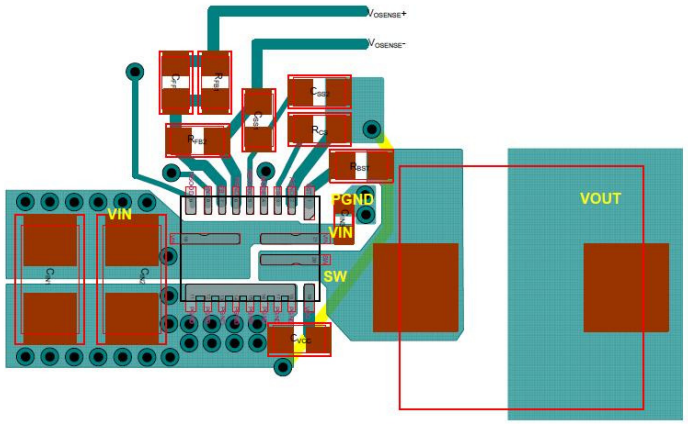
CBB_TPS54424

POWER RAIL	CBB_TPS54424
VIN	12V
VOUT	0.8~5V
SS	1.2MS
FSW	500KHZ
IMAX	4A
OCP	10A
OVP	108%*VOUT
RIPPLE	+/- 1%

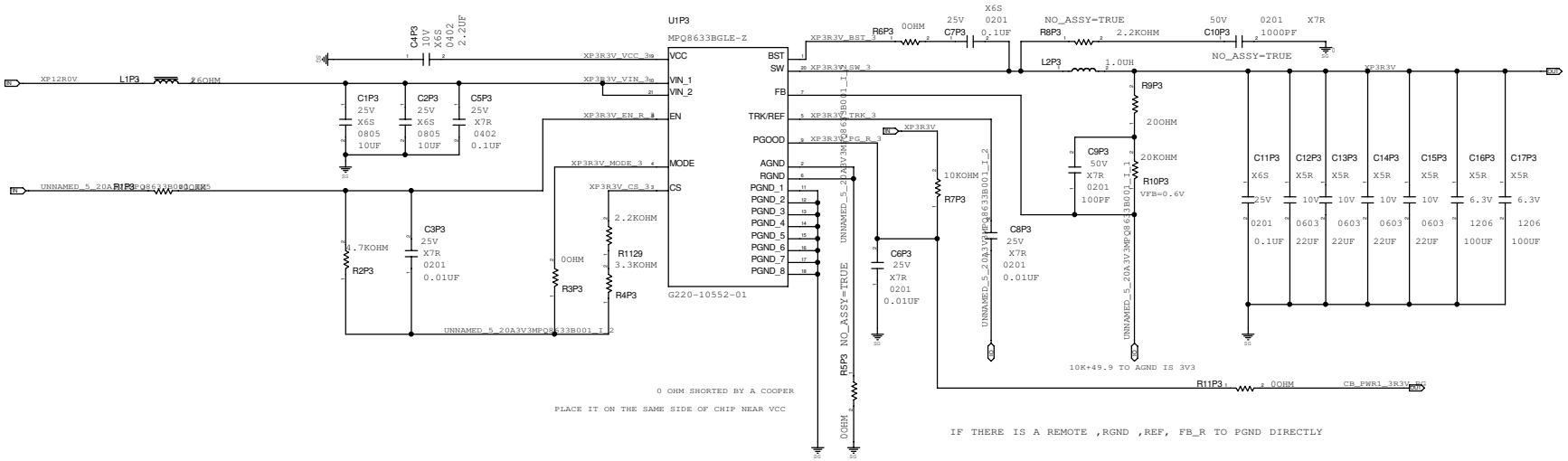
CBB_TPS54424

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XP3R3V			
ID	P014	MPN	MPQ8633B
INPUT (V)	12	OCP (A)	22
INPUT RIPPLE (A)	7.18	TARGET EFFICIENCY (%)	90
OUTPUT (V)	3.3	TUNABLE (Y/N)	N
OUTPUT (A)	16	PMBUS (Y/N)	N
OUTPUT RIPPLE (MV)	<100	ADDRESS	N
VOLTAGE TOLERANCE	(%) +/-3%	PRE-BIAS (Y/N)	N
SS (MS)	2.34	FSW (KHZ)	600
CAP			
INPUT CAP DES	QTY	RIPPLE CURRENT / CAP	
10UF,25V, X6S,0805	2		
0.1UF,25V, X7R,0402	1		
OUTPUT CAP DES	QTY	IMPEDENCE	
22UF,10V, X6S,0805	3		
0.1UF,10V, X7R,0402	1		
100UF,6.3V, X7S,1210	1		



12V-3.3V@16A MPQ8633

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